

Phase Noise Measurement Techniques Using Delta-Sigma TDC

Yusuke Osawa Daiki Hirabayashi Naohiro Harigai
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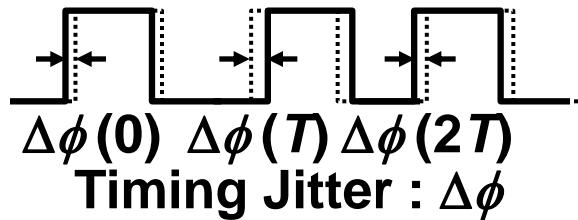


Gunma University Nagoya University STARC

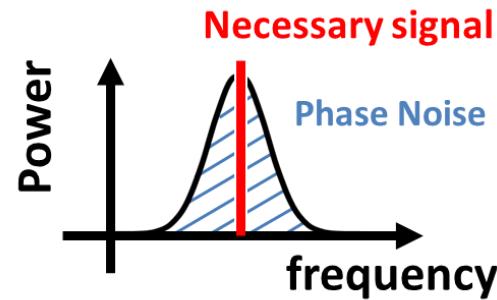
- Research Background & Objective
- Delta-Sigma TDC
- Phase Noise Measurement using $\Delta\Sigma$ TDC
 - with** Reference Clock
 - Phase Noise Measurement using $\Delta\Sigma$ TDC
 - without** Reference Clock
 - Self-Referenced Clock Technique
 - Conclusion

- Research Background & Objective
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Phase noise of clock can cause malfunctions of electronic systems



Oscillator phase noise



Electronic system performance degradation

- RF circuit & system
- ADC

Test & measurement for phase noise, jitter is important

Conventional Method I

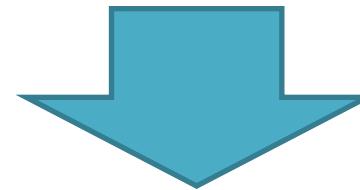
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Conventional Phase Noise Measurement



- **Expensive : Spectrum Analyzer**
- **Long testing time: ~10 seconds**

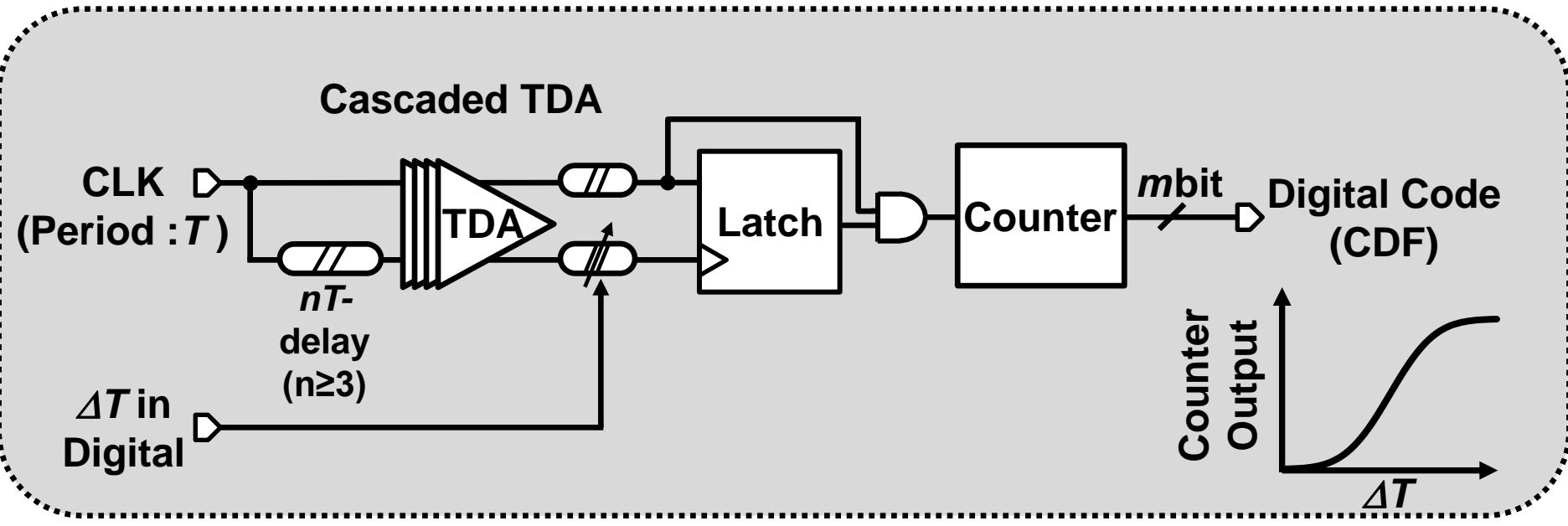
Mass production



Test cost → high



On-chip Jitter Measurement Circuit



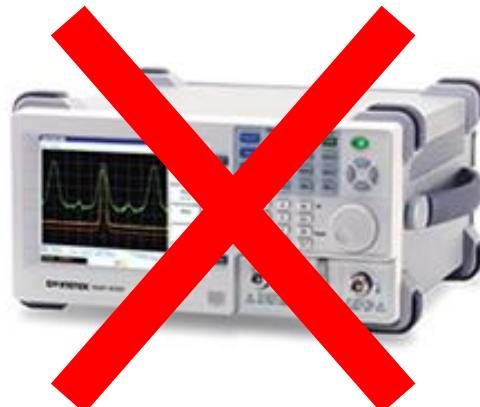
Can NOT measure jitter power spectrum



- [1] K. Niitsu, et al., "CMOS Circuits to Measure Timing Jitter Using a Self-Referenced Clock and a Cascaded Time Difference Amplifier with Duty-Cycle Compensation," IEEE Journal of Solid-State Circuits, Nov. 2012.

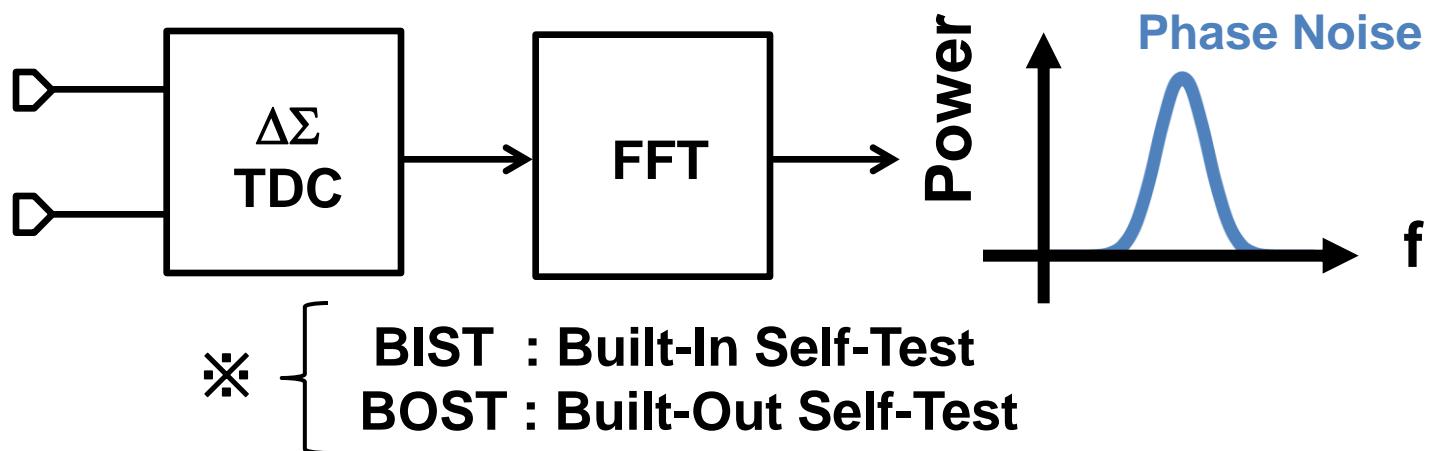
Low cost, high quality phase noise measurement

- w/o Spectrum Analyzer



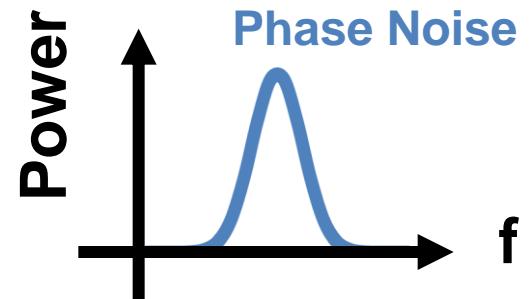
- w/ BIST or BOST Simple circuit

Clock
Under Test
CLKref



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Phase noise : Frequency characteristics



Time domain

Freq. domain

CUT
with
phase noise

Phase noise
measurement

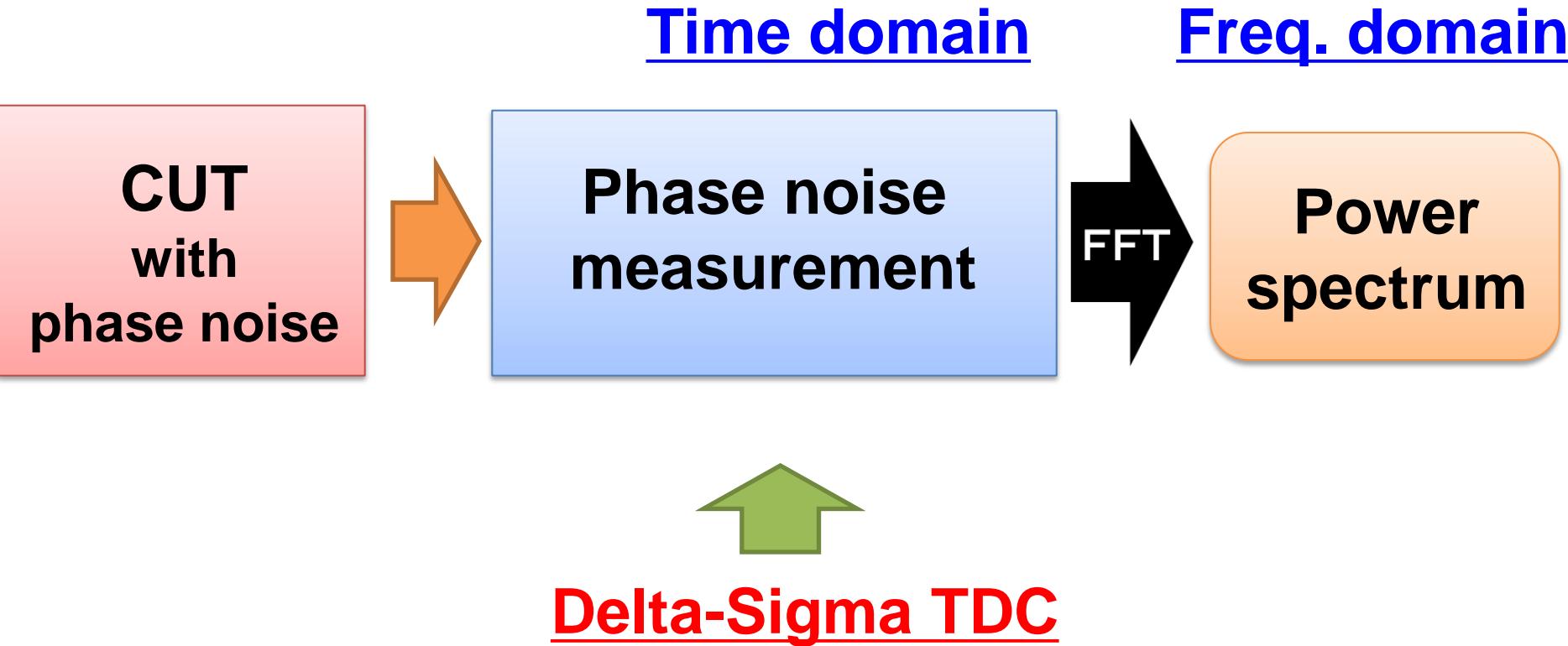
FFT

Power
spectrum

CUT : Clock Under Test

Proposed Method

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Time resolution improved
by longer measurement time

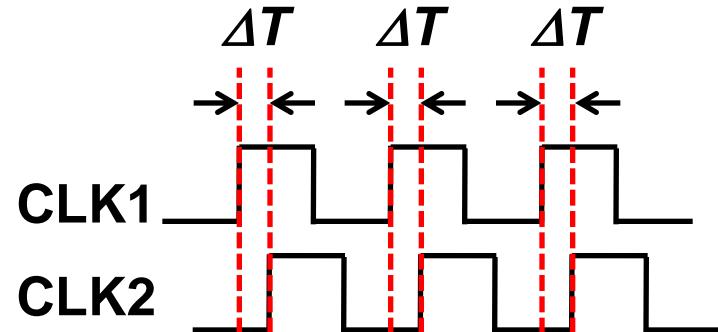
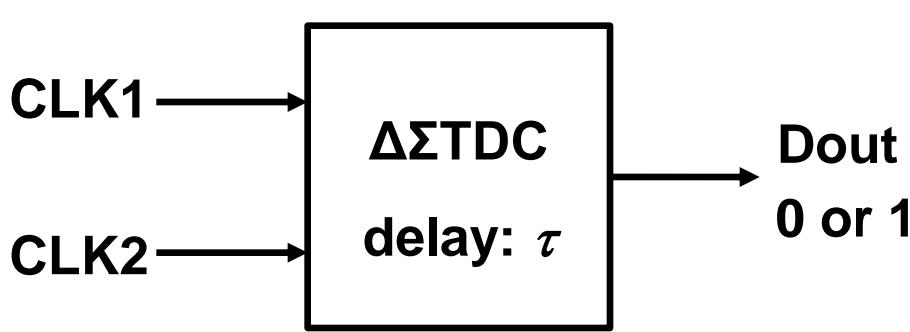
TDC : Time-to-Digital Converter

Ex: $\tau = 1\text{ns}$, $N_{\text{DATA}} = 64\text{K}$
→ $T_{\text{resolution}} = 0.03\text{ps}$

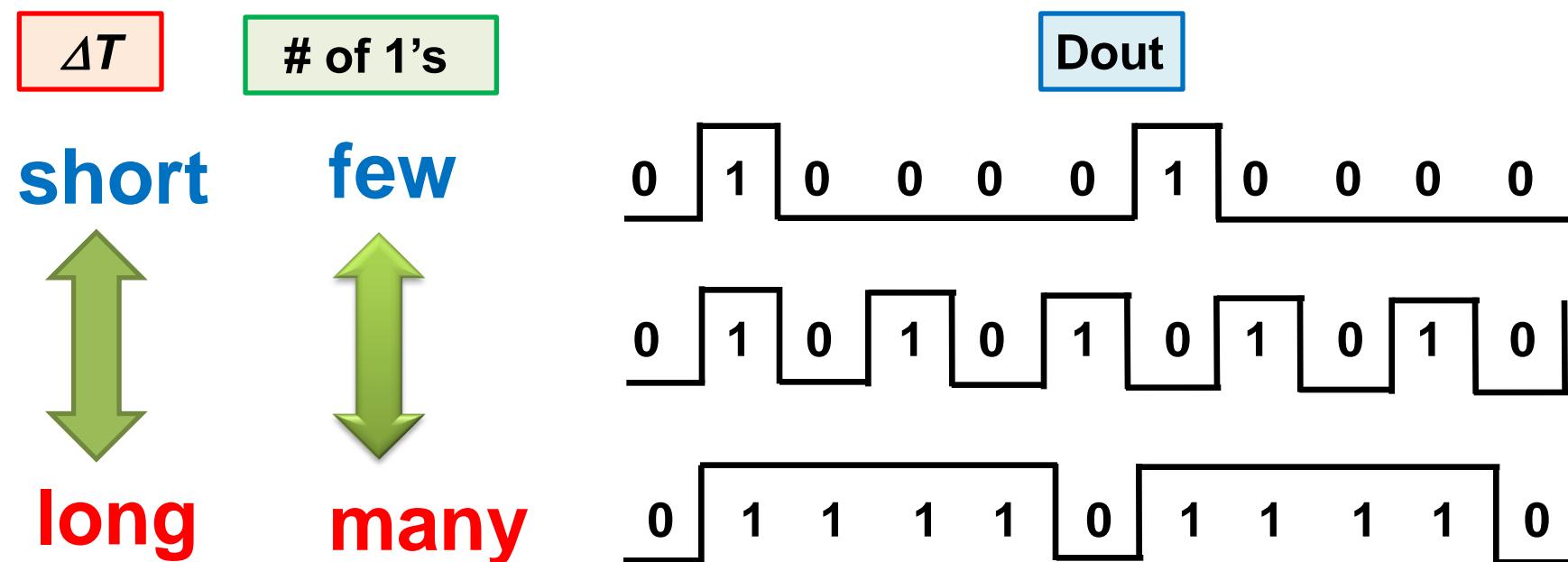
$$T_{\text{resolution}} \propto \frac{2\tau}{\text{time}}$$

Principle of $\Delta\Sigma$ TDC

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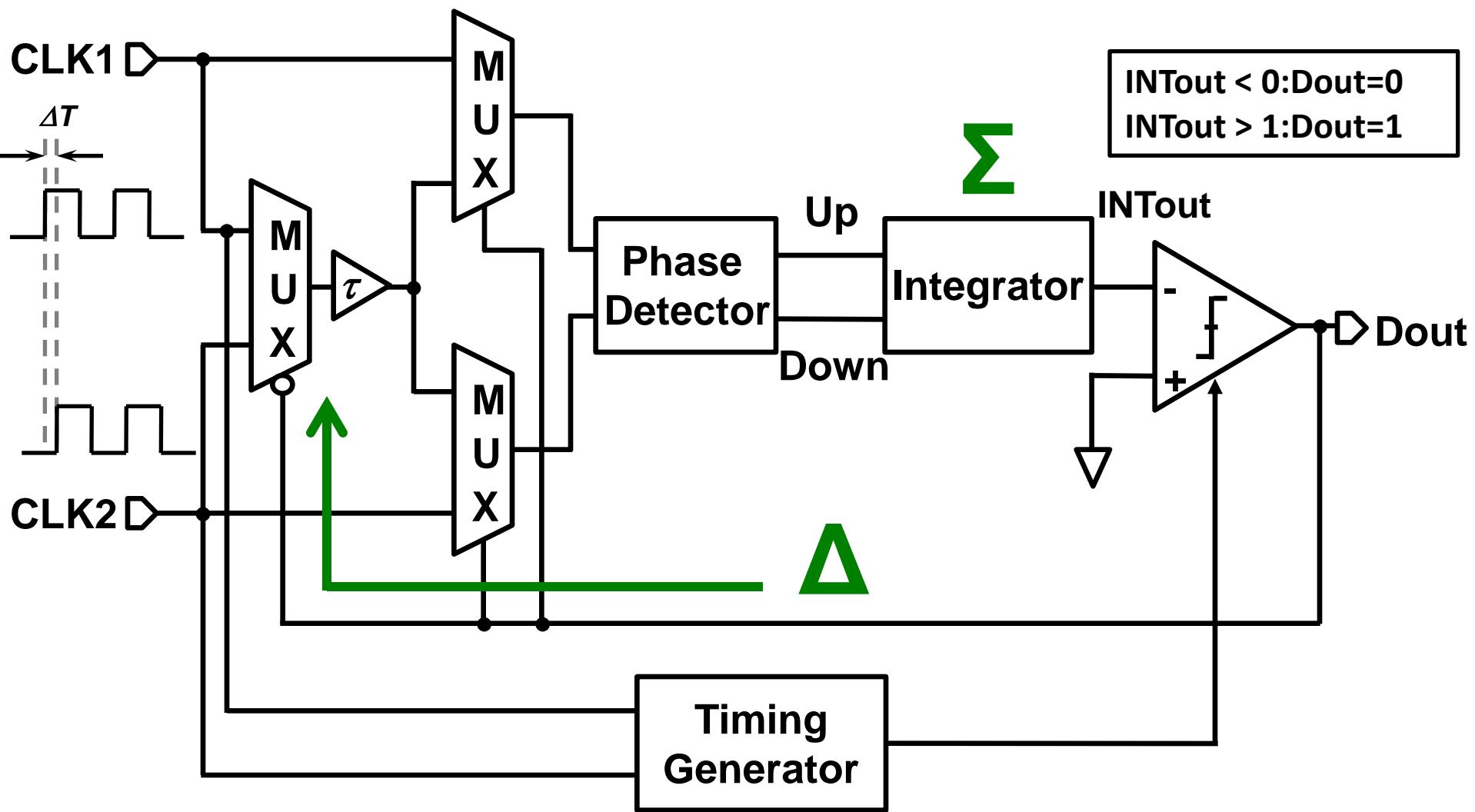


Dout # of 1's is proportional to ΔT



$\Delta\Sigma$ TDC Configuration

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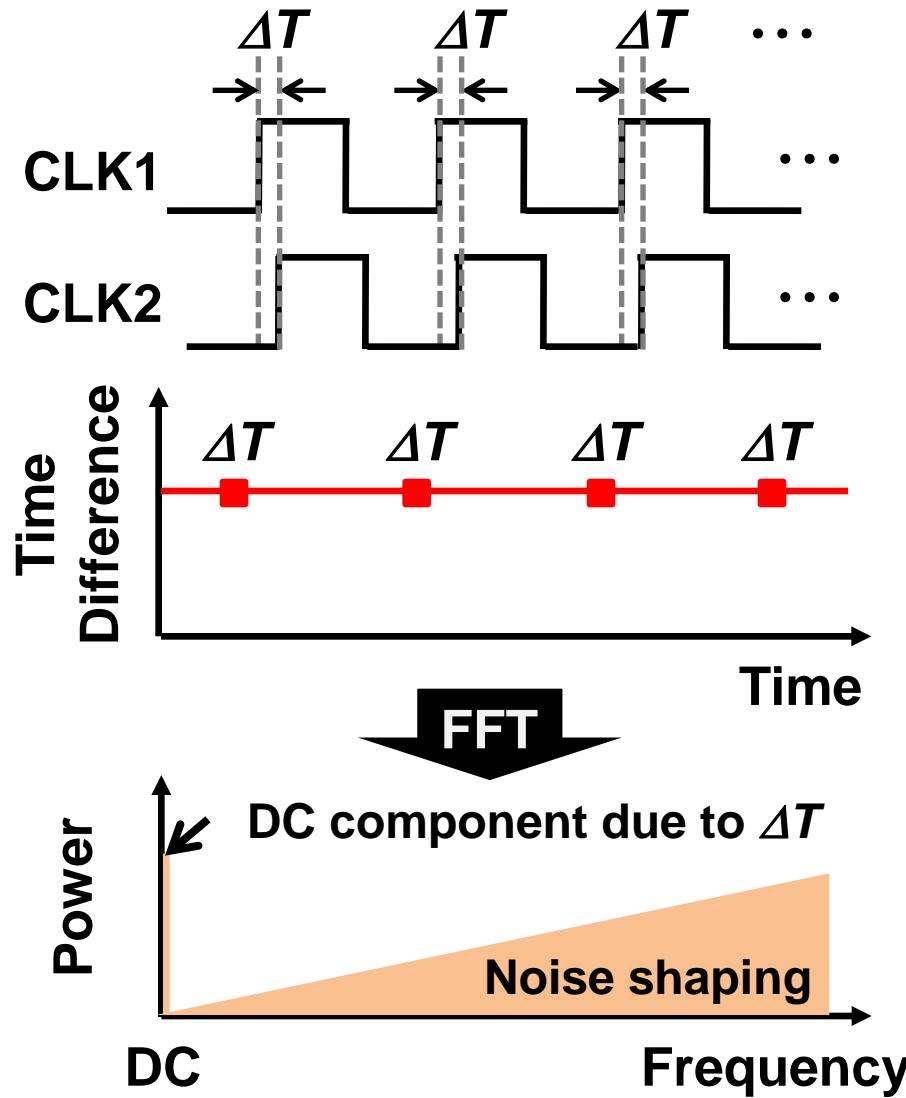


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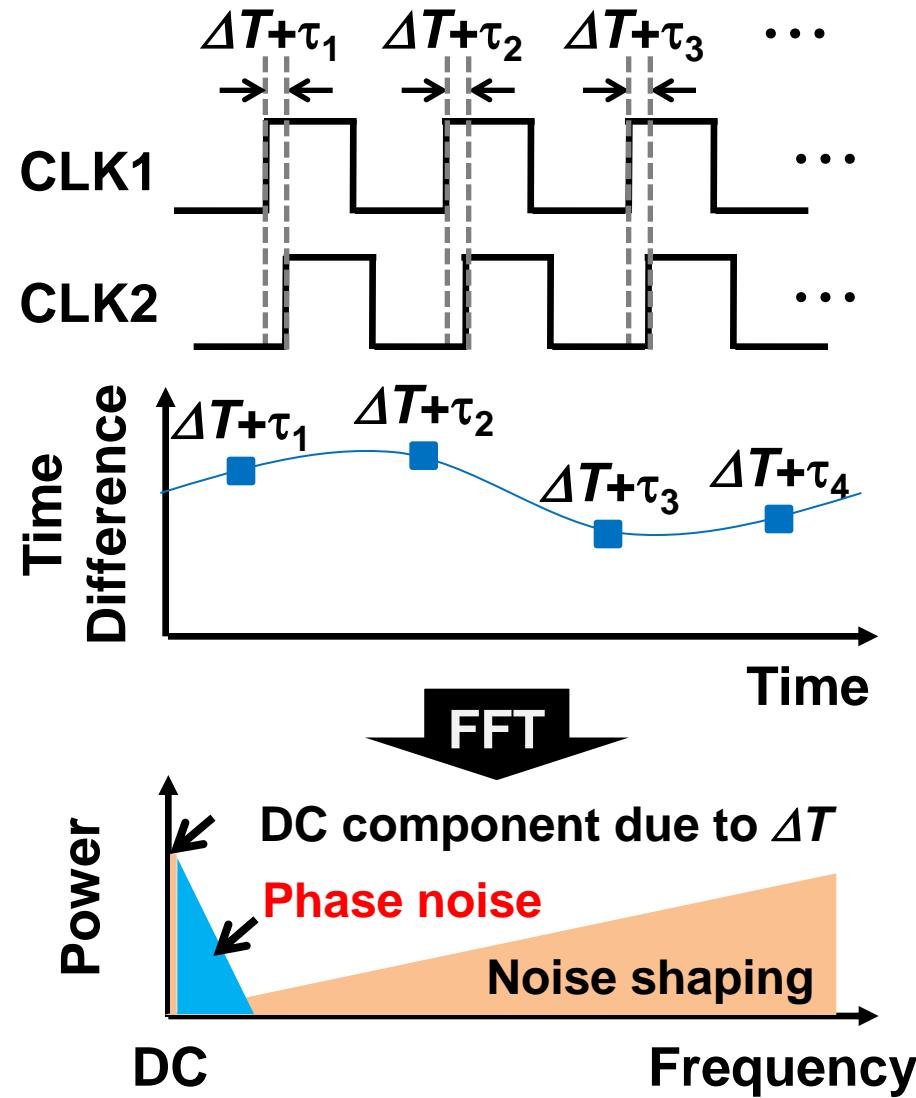
Principle of Phase Noise Measurement

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CLK1 without phase noise

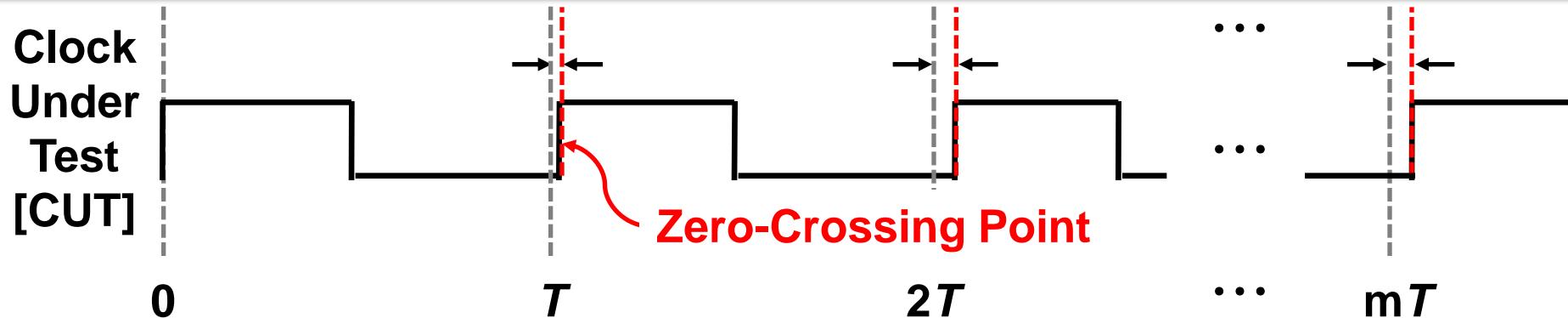


CLK1 with phase noise



Mathematical Analysis

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$$CUT(t) \approx \sin(2\pi f_{in}t + \phi(t))$$

$\tau(m)$: m-th zero-crossing point variation function (noise component)

$\therefore \phi(mT) = -2\pi f_{in} \tau(m)$: phase noise (time domain)

In case of sinusoidal phase fluctuation

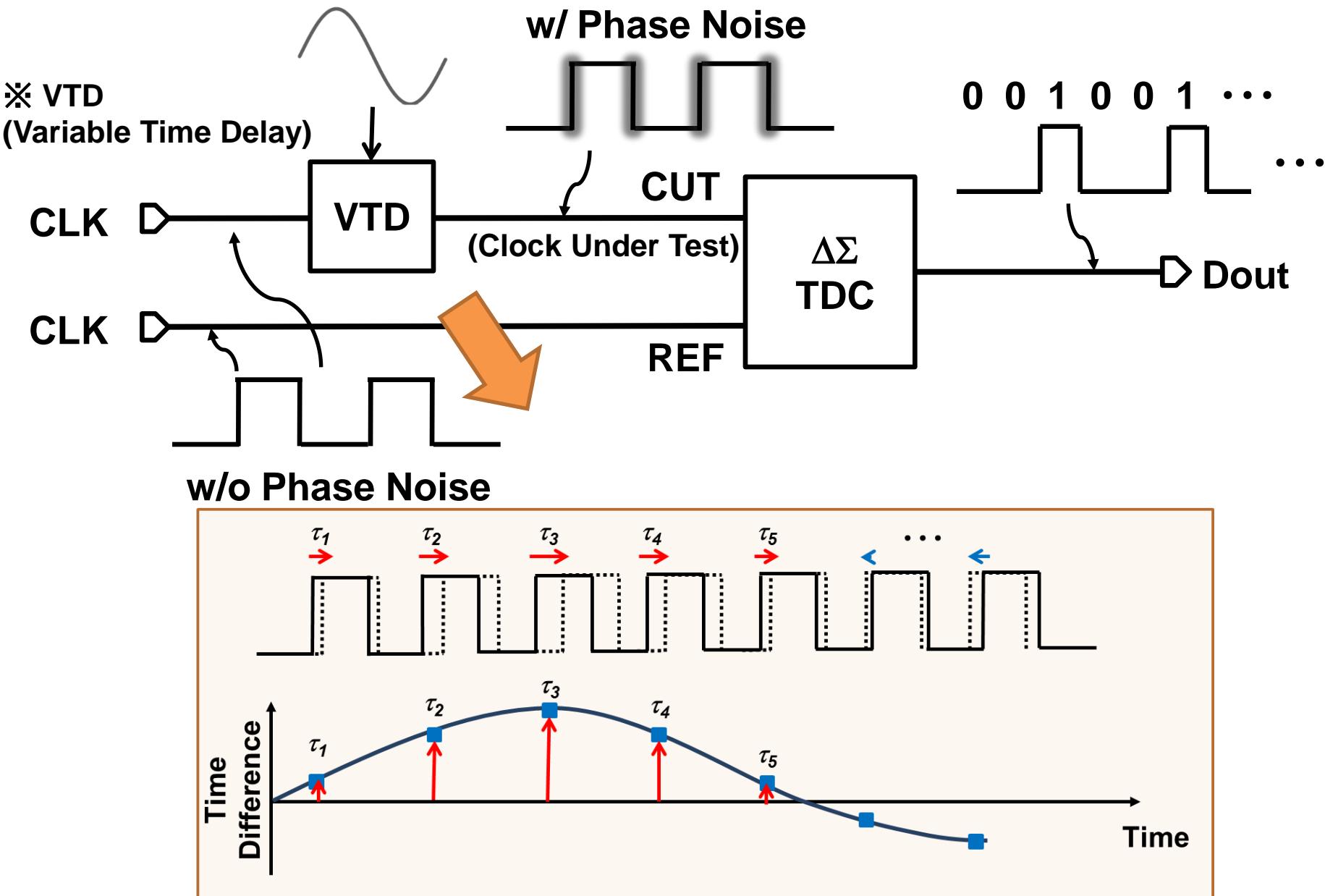
$$\tau(m) = T \cdot \alpha_j \cdot \sin(\omega_j mT) \quad 0 \leq \alpha_j \leq 1$$

$\phi(mT) = -2\pi \alpha_1 \cdot \sin(\omega_1 mT)$: phase noise (time domain)

$$\Phi(\omega_1) = \frac{1}{2} (2\pi \alpha_1)^2 \quad : \text{phase noise (freq. domain)}$$

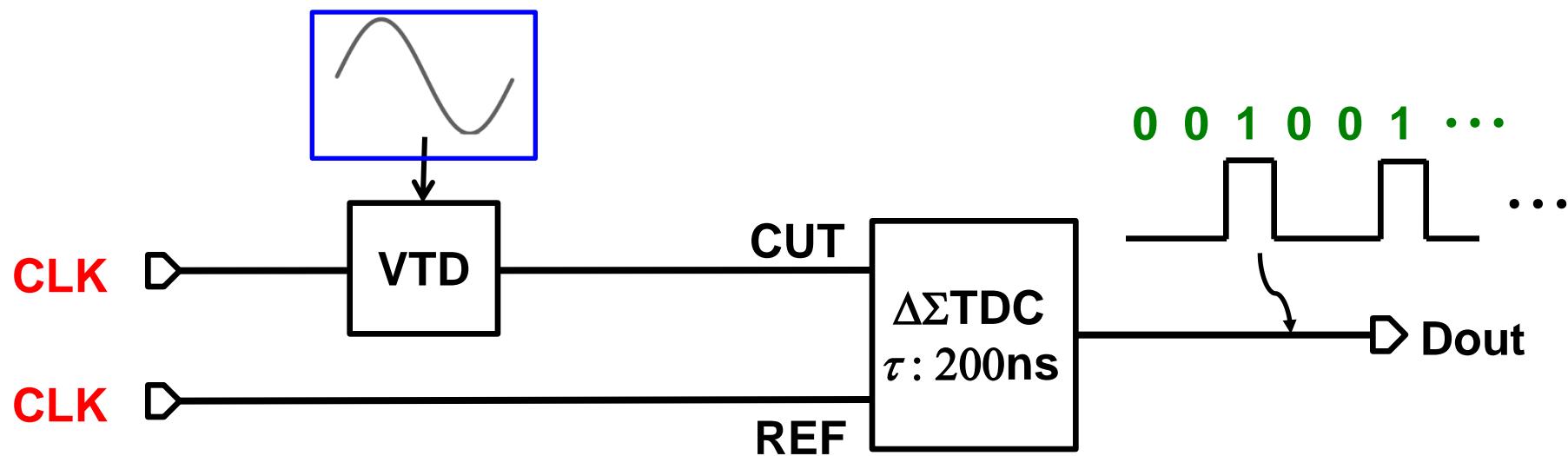
MATLAB Simulation

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Simulation Conditions

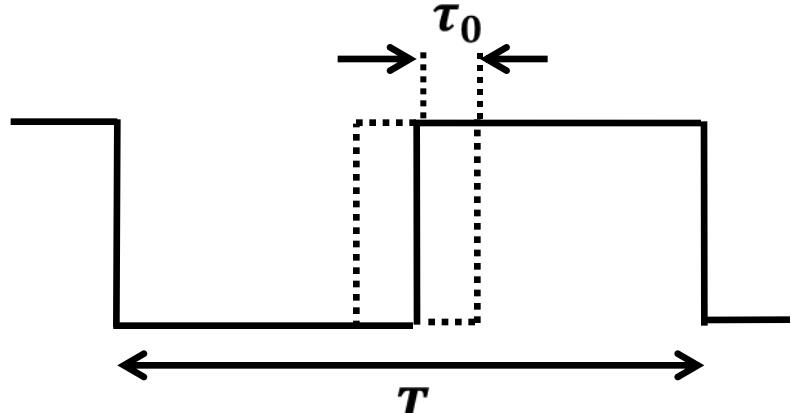
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- **CLK:**
Input freq. = 1 MHz ($T = 1 \mu\text{s}$)

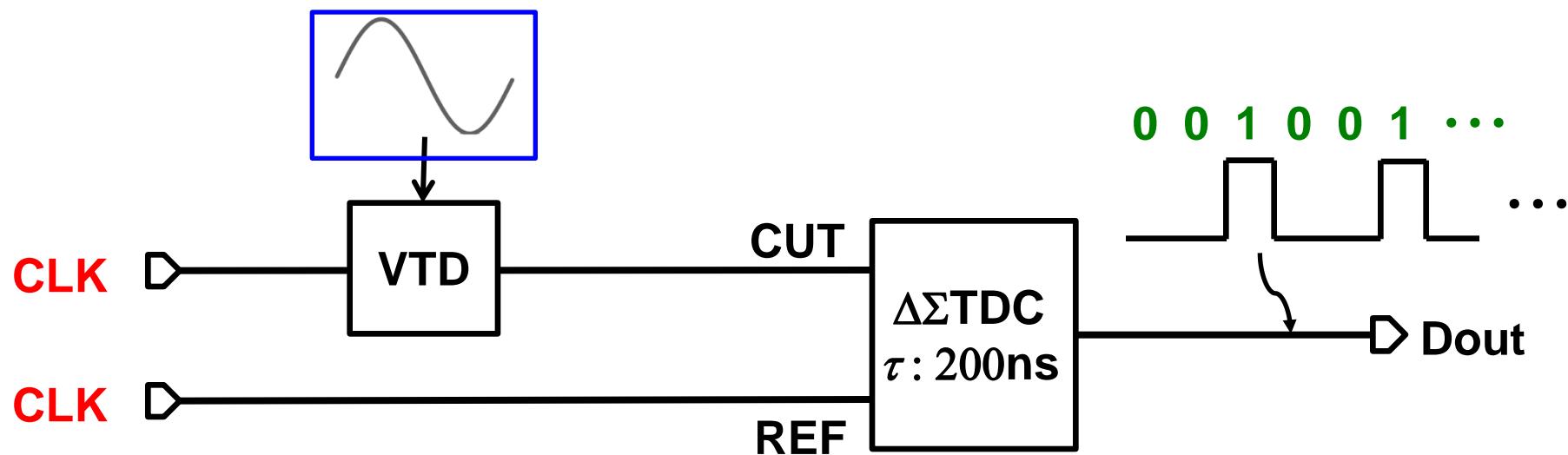
Phase variation (sinusoidal)

- Phase noise frequency :
 f_j ➡ varied
- Jitter variation :
 $-0.1\mu\text{s} \leq \tau_0 \leq 0.1\mu\text{s} (= \frac{T}{10})$
- Number of data:
4096



Simulation Conditions

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- **CLK:**

Input freq. = 1 MHz ($T = 1 \mu\text{s}$)

Phase variation (sinusoidal)

- Phase noise frequency :

$$f_i = \text{varied}$$

- Jitter variation :

$$-0.1\mu\text{s} \leq \tau_0 \leq 0.1\mu\text{s} (= \frac{T}{10})$$

- Number of data:

4096

- Single sine wave

① $f_1 = 10 \text{ kHz}$

② $f_1 = 50 \text{ kHz}$

- Multiple sine waves

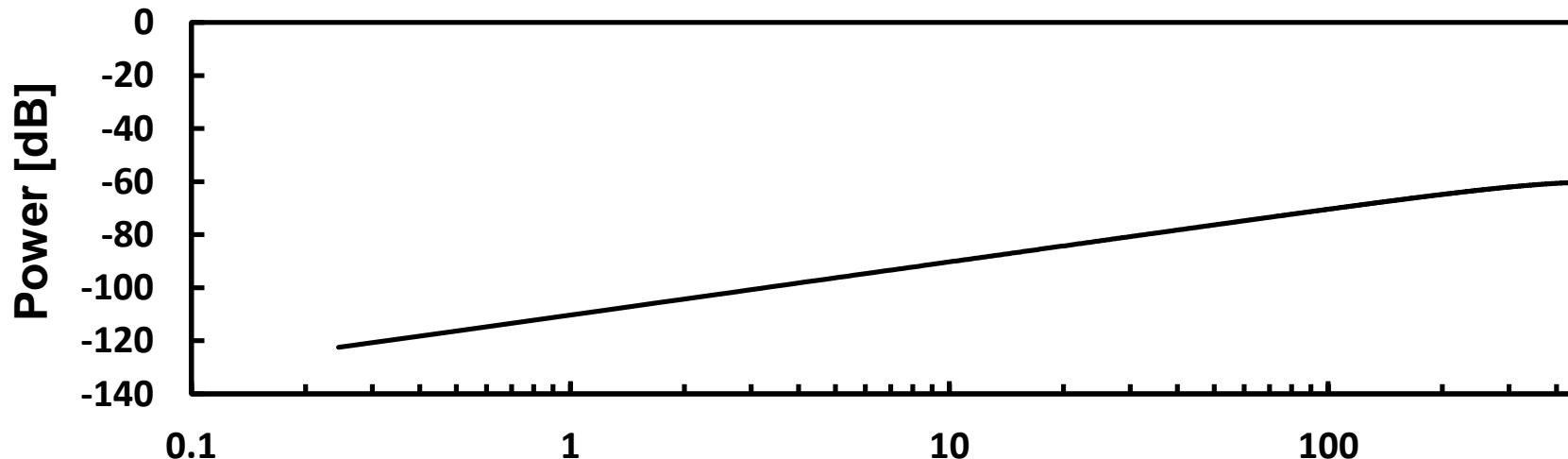
③ $f_1 = 10 \text{ kHz}$

$f_2 = 50 \text{ kHz}$

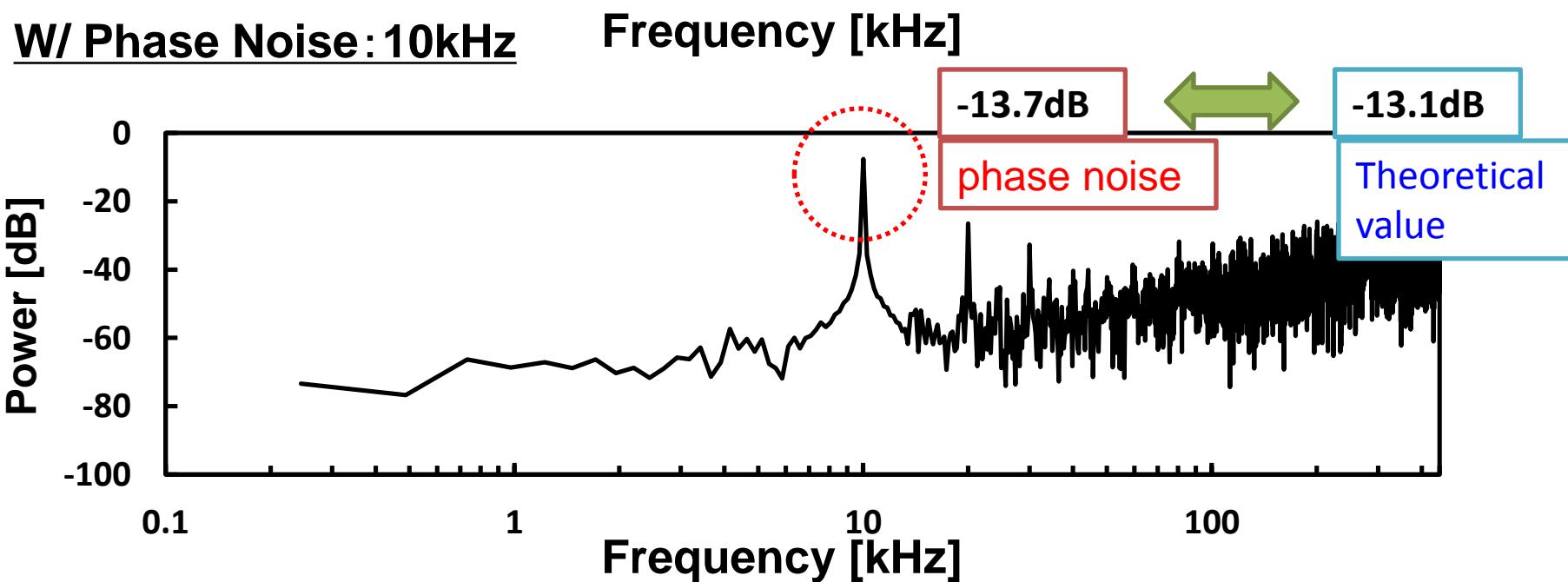
Simulation Results ①

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W/O Phase Noise



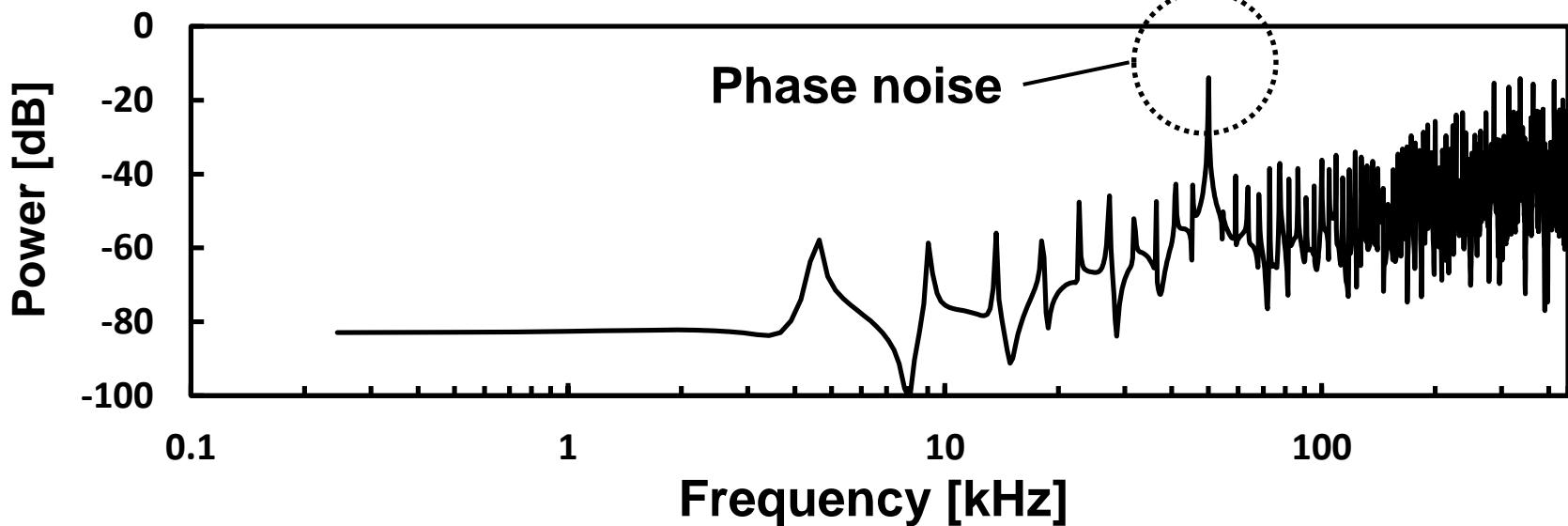
W/ Phase Noise : 10kHz



Simulation Results ②

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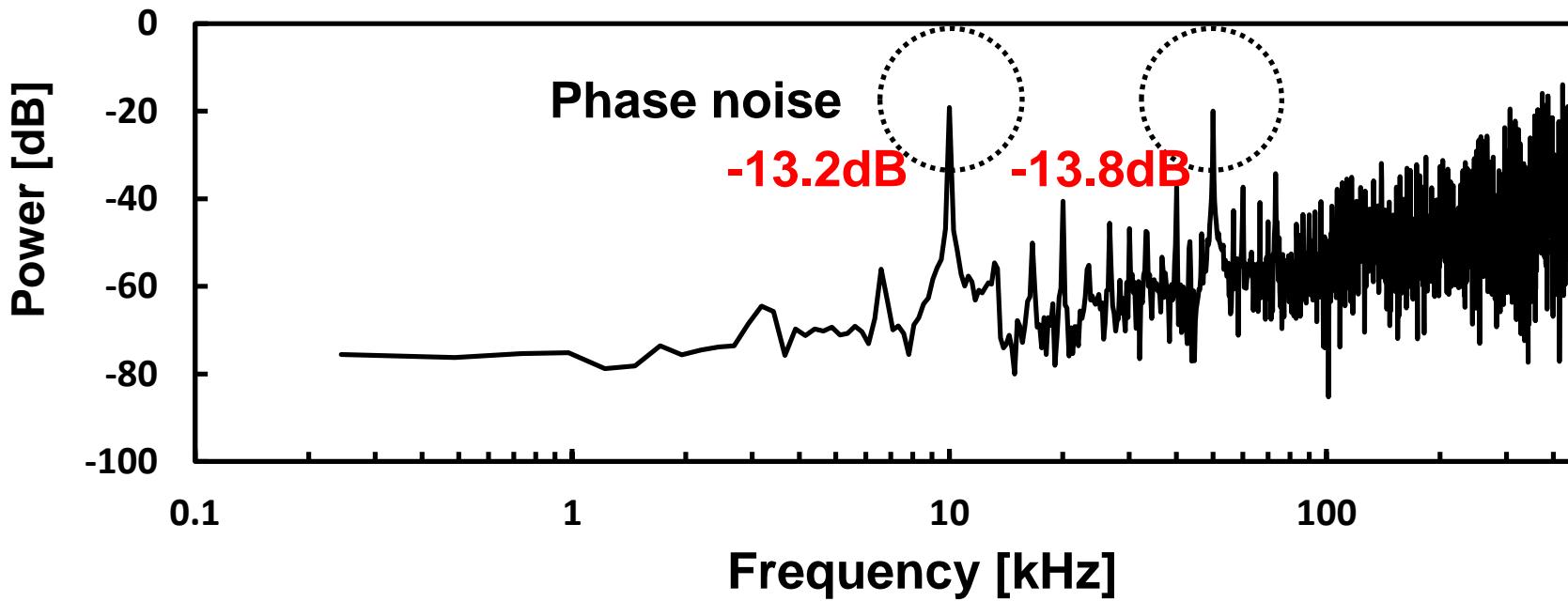
W/ Phase noise : 50kHz



Simulation Results ③

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Phase noise: 10kHz & 50kHz



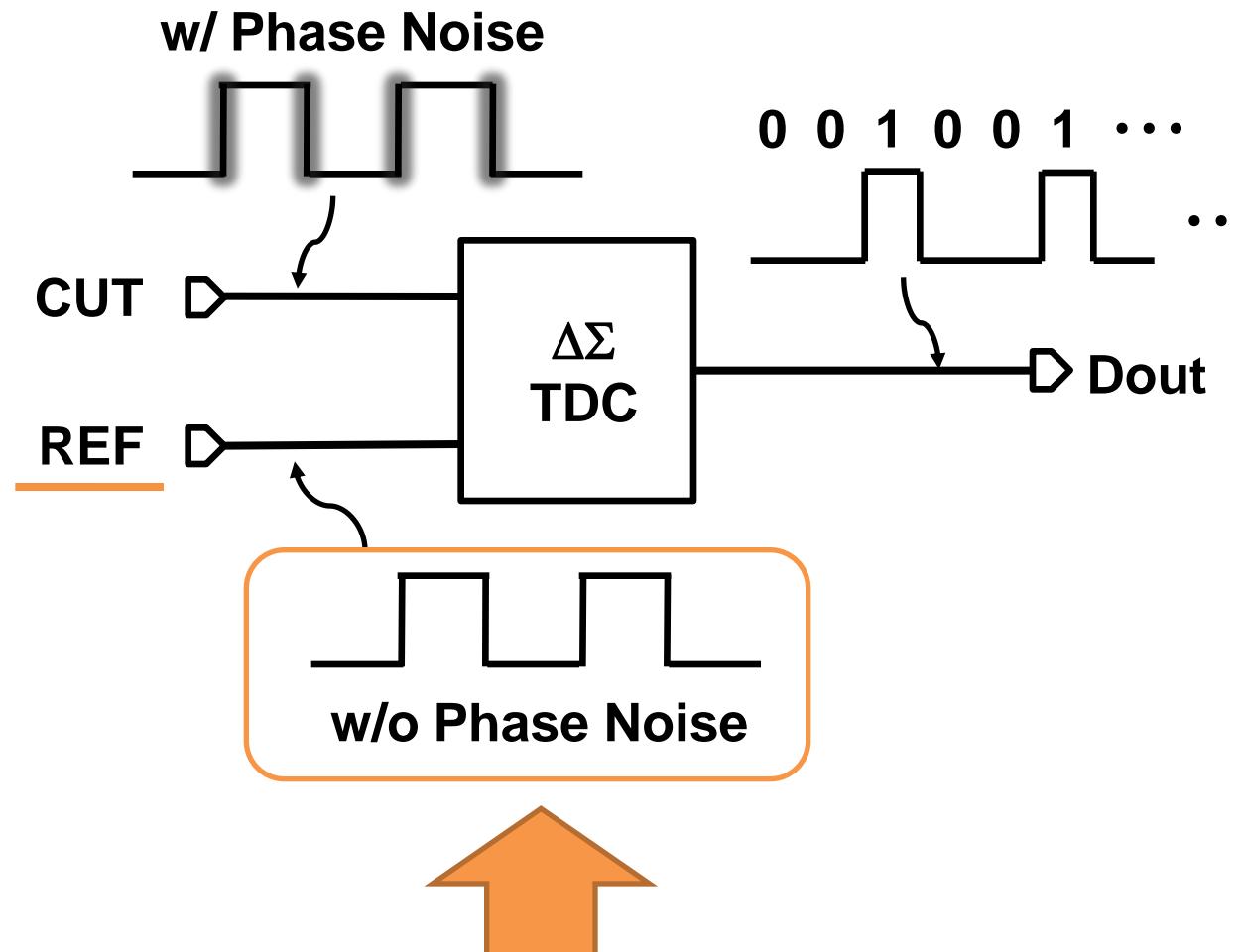
Theoretical value

Power = -13.1[dB]

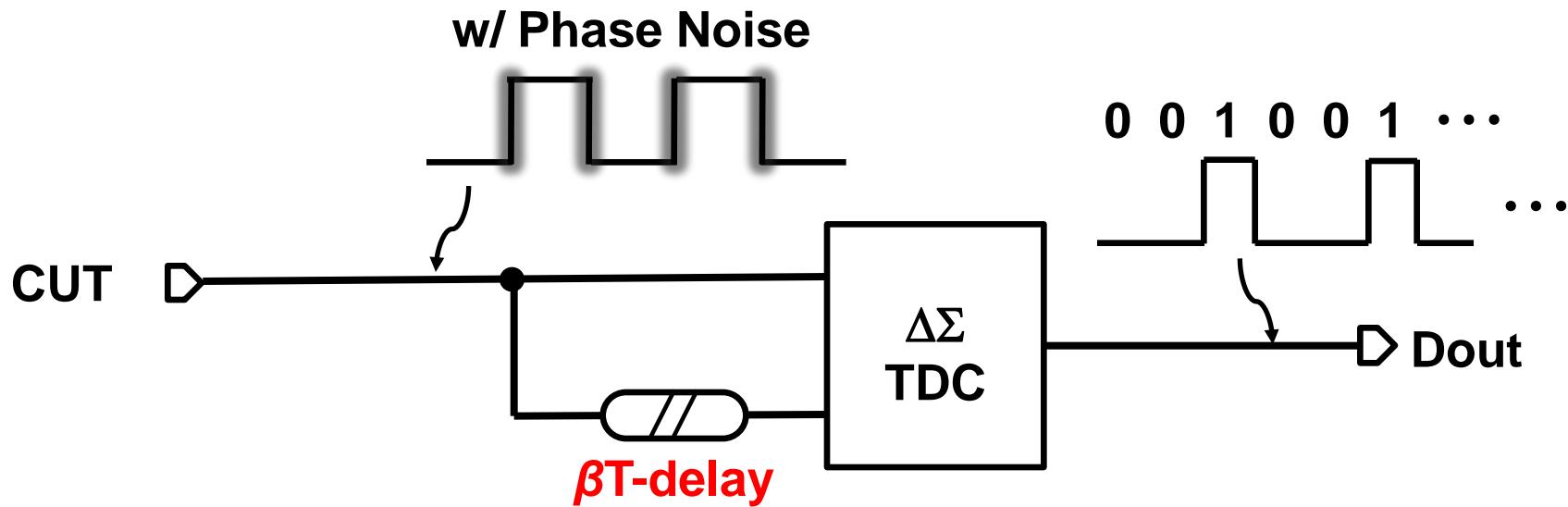
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Problem of Proposed Method I

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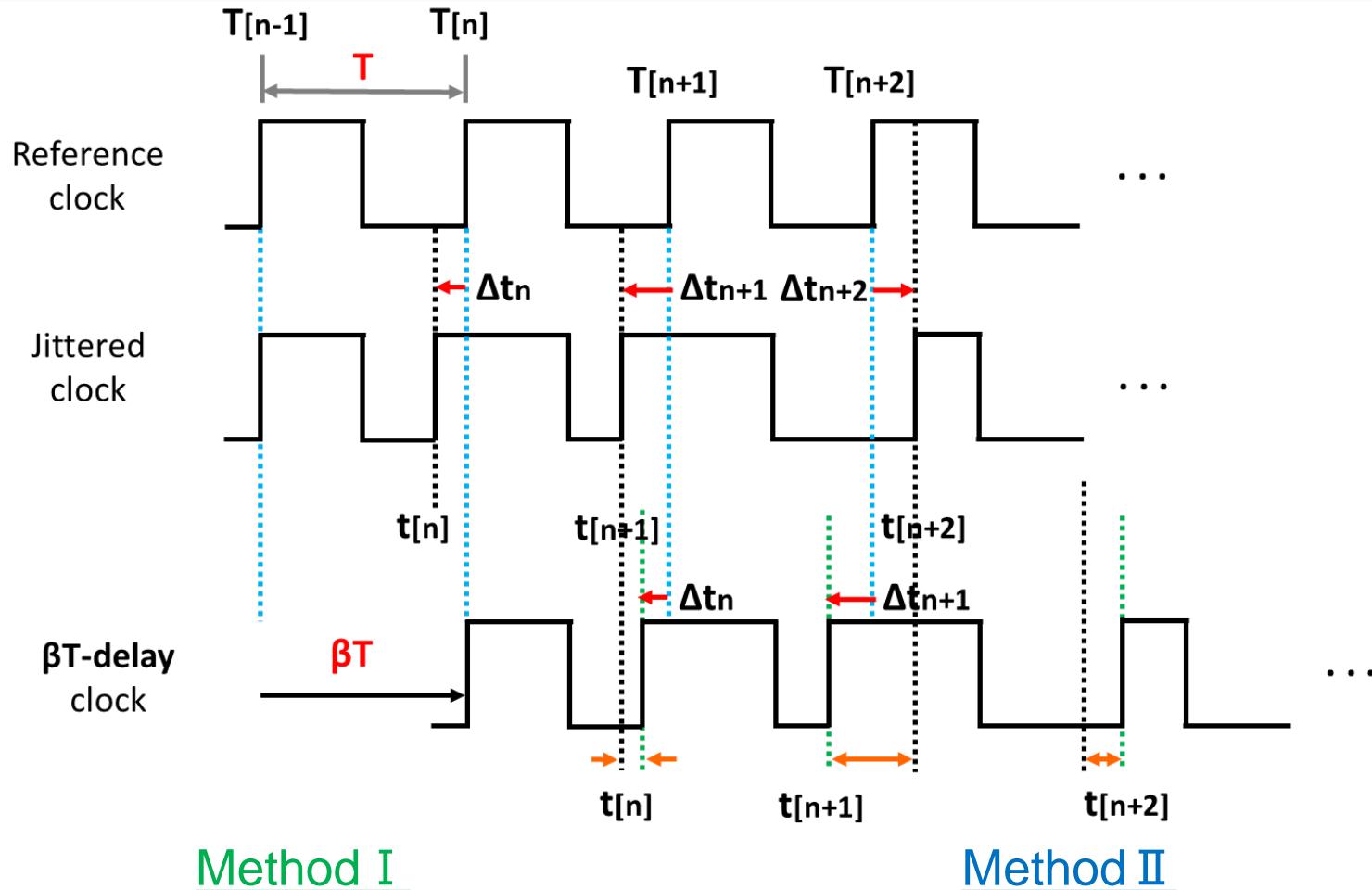
Difficult to implement



- No need for jitterless reference clock
- βT -delay: β is not required to be an integer.

Proposed Methods I & II

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Method I

Timing jitter measurement

Method II



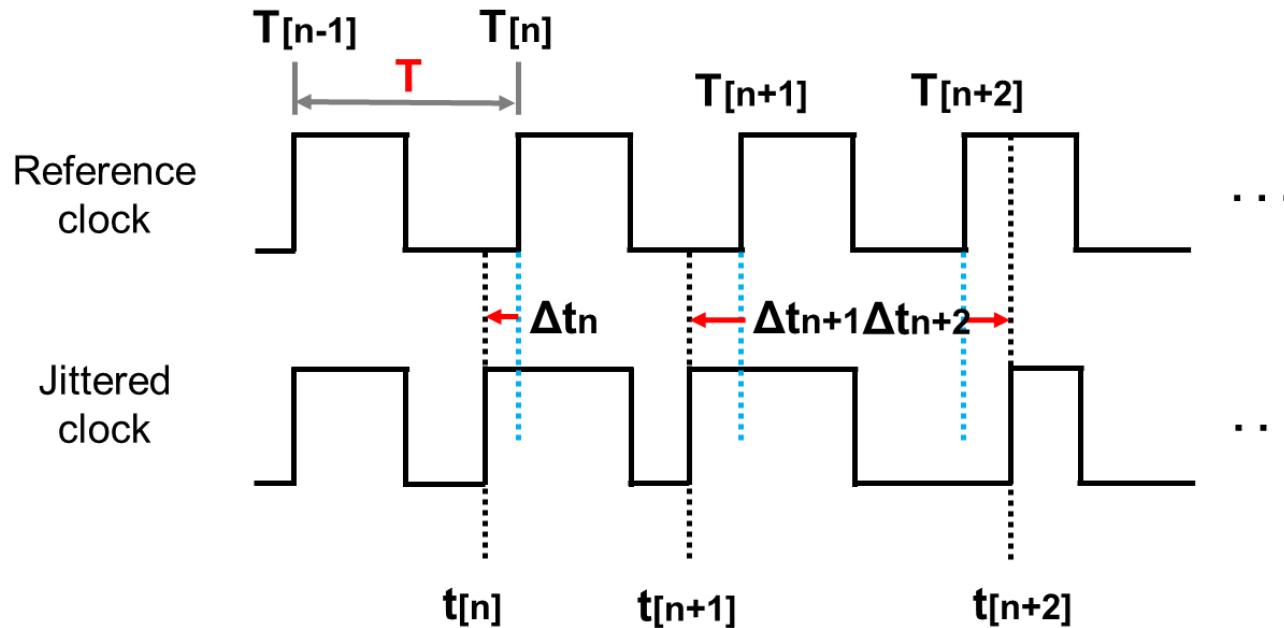
Period jitter measurement

$$J_{PER}(n) = [\Delta t(n) - \Delta t(n-1)] - T_0$$

$$\therefore J_{PER}(n) = J(n) - J(n-1)$$

Mathematical Analysis

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$$\phi(mT) = -2\pi f_{in} \tau(m) : \text{phase noise (time domain)}$$

$$\textcircled{1} \quad \tau(m) = T \cdot \alpha_j \cdot \sin(\omega_j mT)$$

In case of
sinusoidal phase variation

$$0 \leq \alpha_j \leq 1$$

Measurement of each period

$$\begin{aligned}\tau(m+1) - \tau(m) + (\beta - 1)T \\ &= T \cdot \alpha_1 [\sin(\omega_1 (m+1)T) - \sin(\omega_1 \cdot mT)] + (\beta - 1)T \\ &= 2T \cdot \alpha_1 \sin(\omega_1 T/2) \cos(\omega_1 (m+1/2)T) + (\beta - 1)T\end{aligned}$$

$$\textcircled{1} \quad \tau(m) = T \cdot \alpha_j \cdot \sin(\omega_j mT)$$

In case of
sinusoidal phase variation

$$0 \leq \alpha_j \leq 1$$

phase noise (time domain)

$$\therefore \phi'(mT) = 2T \cdot \alpha_1 \sin(\omega_1 T/2) \cos(\omega_1 (m + 1/2)T)$$

phase noise (frequency domain)

$$\therefore \Phi'(\omega_1) = \frac{1}{2} (2\pi\alpha_1)^2 [2 \sin 2(\omega_1 T/2)]^2$$

$$\approx \frac{1}{2} (2\pi\alpha_1)^2 \omega_1^2 T^2 \quad (\because \omega_1 T/2 \ll 1)$$

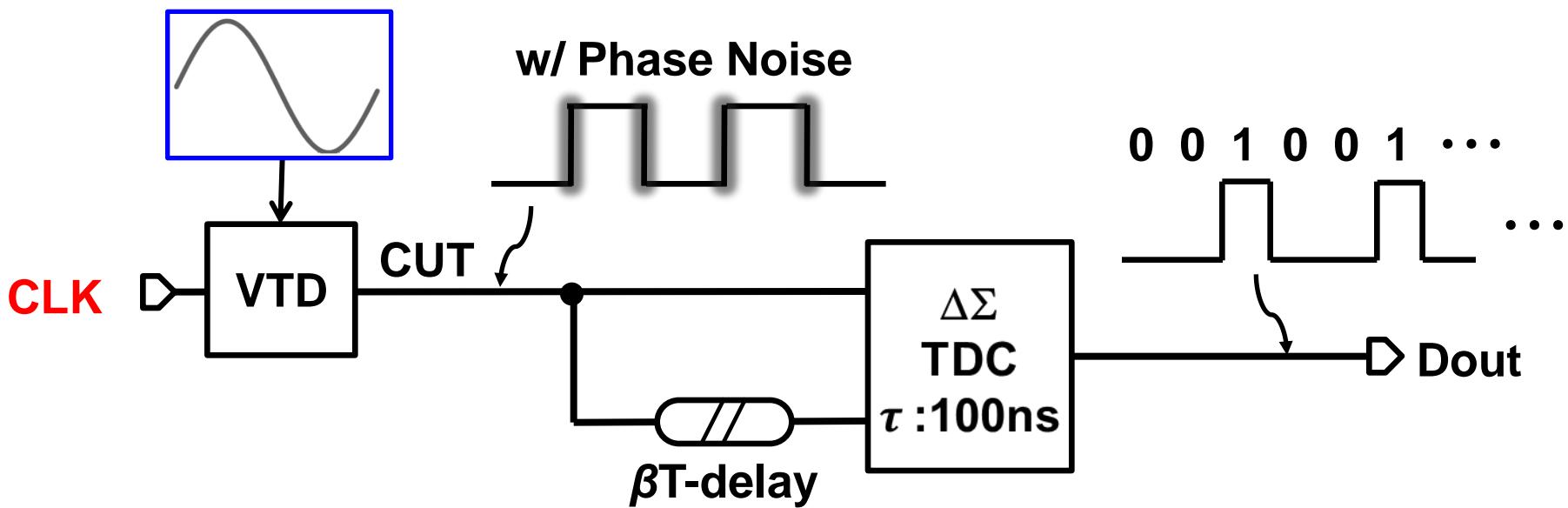
ω_1 : phase noise freq. [low freq.]
 T : input CLK period ($=1/f$)

phase noise power at ω_1

$$\Phi(\omega_1) = \frac{1}{2} (2\pi\alpha_1)^2$$

Simulation Conditions

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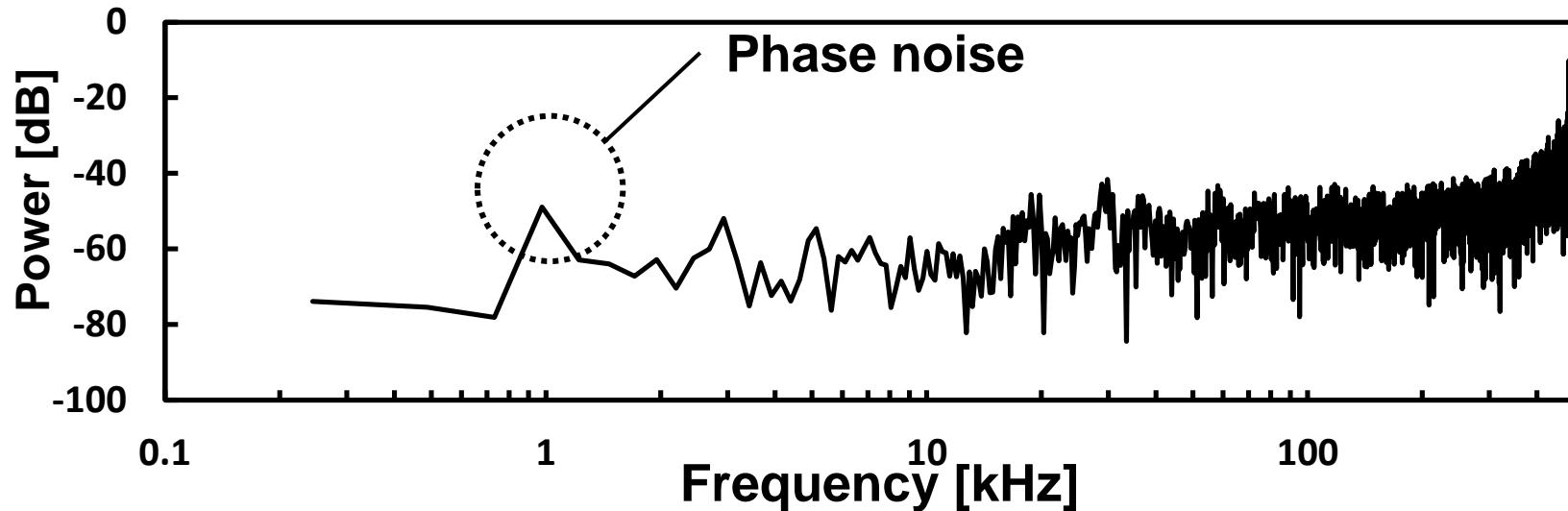


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Input freq. = 1 MHz ($T = 1 \mu\text{s}$)
- Phase variation (sinusoidal)
- Phase noise frequency :
 $f_i = \text{varied}$
- Jitter variation :
 $-0.1\mu\text{s} \leq \tau_0 \leq 0.1\mu\text{s} (= \frac{T}{10})$
- Number of data:
4096
- Single sinusoidal
 - ① $f_1 = 1 \text{ kHz}$
 - ② $f_1 = 10 \text{ kHz}$
 - ③ $f_1 = 100 \text{ kHz}$
- Multiple sinusoidal
 - ④ $f_1 = 10 \text{ kHz}$
 $f_2 = 50 \text{ kHz}$

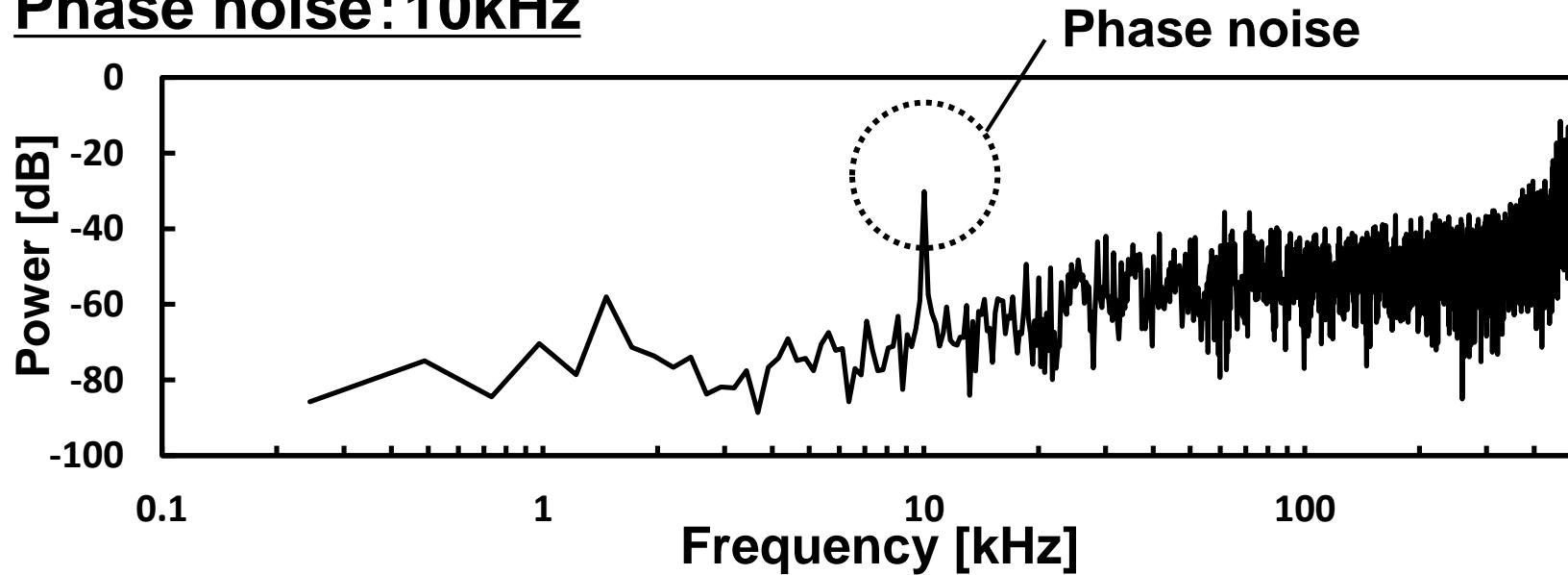
Simulation Results ① & ②

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Phase noise : 1kHz



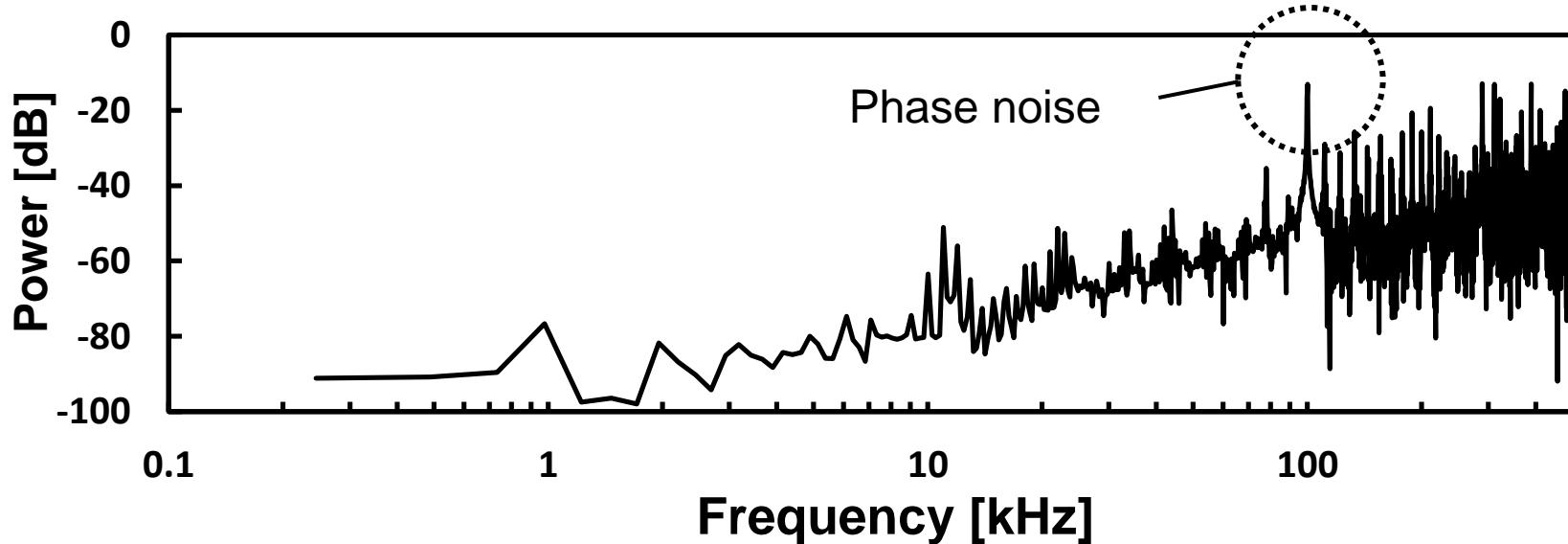
Phase noise : 10kHz



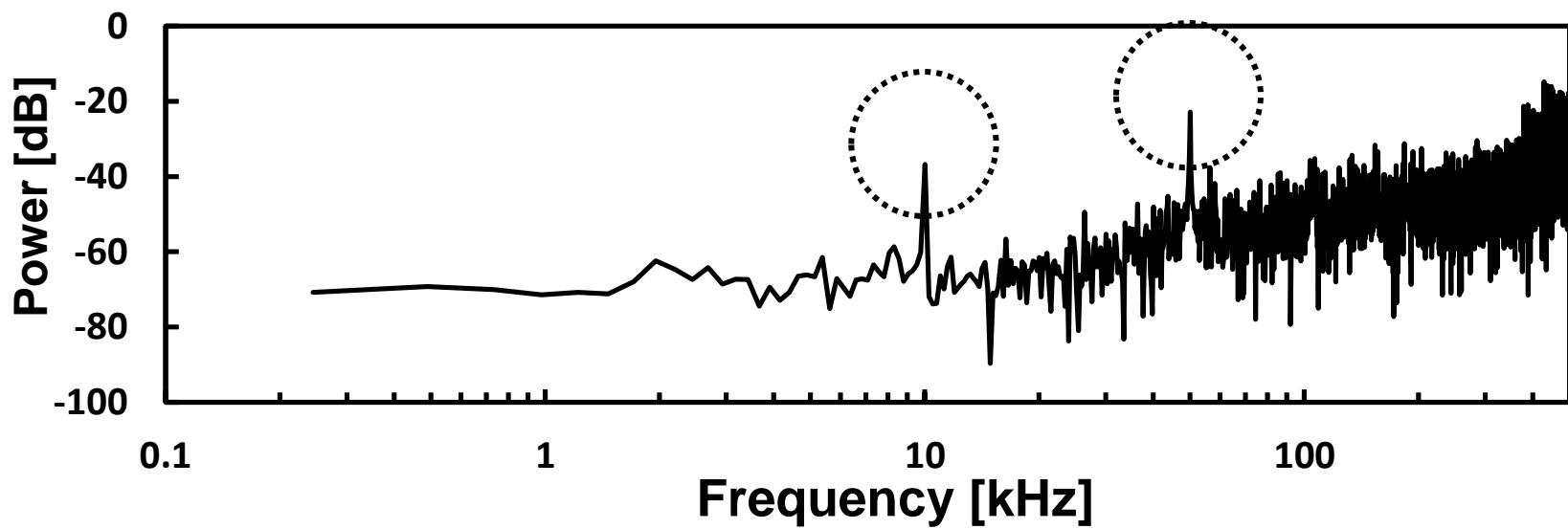
Simulation Results ③ & ④

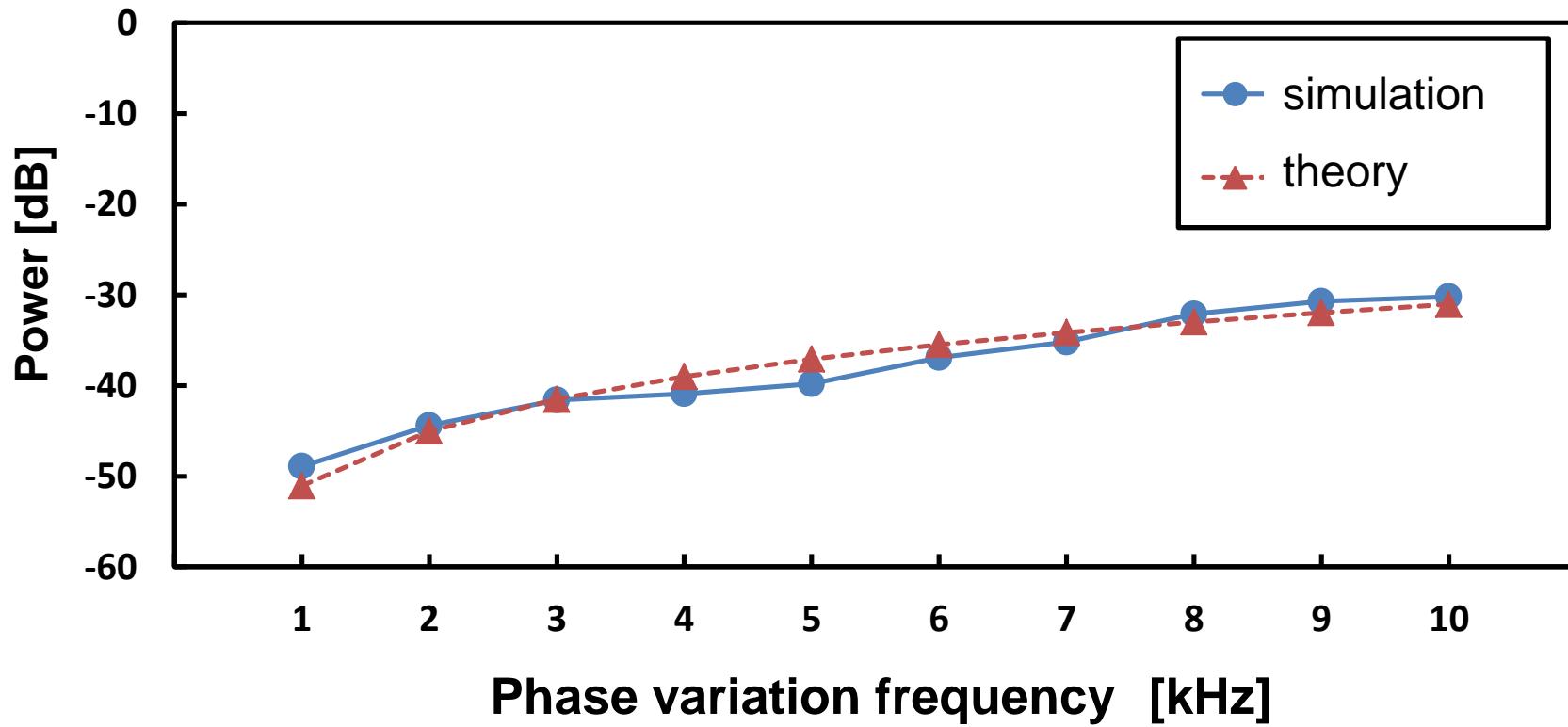
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Phase noise : 100kHz



Phase noise : 10kHz & 50kHz



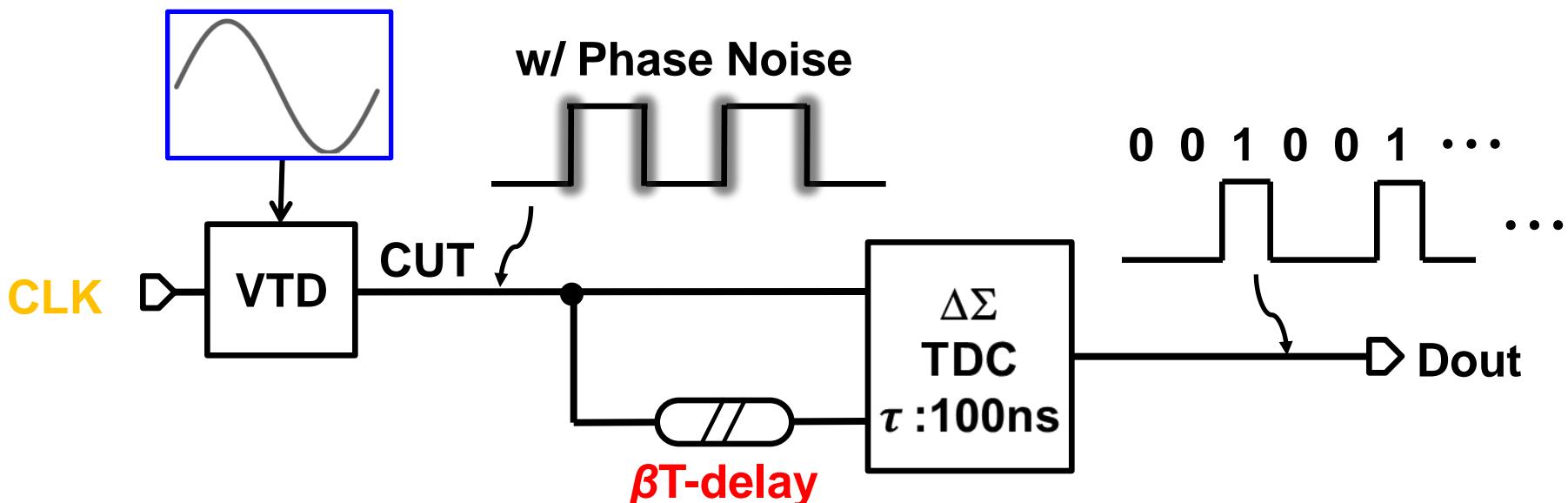


Theoretical expression

$$\Phi'(\omega_1) = \frac{1}{2} (2\pi\alpha_1)^2 \omega_1^2 T^2$$

Simulation Conditions

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- **CLK:**
Input freq. = 1 MHz ($T = 1 \mu\text{s}$)

Phase variation (sinusoidal)

- Phase noise frequency :
 $f_i = 10\text{kHz}$
- Jitter variation :
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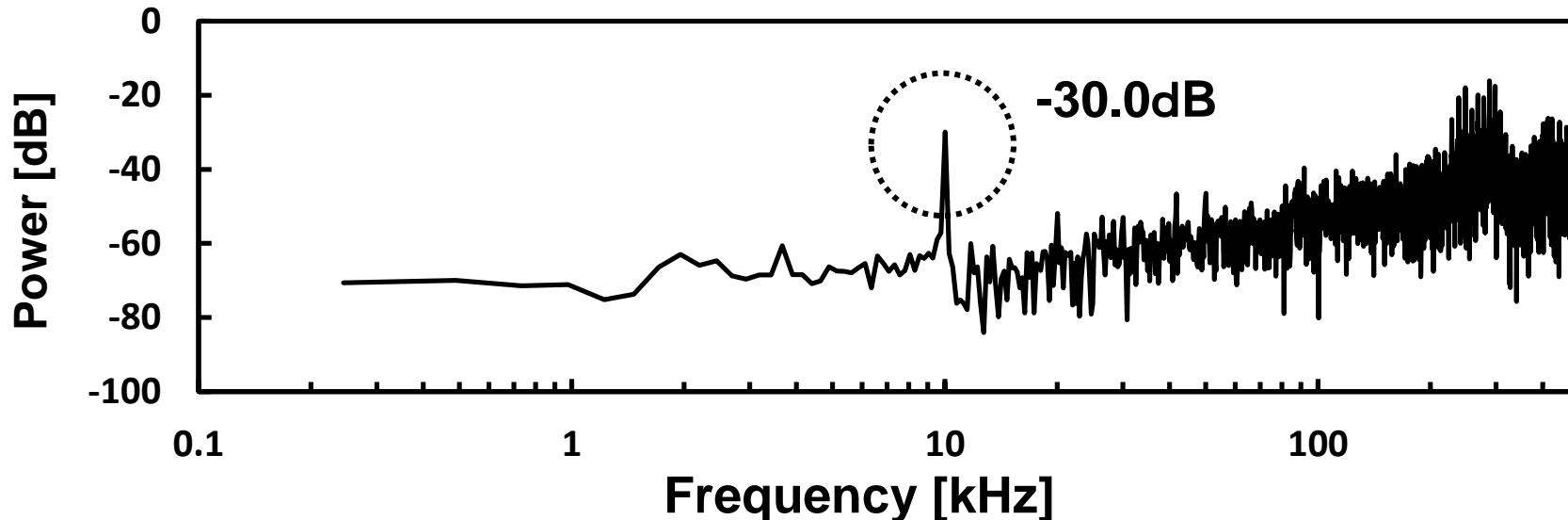
- Number of data:
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- **β value deviation by $\pm 5\%$ from 1.0**
 - $\beta=0.95$
 - $\beta=1.05$

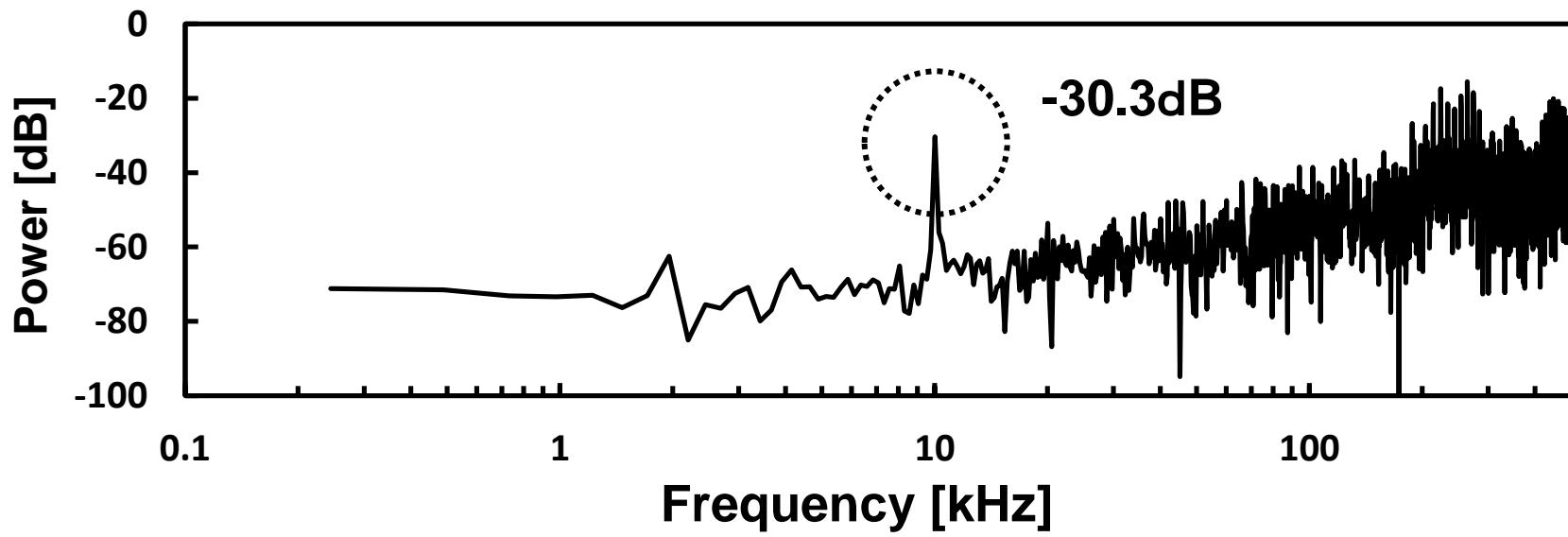
Simulation Results (delay β variation)

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$\beta = 0.95$ (error -5%)



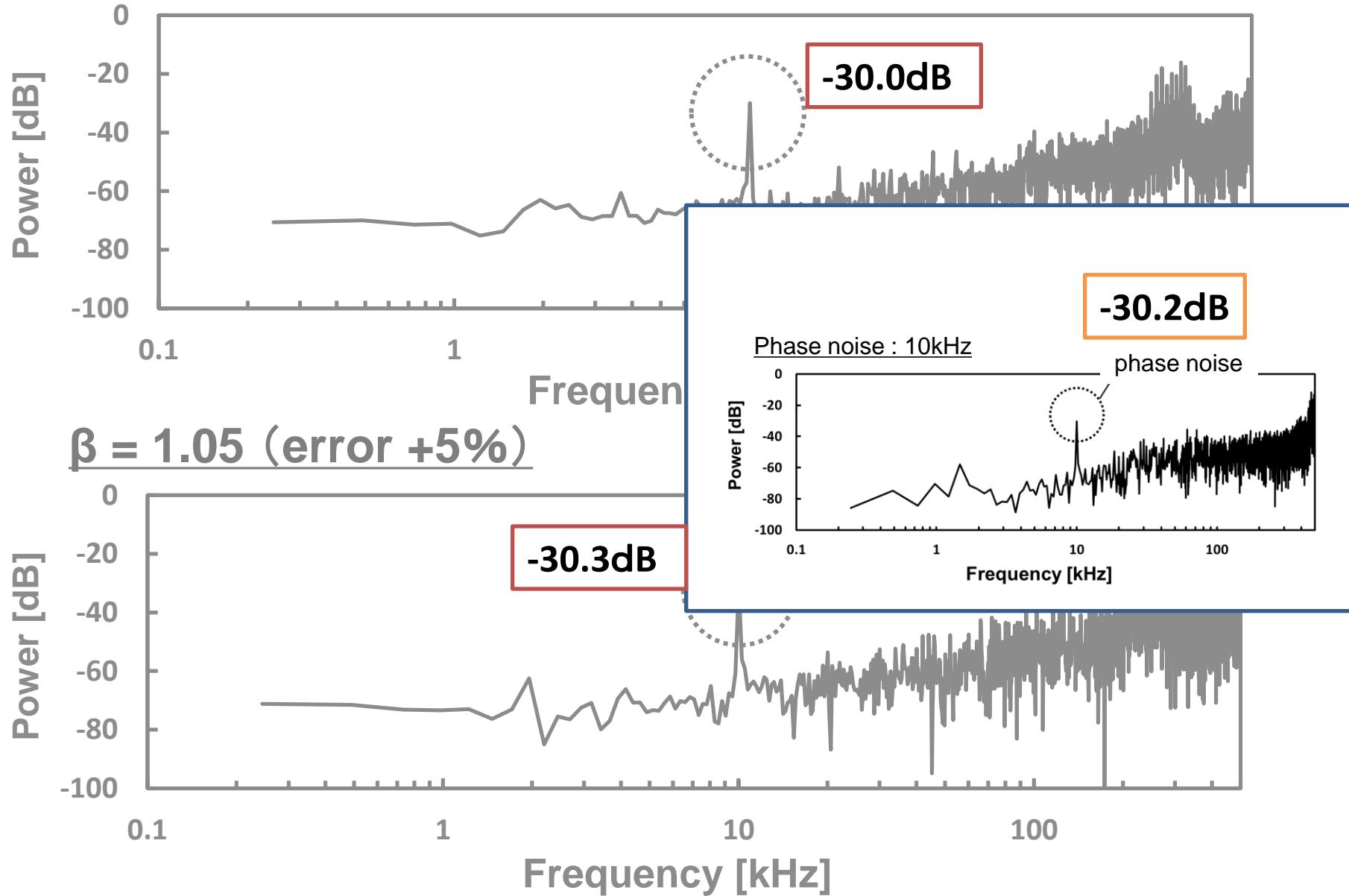
$\beta = 1.05$ (error +5%)



Simulation Results (delay β variation)

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$\beta = 0.95$ (error -5%)



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■ Proposal of two phase measurement techniques with $\Delta\Sigma$ TDC

- Low cost testing without requiring spectrum analyzer
- On-chip high-precision phase noise measurement
- Fine time resolution measurement possible with $\Delta\Sigma$ TDC
- Phase noise power spectrum obtained by FFT of TDC digital output

1MHz carrier (clock), 64K TDC output data

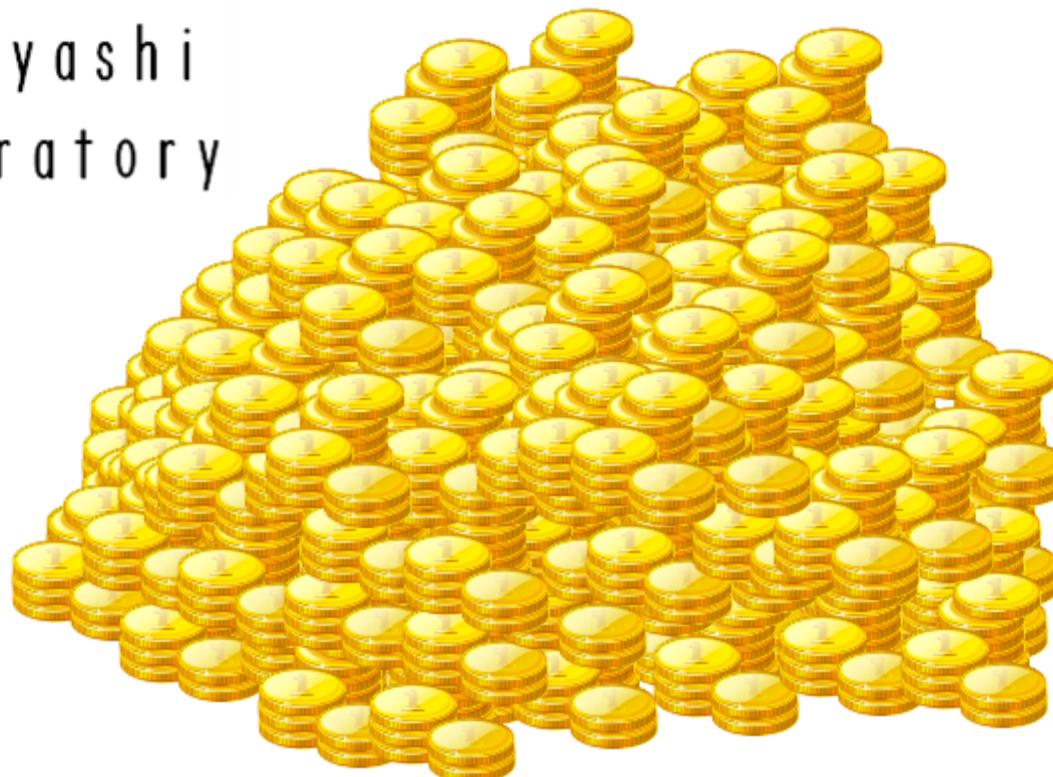
 Phase noise power spectrum of 0 to 0.5MHz away from 1MHz
with 15.2Hz resolution.

■ MATLAB simulation verification

- Verified by superimposing several sinusoidal phase variation components
- Compared theoretical analysis and simulation results
- Self-referenced clock method with several β delay coefficient values



Kobayashi
Laboratory



Time is GOLD !!

$\Delta\Sigma TDC$ is the key.

Acknowledgement

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