

Low-Distortion Single-Tone and Two-Tone Sinewave Generation Using $\Sigma\Delta$ DAC

Takafumi Yamada, Osamu Kobayashi †, Keisuke Kato, Kazuyuki Wakabayashi, Haruo Kobayashi
 Tatsuji Matsuura, Yuji Yano †, Tatsuhiro Gake †, Kiichi Niitsu, Nobukazu Takai, Takahiro J. Yamaguchi
 Dept. of Electronic Engineering, Gunma University, Kiryu Gunma 376-8515 Japan email: kharuo@el.gunma-u.ac.jp
 † Semiconductor Technology Academic Research Center (STARC), Yokohama 222-0033 Japan

This paper describes algorithms for generating low-distortion single-tone and two-tone signals for ADC testing using a multi-bit $\Sigma\Delta$ DAC (Fig.1). The DSP part provides two interleaved signals with the same frequency but different phase to the input of the $\Sigma\Delta$ DAC, in order to precompensate for distortion caused by multi-bit DAC nonlinearity (Fig.1). Theoretical analysis, simulation, and experimental results all show the effectiveness of this approach.

We consider the case that third-order harmonic distortion (HD3) is dominant in the $\Sigma\Delta$ DAC (Figs.1, 2).

For low-HD3 sinusoidal signal generation, we use the following digital input D_{in} to the $\Sigma\Delta$ DAC:

$$D_{in}(n) = \begin{cases} A\sin(\omega_{in}nT_s + \pi/6) & \text{in case } n:\text{even} \\ A\sin(\omega_{in}nT_s - \pi/6) & \text{in case } n:\text{odd.} \end{cases}$$

Fig.3 shows experimental results for single-tone signal generation using a second-order $\Sigma\Delta$ DAC with 4-bit internal DAC: we see that the proposed algorithm suppresses HD3.

For low distortion two-tone signal generation, we use the following D_{in} :

$$D_{in}(n) = \begin{cases} A\sin(\omega_1nT_s + \pi/6) + B\sin(\omega_2nT_s - \pi/6) & n:\text{even} \\ A\sin(\omega_1nT_s - \pi/6) + B\sin(\omega_2nT_s + \pi/6) & n:\text{odd.} \end{cases}$$

Fig.4 shows experimental results for two-tone signal generation using a second-order $\Sigma\Delta$ DAC with 4-bit internal DAC: we see that the proposed algorithm reduces third-order intermodulation distortion (IMD3) components.

We note that: (i) Methods such as dynamic element matching and self-calibration have been used for reducing the effects of multi-bit DAC nonlinearity in $\Sigma\Delta$ modulators [1], but these methods require additional hardware. However, our proposed method needs only a DSP program algorithm change.

(ii) Our proposed method does not require exact identification of the DAC nonlinearity.

(iii) Generalization of the proposed algorithm, e.g. for simultaneous cancellation of HD2 and HD3, is possible [2].

(iv) Our proposed method may be implemented by utilizing existing DSP and DAC cores inside an SoC in test mode.

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REFERENCES

- [1] R. Schreier and G. C. Temes, *Understand Delta-Sigma Data Converters*, IEEE Press (2005).
- [2] K. Wakabayashi, et. al., "Low-Distortion Single-Tone and Two-Tone Sinewave Generation Algorithms Using an Arbitrary Waveform Generator," IEEE IMS3TW, Santa Barbara, CA (May 2011).

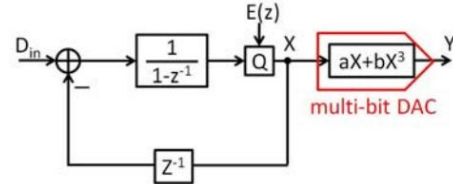


Fig.1. $\Sigma\Delta$ DAC with an internal multi-bit DAC.

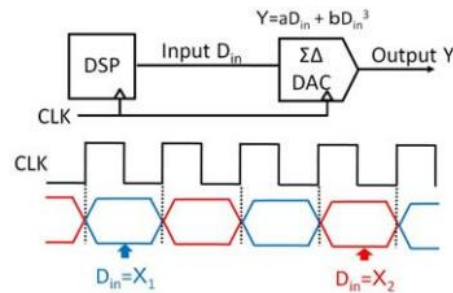


Fig.2. Proposed method to generate low-distortion signal with $\Sigma\Delta$ DAC. DSP provides the input signal X1 and X2 alternatively to the following $\Sigma\Delta$ DAC, and distortion components (such as HD3, IMD3) are cancelled.

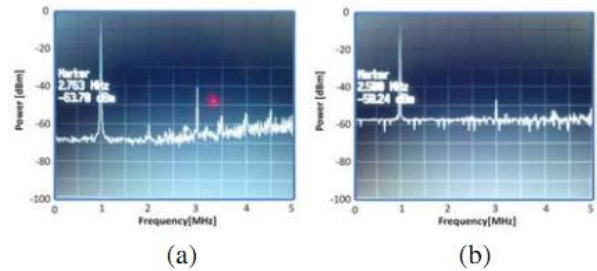


Fig.3. Single-tone generation with $\Sigma\Delta$ DAC. (a) Conventional method. (b) Proposed method.

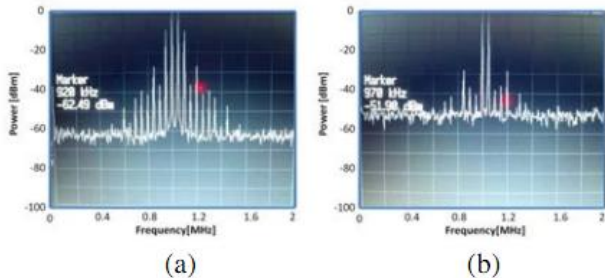


Fig.4. Two-tone signal generation with $\Sigma\Delta$ DAC. (a) Conventional method. (b) Proposed method.

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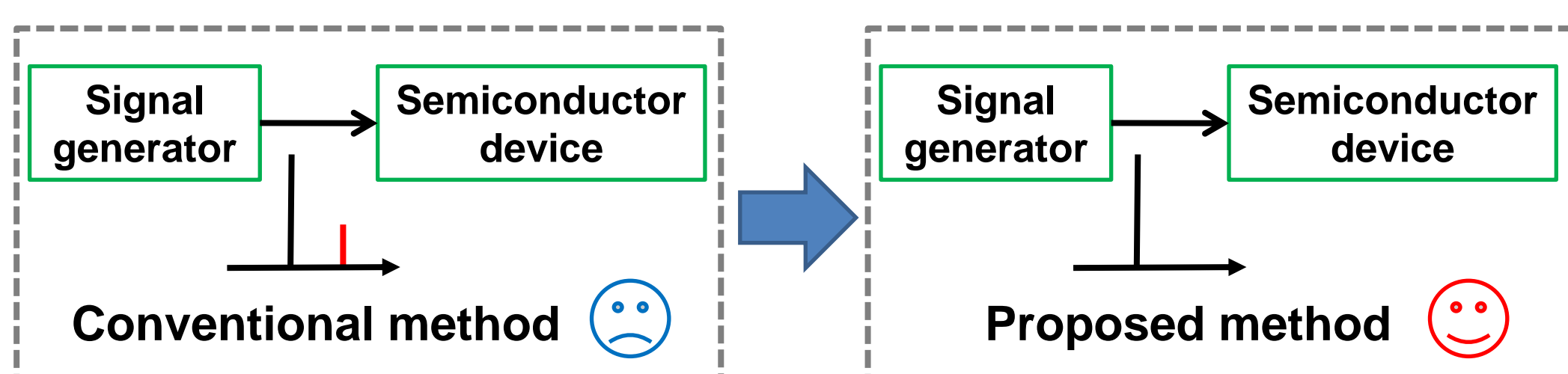
Dept. of Electronic Engineering, Gunma University, Kiryu Gunma 376-8515 Japan email: t11801611@gunma-u.ac.jp † Semiconductor Technology Academic Research Center (STARC), Yokohama 222-0033 Japan

Introduction

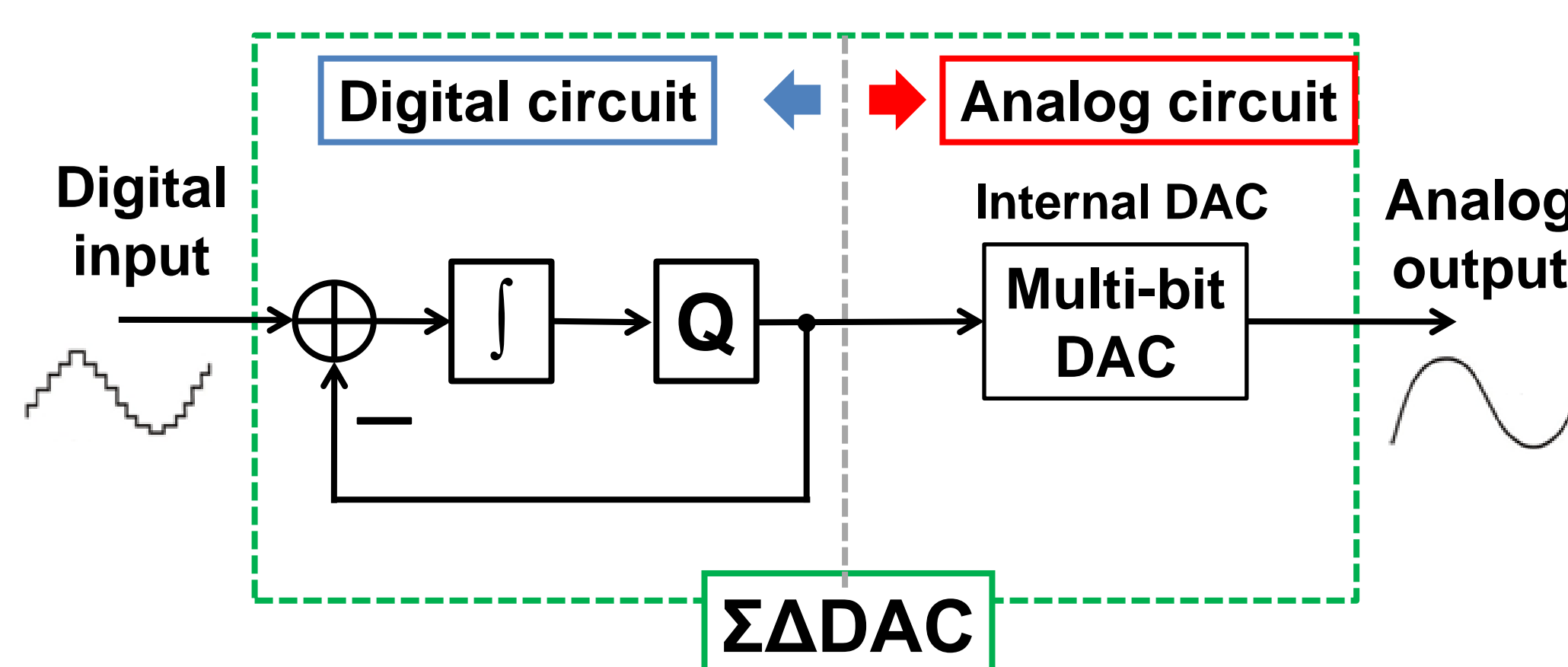
Research Purpose

Proper-quality low-cost testing of ADCs in SoC

Low-distortion sinusoidal signal generation with DSP and DAC cores in SoC

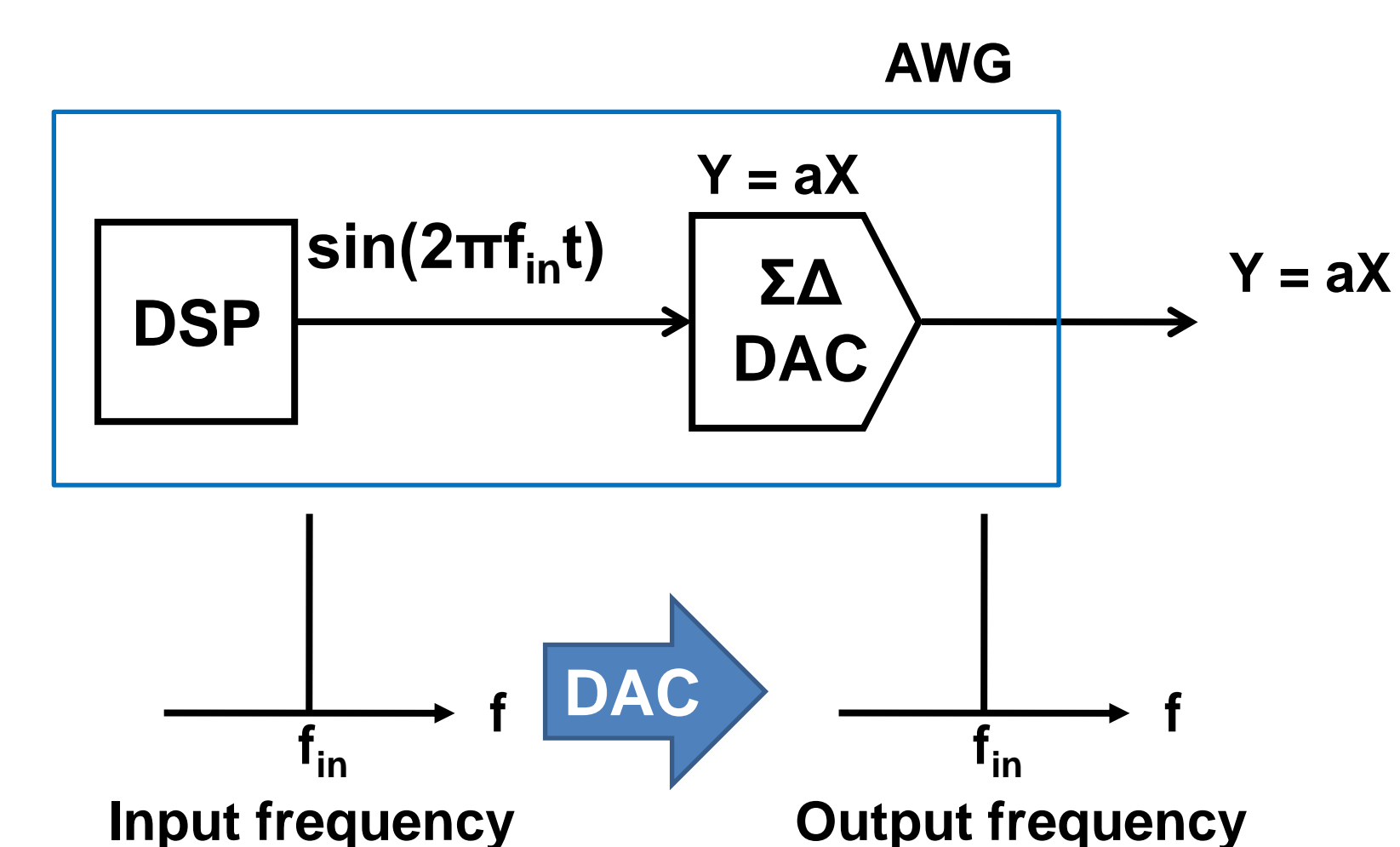


$\Sigma\Delta$ DAC Configuration



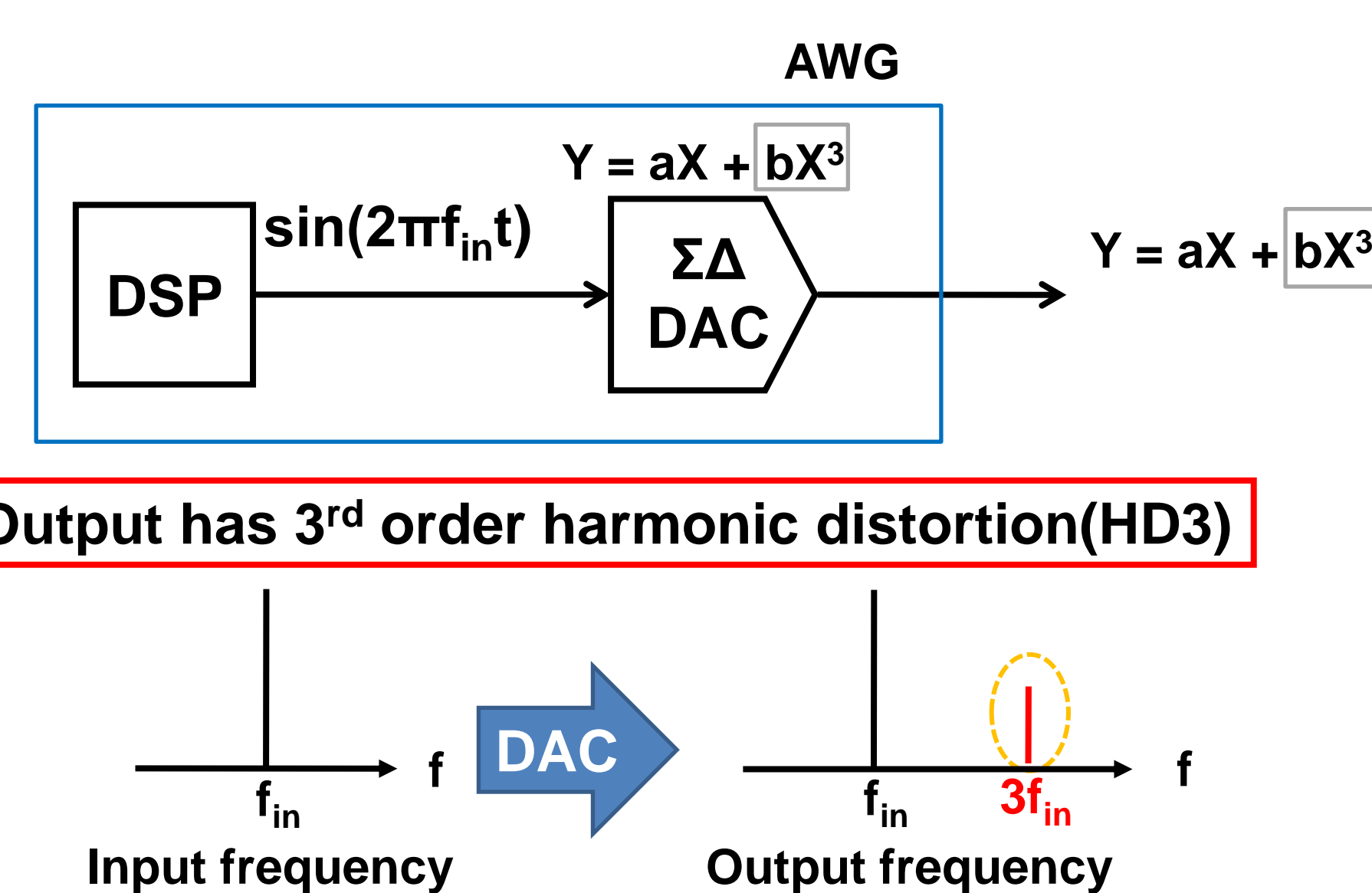
$\Sigma\Delta$ DAC \Rightarrow can be implanted with DSP and DAC cores inside SoC in test mode

Ideally Linear $\Sigma\Delta$ DAC



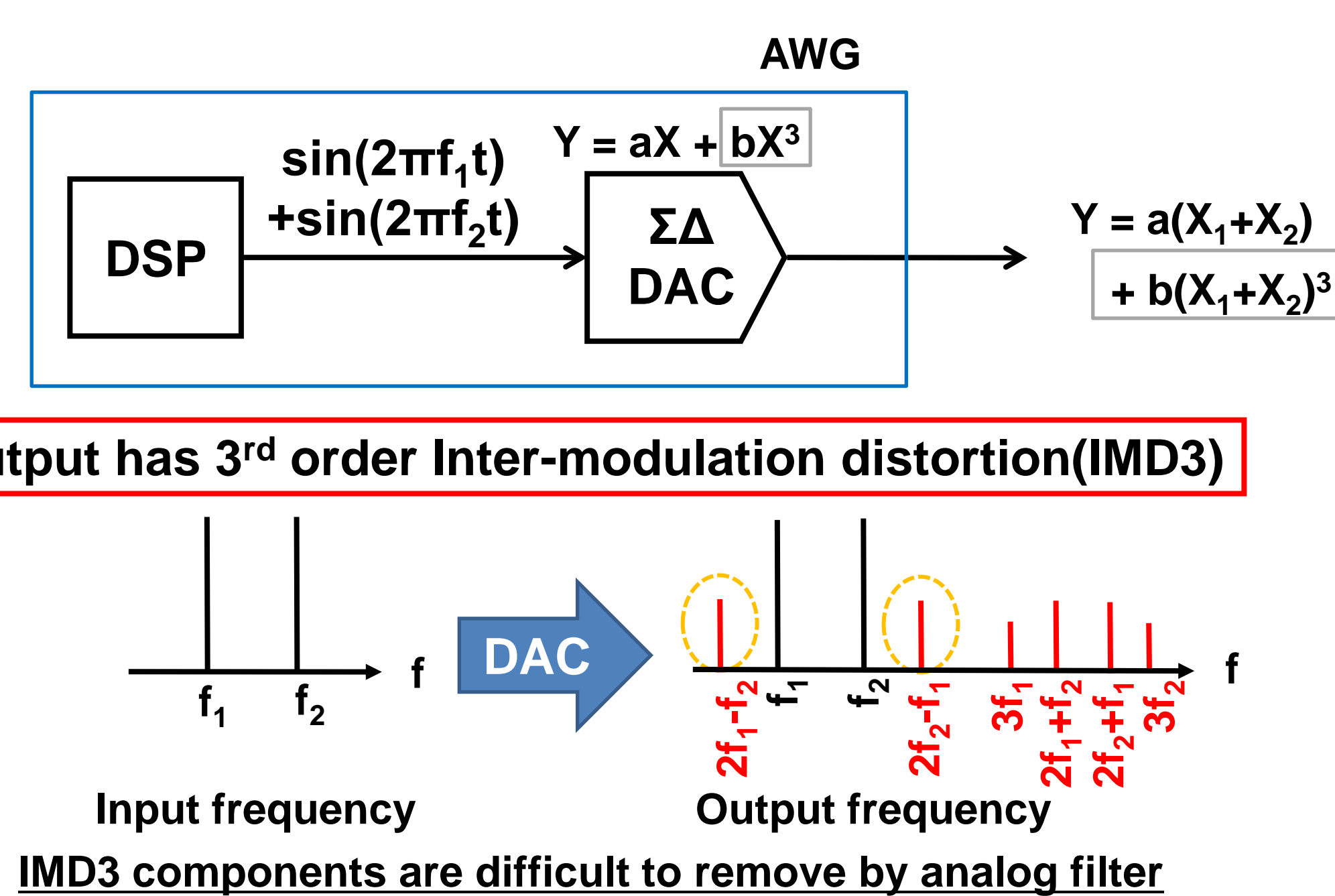
The same frequency of input and output signals

Actual $\Sigma\Delta$ DAC Single-tone Generation



Output has 3rd order harmonic distortion(HD3)

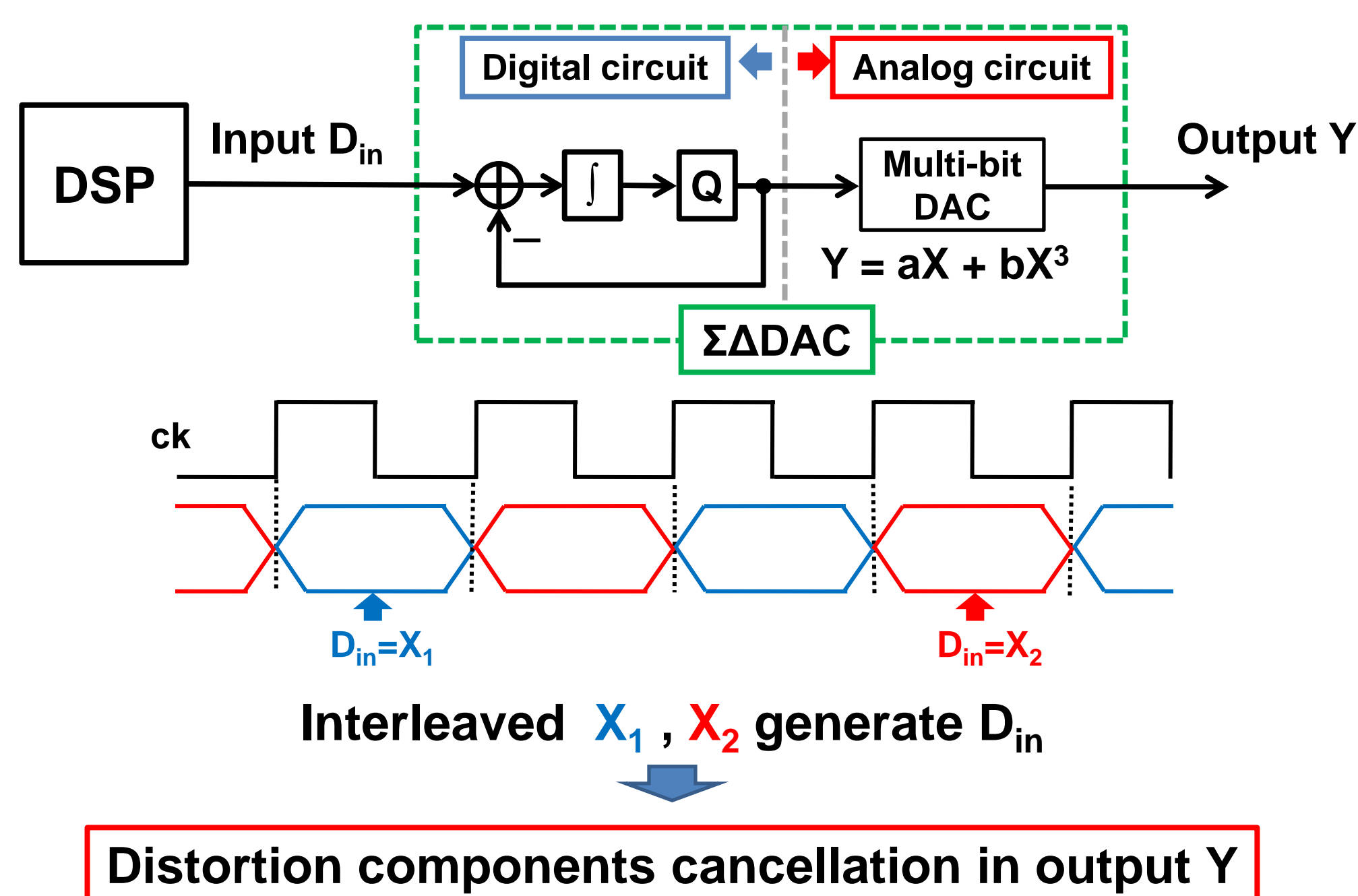
Actual $\Sigma\Delta$ DAC Two-tone Generation



Output has 3rd order Inter-modulation distortion(IMD3)

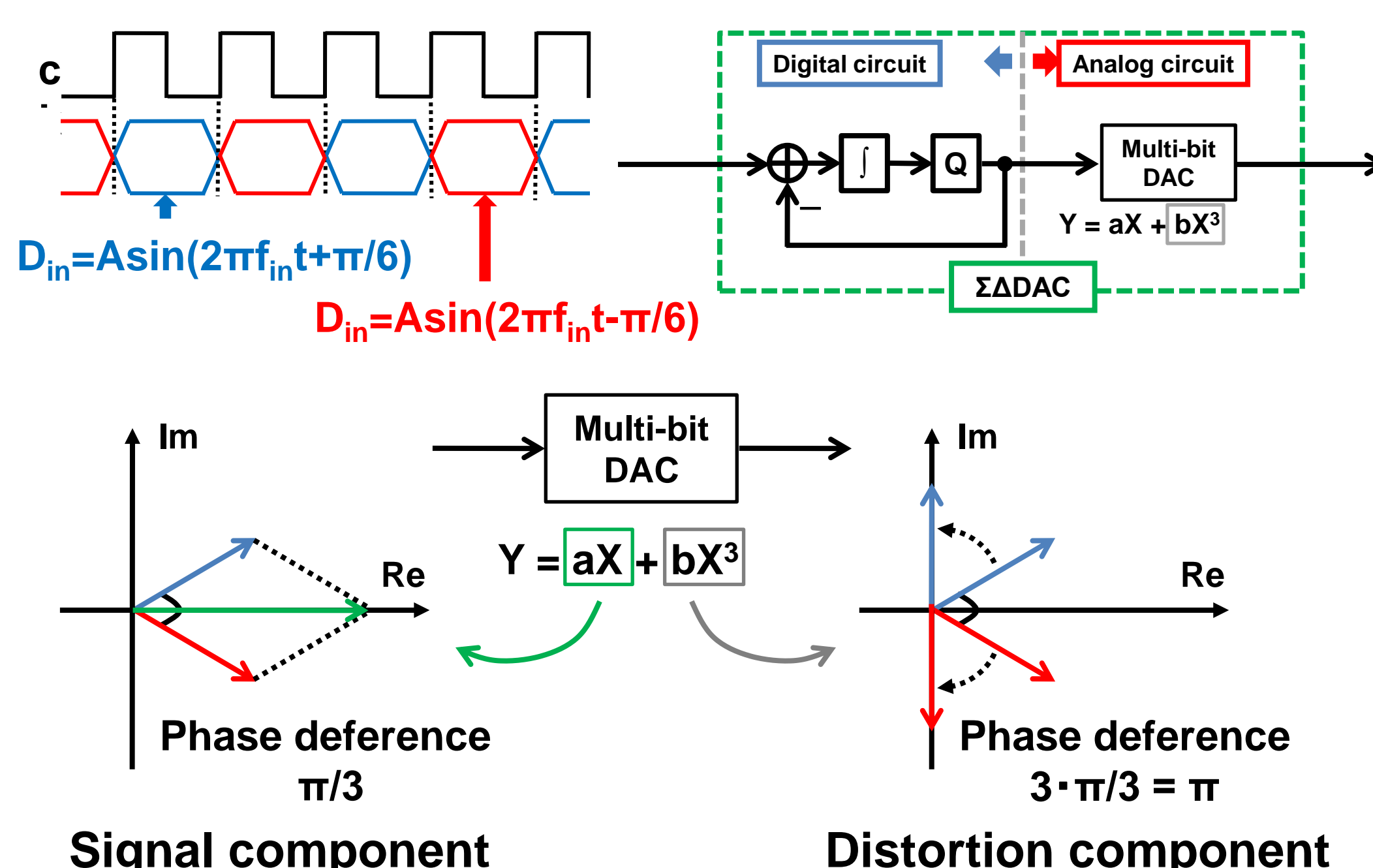
IMD3 components are difficult to remove by analog filter

Proposed Method

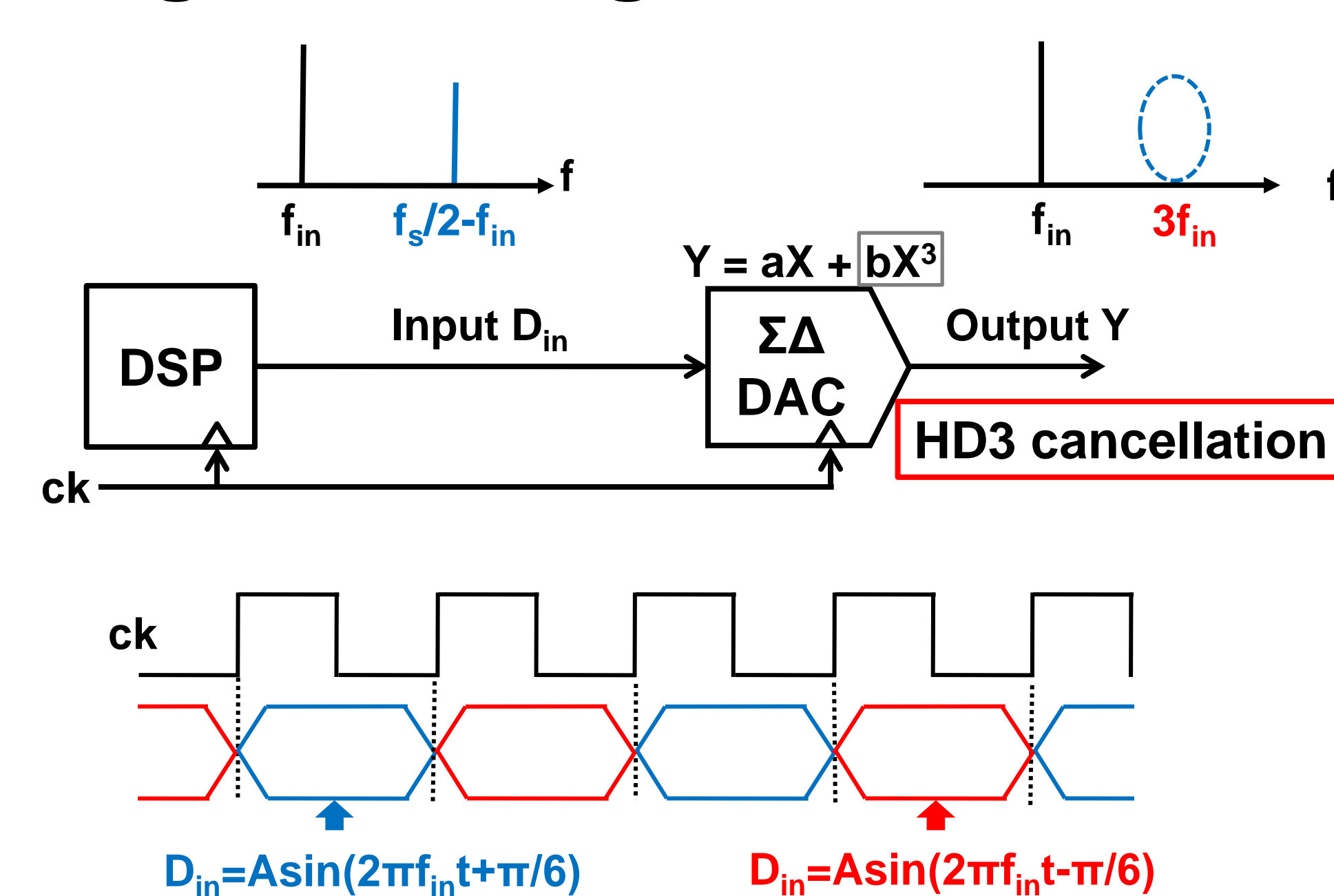


Distortion components cancellation in output Y

Principle of Proposed Method

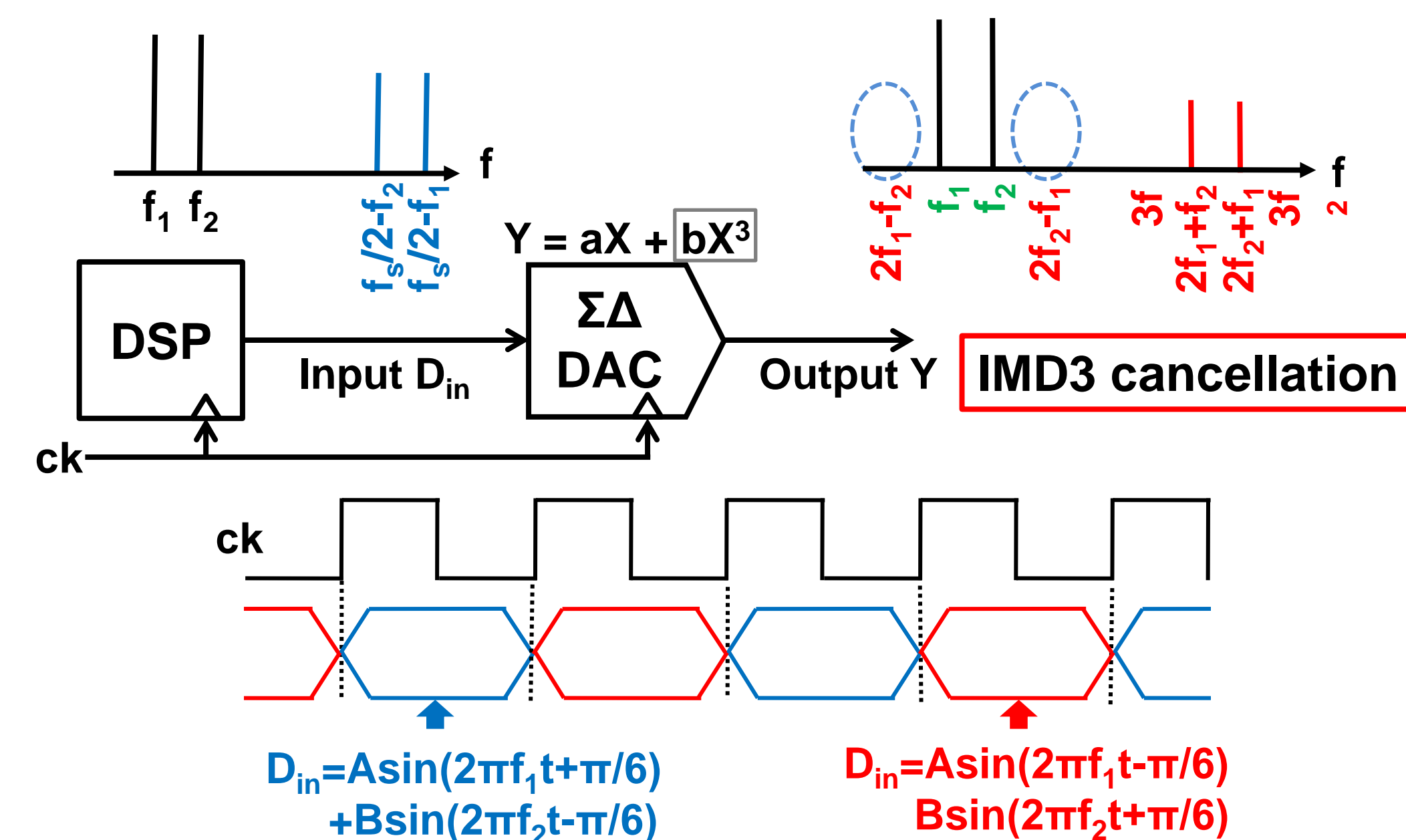


Single-tone Signal Generation



HD3 cancellation

Two-tone Signal Generation



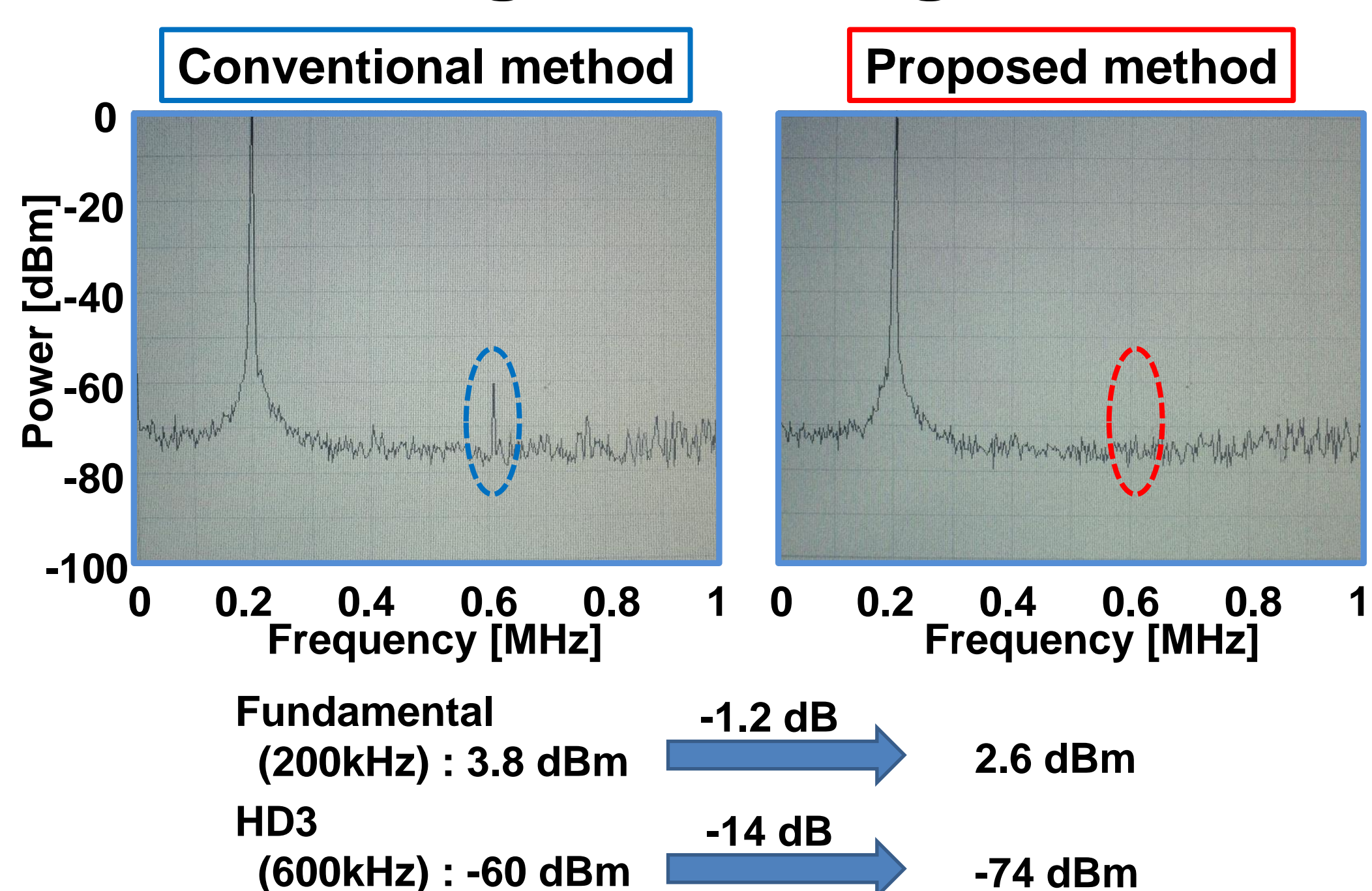
IMD3 cancellation

Experiment Condition

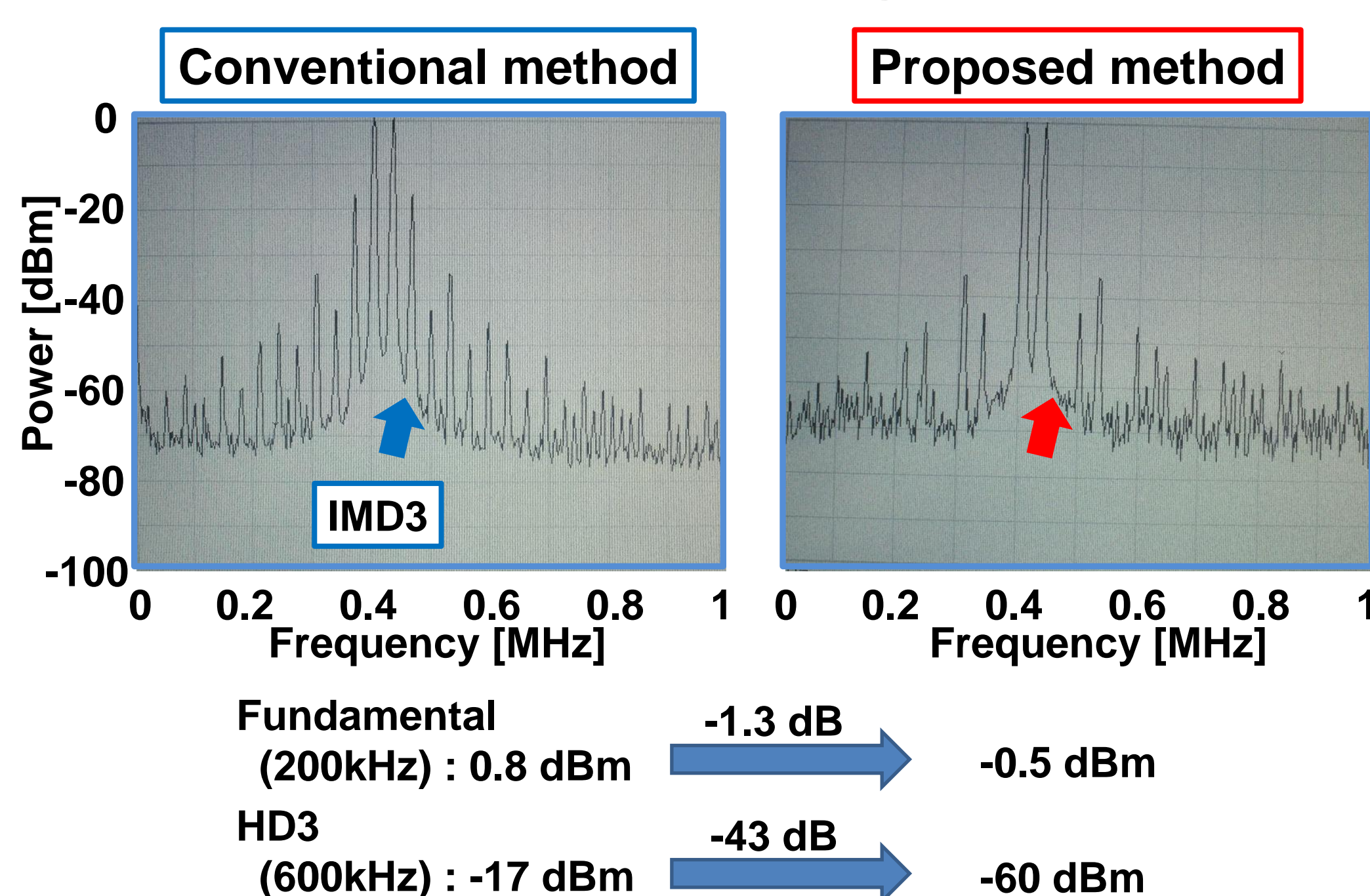
AWG Agilent 33120A Spectrum Analyzer : hp ESA-L1500A

Max. Sampling frequency (Hz)	40M	Frequency range (Hz)	9k~ 1.5G
Resolution (bit)	12	Max amplitude (Vpp)	19.8
Linearity	Δ	RBW : Resolution band width (Hz)	1k
Input frequency (Hz)	200k	VBW : Video band width (Hz)	100k
Input amplitude (V _{pp})	1		
Sampling frequency (Hz)	8M		

Single-tone Signal



Two-tone signal



Conclusion

- Low-distortion sinewave generation using $\Sigma\Delta$ DAC
 - Single-tone : HD3 cancellation
 - Two-tone : IMD3 cancellation
- Only DSP programming change
- No need for DAC nonlinearity identification
- Effectiveness is verified with theoretical analysis and experiments

Proposed test method

Conclusion

Measurement