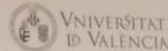
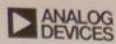


Presented by Shintaro Takagi (都木 新太郎)

Shintaro Takagi, Biswas Sumit Kumar,
Nobukazu Takai, Haruo Kobayashi,
“Successive-Approximation-Register Digital Low-Dropout
Regulator (SAR digital LDO) with Fast Transient-Response
and No Output Ripple,”
International Conference on Analog VLSI Circuits,
Valencia, Spain (Oct. 24-26, 2012)

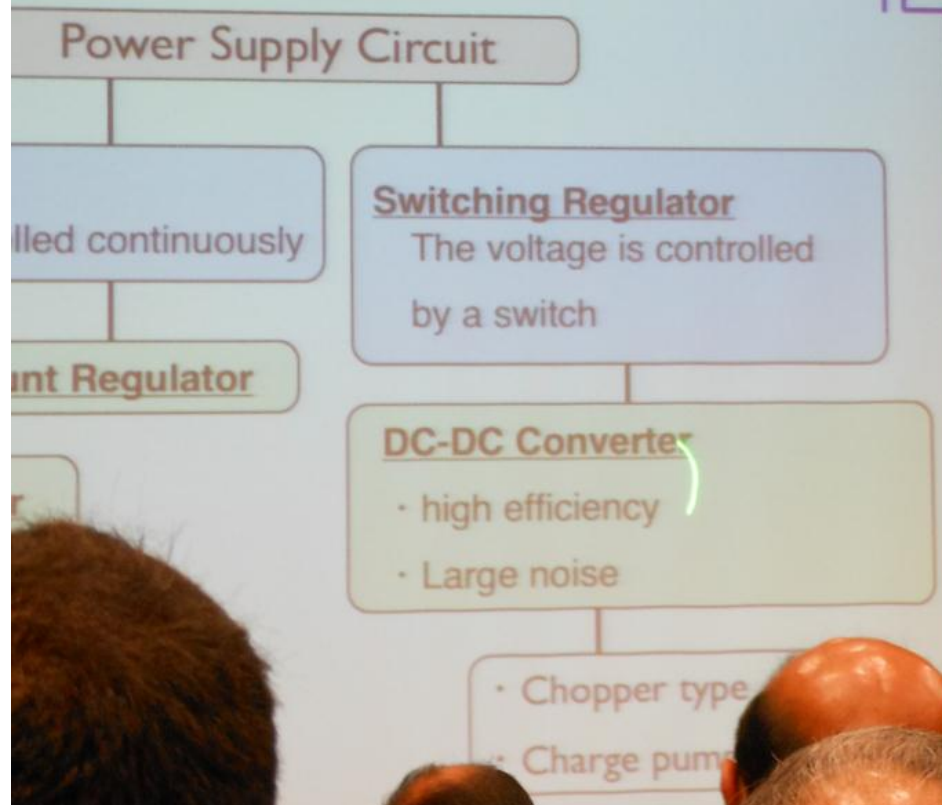


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Classification of Power Supply Circuit



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