

Digitally-Controlled Gm-C Band-pass Filter

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Supported by STARC

Presented by Guanglei Jin (靳光磊)



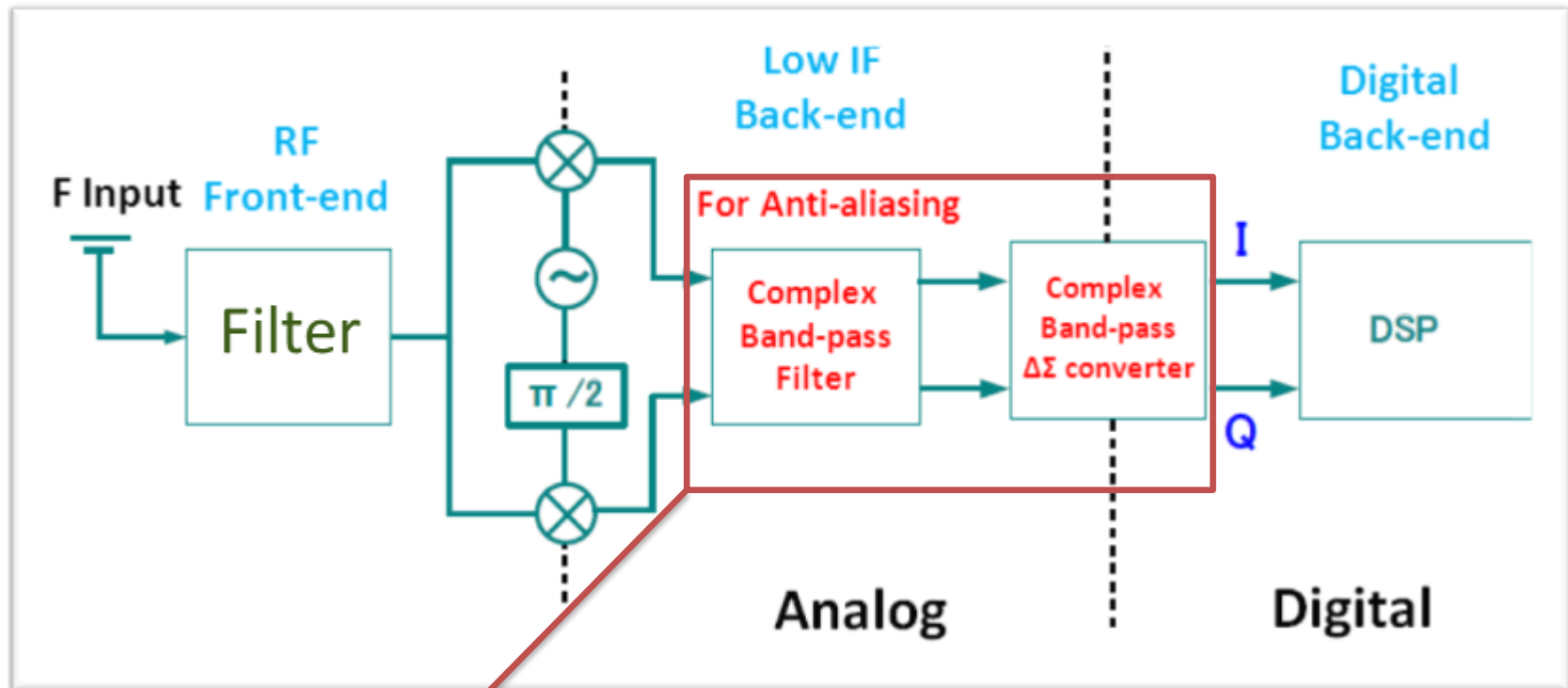
Outline

- *Research Objective*
- *Switched Gm-C Band-pass Filter*
- *Center Frequency Tuning*
- *Q-Value Tuning*
- *Conclusion*

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Wireless LAN, Bluetooth, etc. IF Receiver



Gm-C filters are needed



Center frequency & Q-value adjustment is a challenge

Research Objective

Fine CMOS process  Low voltage



Analog band-pass filter

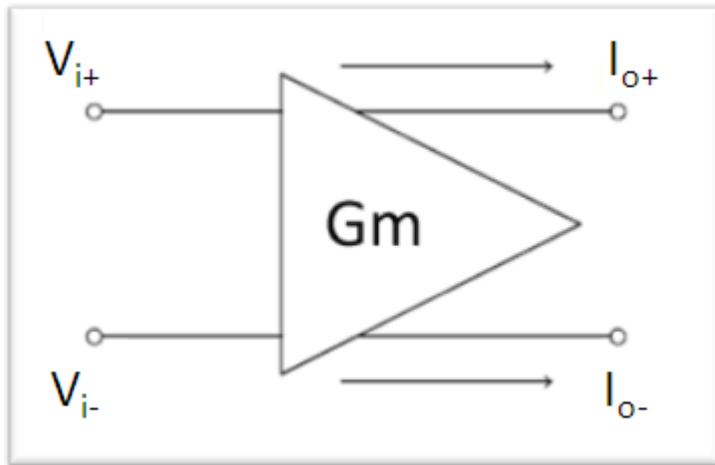
- Switched Gm-C integrator
- Digital schemes
 - Center Frequency
 - Q-value

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- *Center Frequency Tuning*
- *Q-Value Tuning*
- *Conclusion*

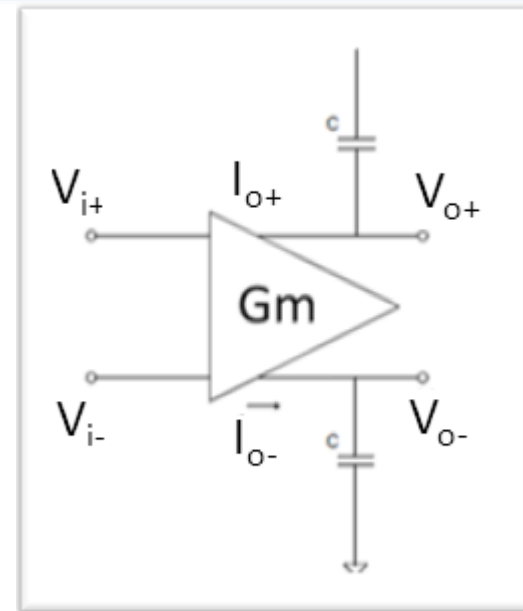
Proposed Switched Gm-C Integrator

Input Voltage Output Current



Gm Cell (OTA)

$$I_o = I_{o+} - I_{o-} = gm(V_{i+} - V_{i-})$$



Gm-C Integrator

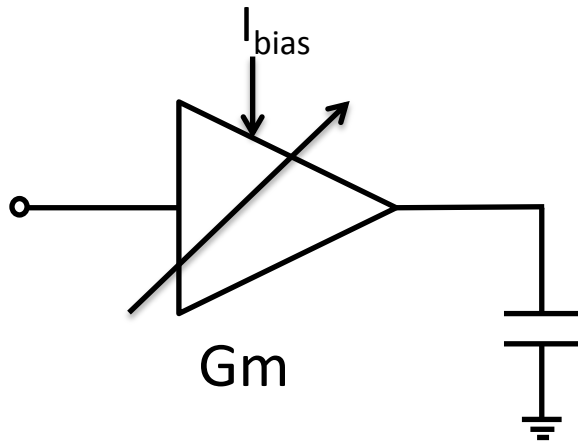
$$V_{o+} = \frac{I_{o+}}{sC} = \frac{gm}{2sC} (V_{i+} - V_{i-})$$

$$V_{o-} = \frac{I_{o-}}{sC} = -\frac{gm}{2sC} (V_{i+} - V_{i-})$$

$$V_o = V_{o+} - V_{o-} = \frac{gm}{sC} V_i$$

Proposed Switched Gm-C Integrator

Conventional approach



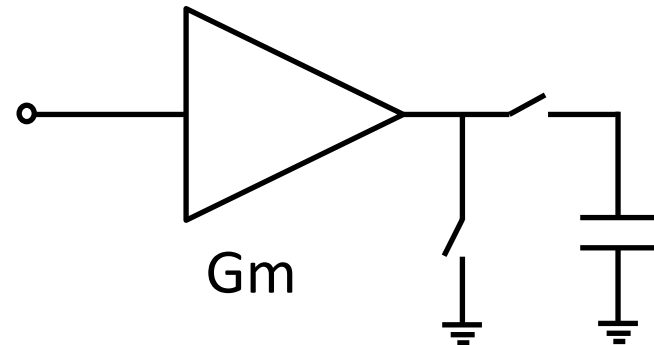
Analog adjustment of Gm using I_{bias}



Difficult for fine CMOS with low voltage



Proposed method



Digital adjustment of Gm by switch



low voltage control

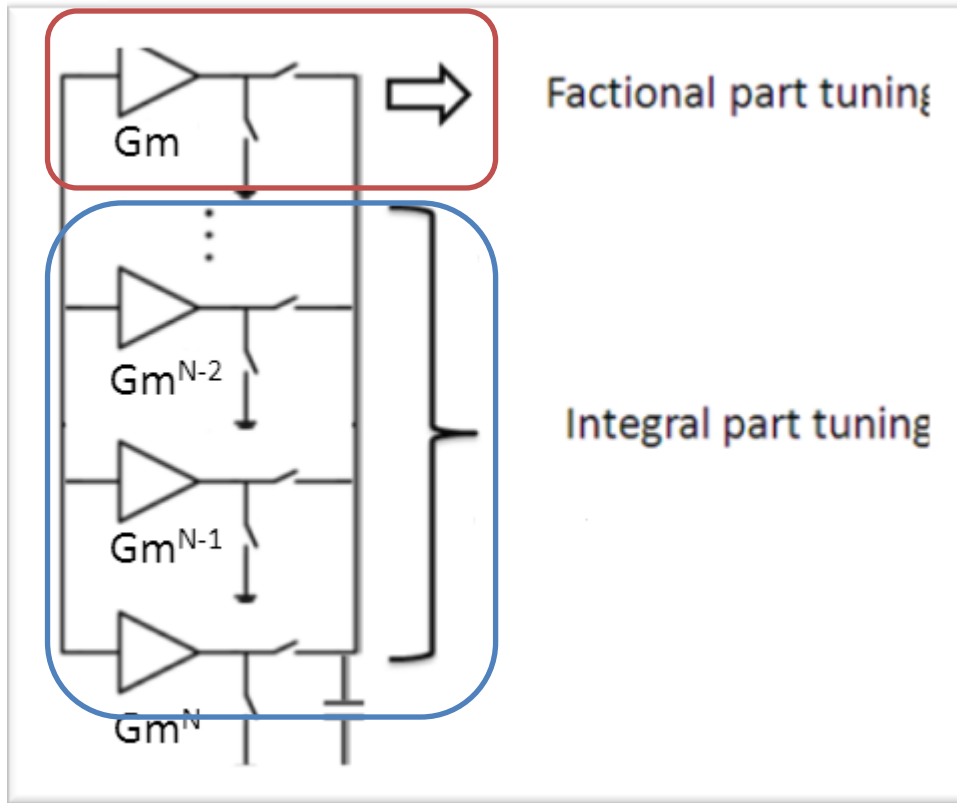


Continuous Adjustment of Switched Gm-C Integrator

Noise characteristics



$C \rightarrow \text{Constant}$
 $G_m \rightarrow \text{Adjustable}$



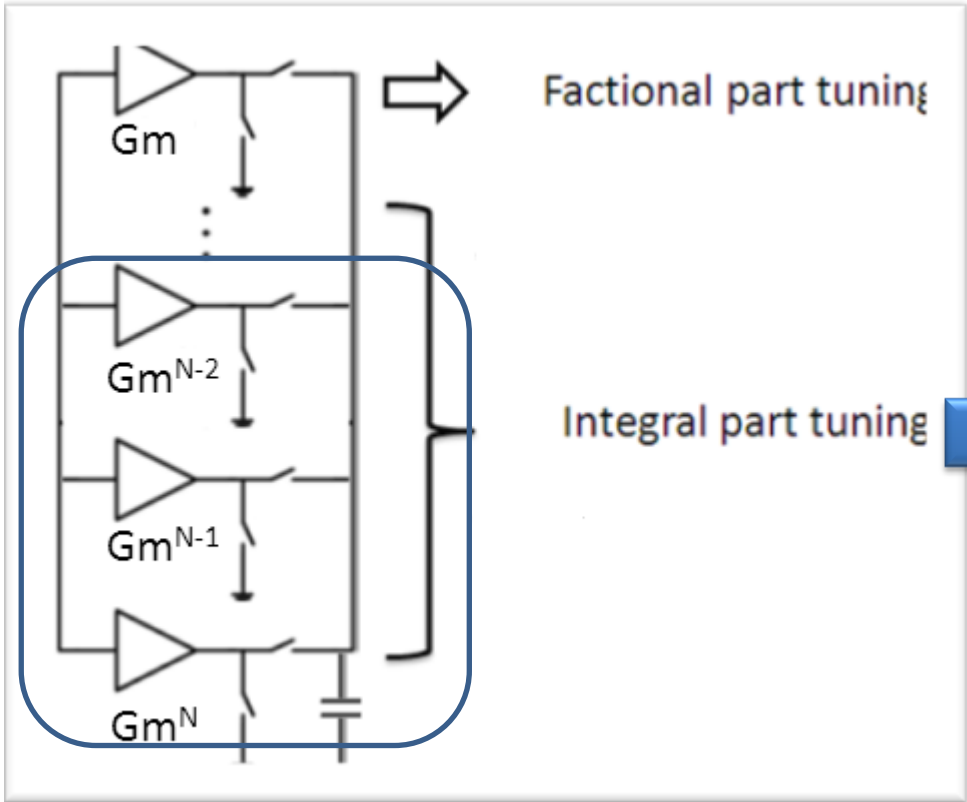
Continuous Adjustment

Switched Gm-C Integrator

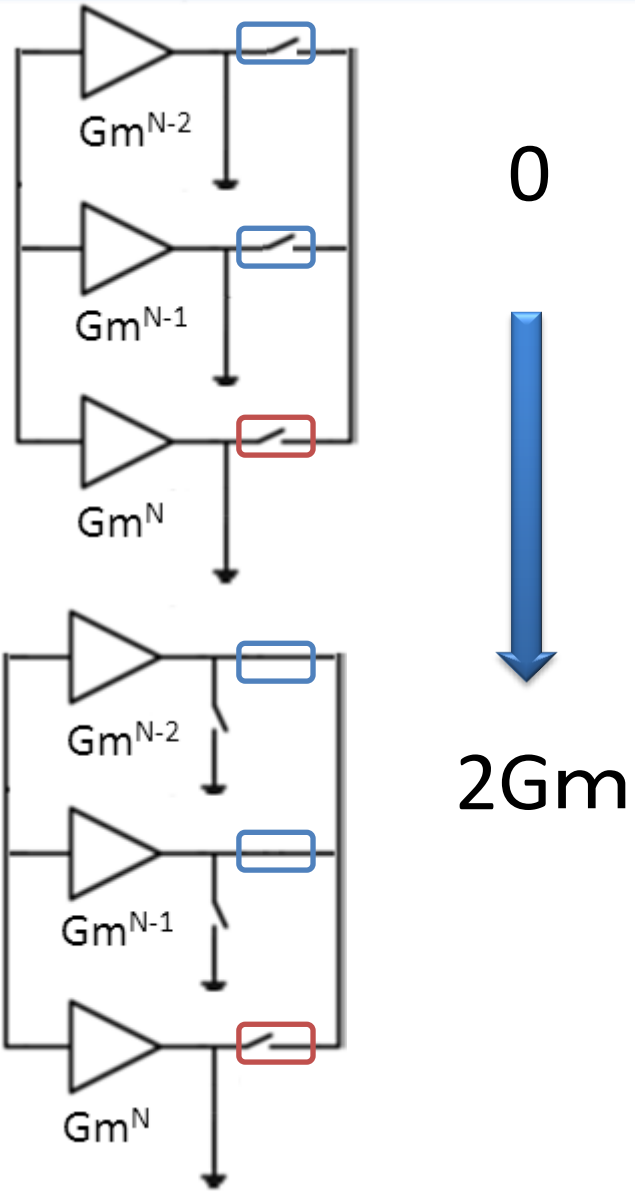


- Low-voltage
- Digital control

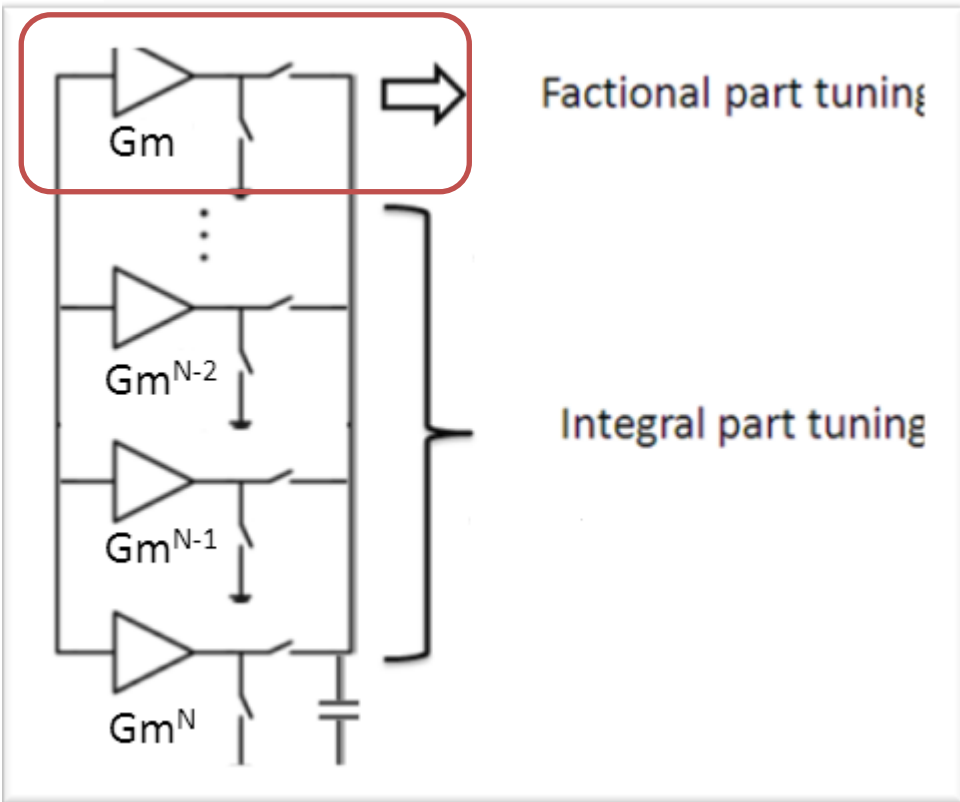
Integral Part Adjusting



Switched Gm-C Integrator



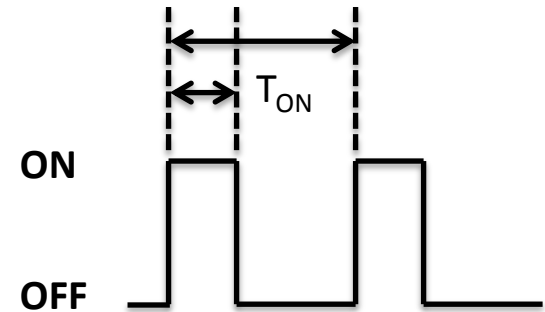
Adjust Fractional Part by PWM



Switched Gm-C Integrator



Tr

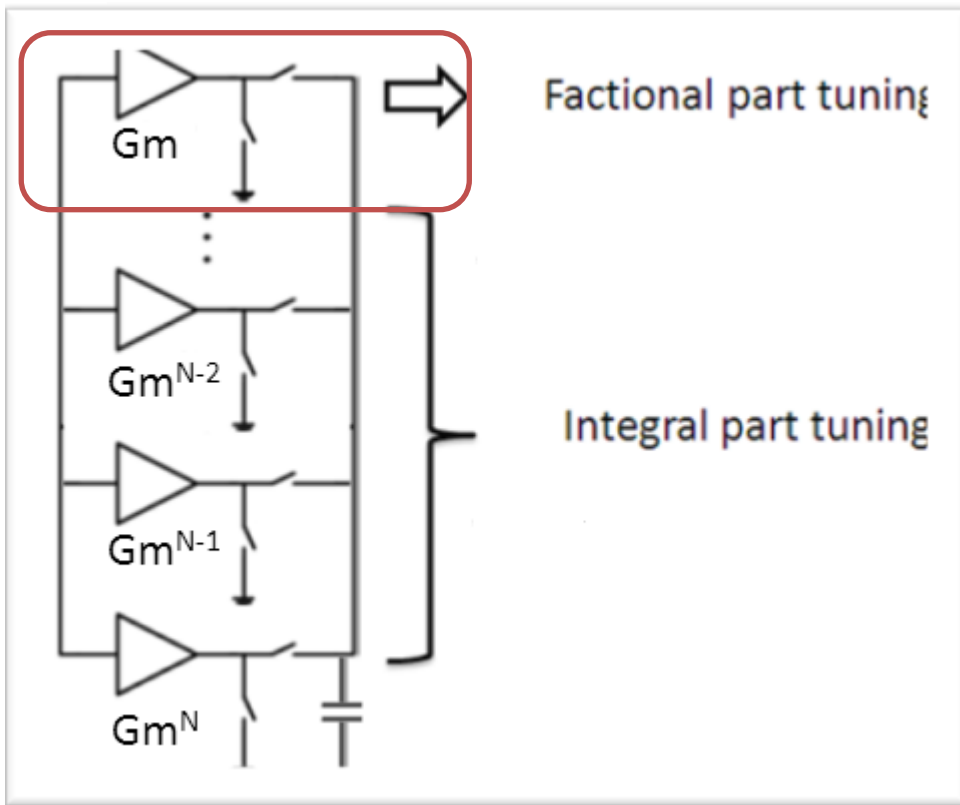


$$\frac{I_{out}}{V_{in}} = D \cdot Gm$$

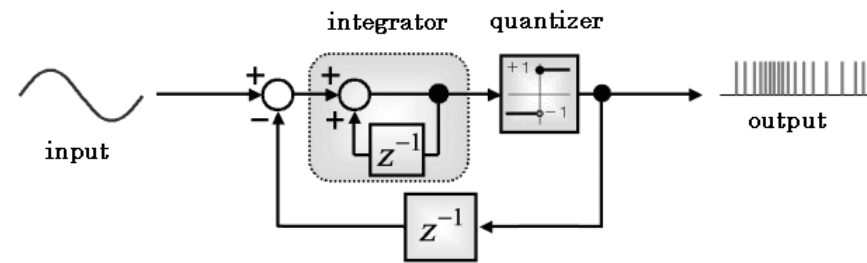
$$D = \frac{T_{on}}{T_{on} + T_{off}}$$

PWM control

Adjust Fractional Part by $\Delta\Sigma$



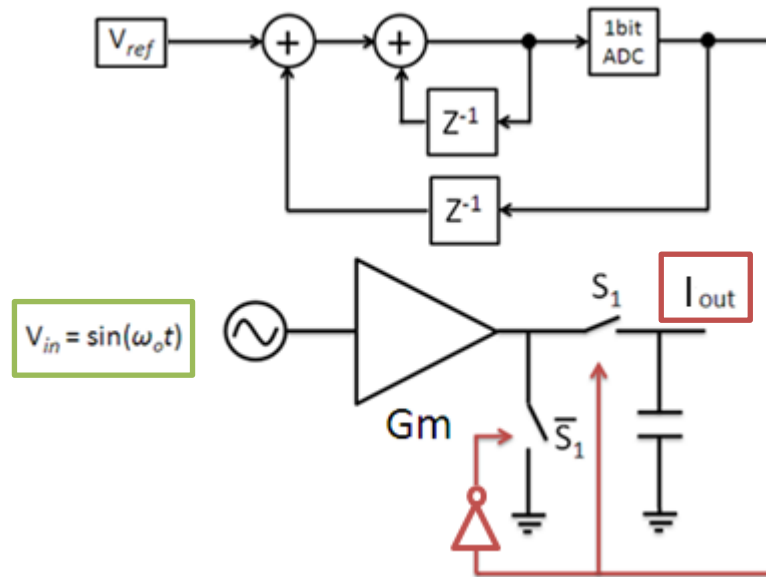
1bit $\Delta\Sigma$ converter for high accuracy



1bit $\Delta\Sigma$ converter

Switched Gm-C Integrator

Input and Output Waveforms of Switched Gm-C Integrator



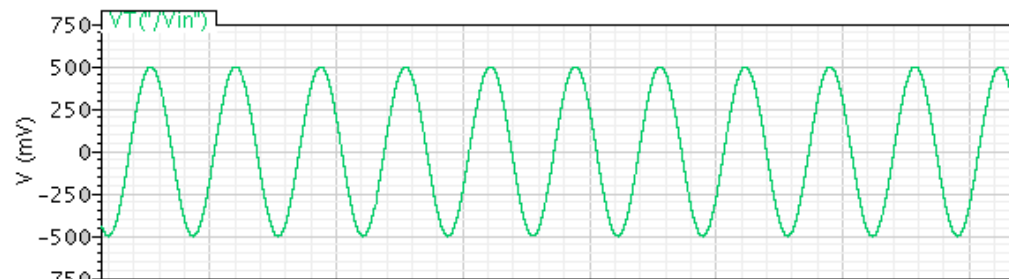
$$V_{in} = 500mV$$

$$f = 598kHz$$

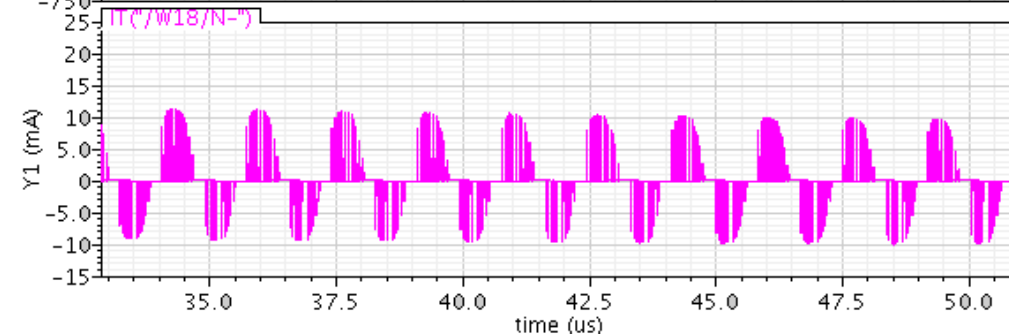
$$C = 1pF$$

$$1/G_m = 2 \times 10^6 S$$

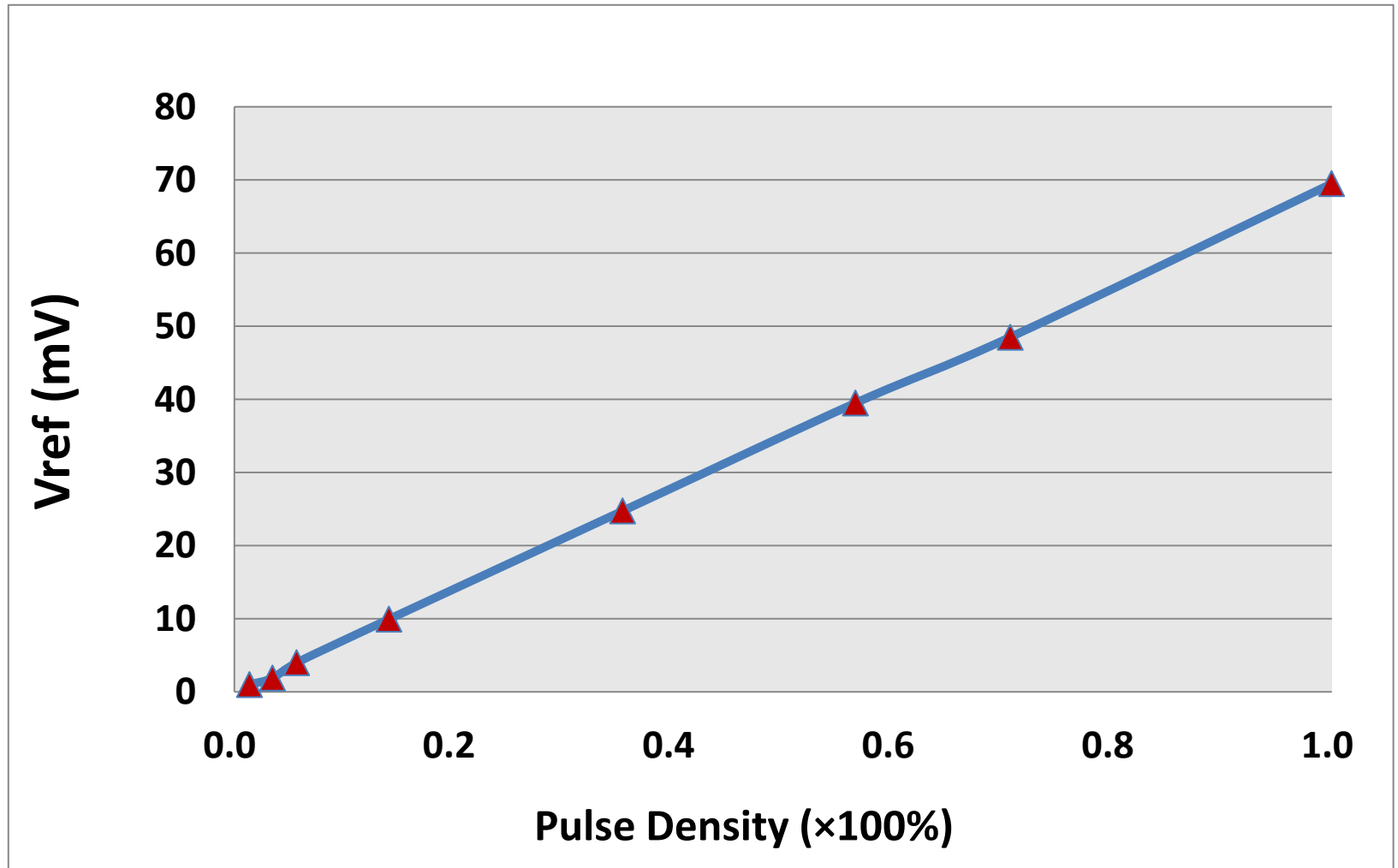
V_{in}



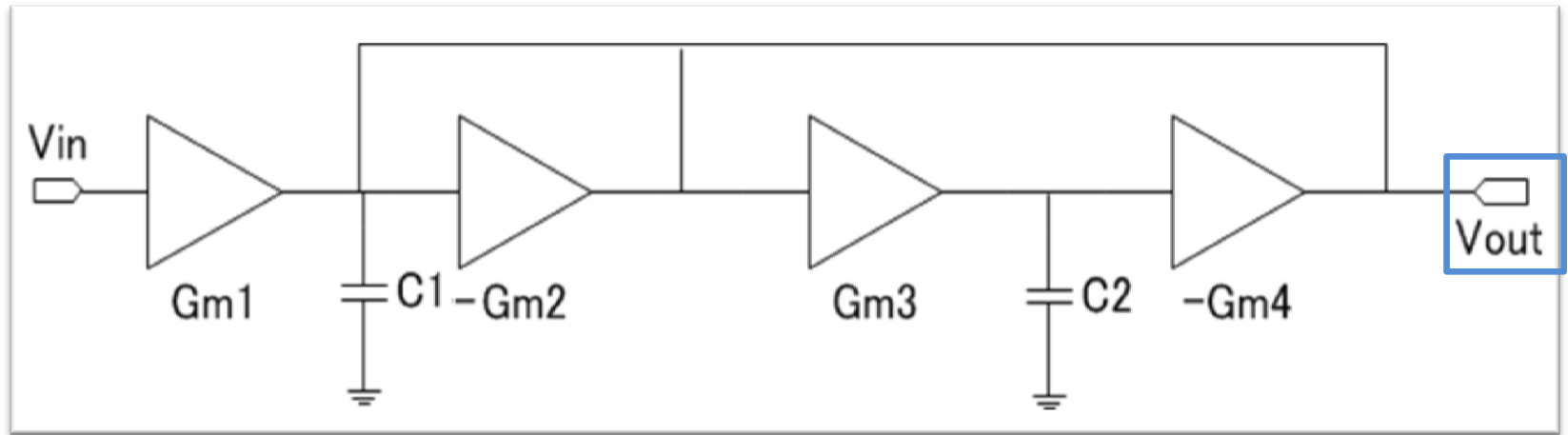
I_{out}



Input Voltage Amplitude and Pulse Density with $\Delta\Sigma$ Adjustment



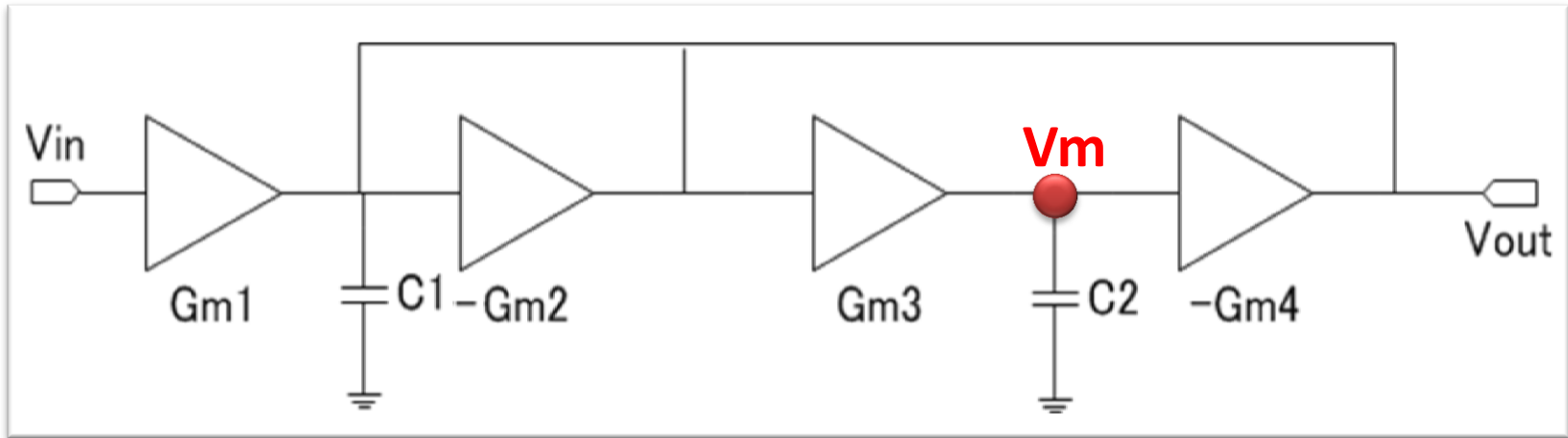
Gm-C Second-order BPF



$$H(s) = \frac{Gm_1 C_2 s}{s^2 C_1 C_2 + s C_2 Gm_2 + Gm_3 Gm_4} \quad \Rightarrow \quad H(s) = \frac{K \omega_0 s}{s^2 + \frac{\omega_0}{Q} s + \omega_0^2}$$

$$\omega_0 = \sqrt{\frac{Gm_3 Gm_4}{C_1 C_2}} \quad Q = \sqrt{\frac{C_1 Gm_3 Gm_4}{C_2 Gm_2^2}} \quad K = \sqrt{\frac{C_2 Gm_1^2}{C_1 Gm_3 Gm_4}}$$

Gm-C Second-order LPF

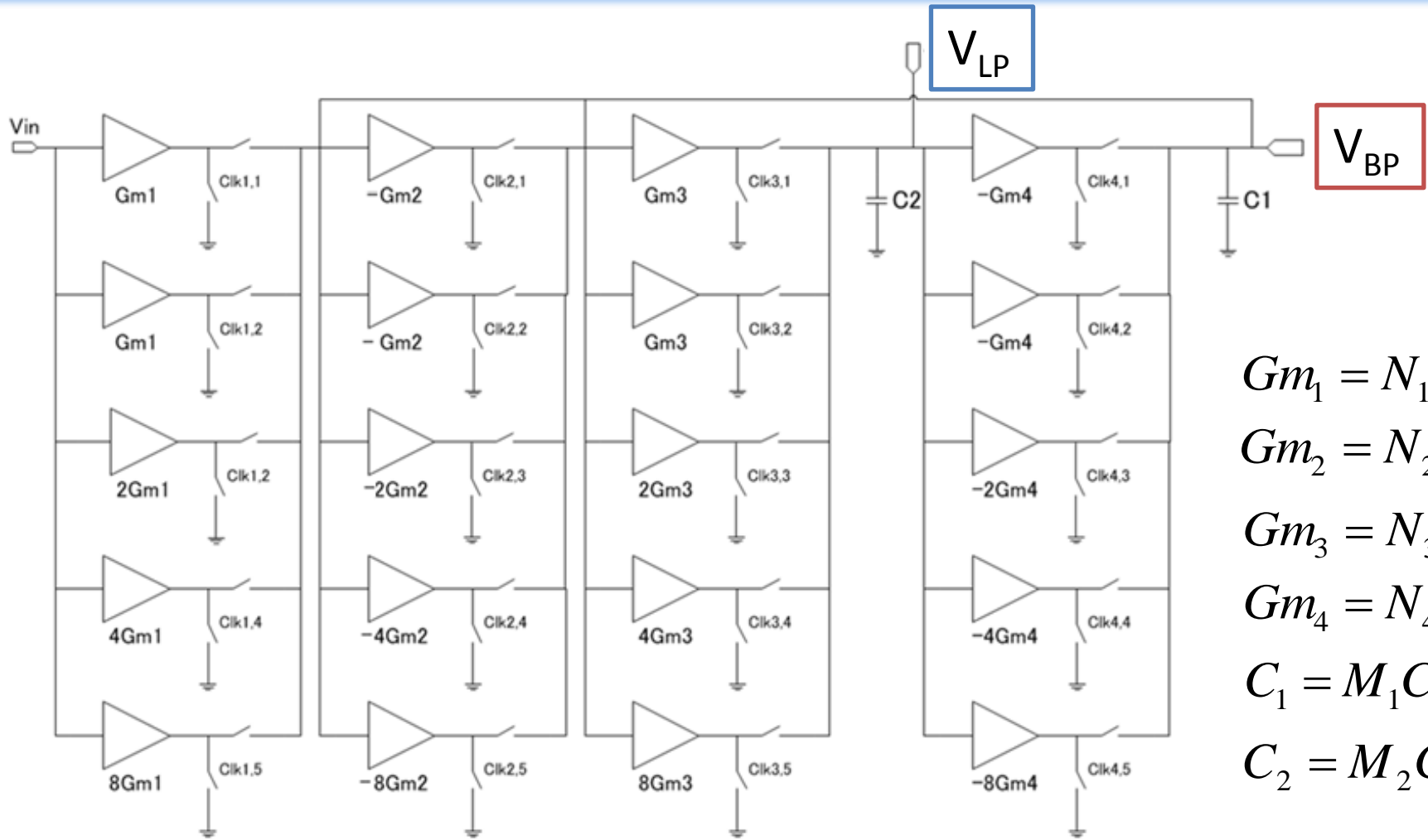


Another node of the filter

$$H''(s) = \frac{V_m}{V_{in}} = \frac{Gm_1 Gm_3}{s^2 C_1 C_2 + s C_2 Gm_2 + Gm_3 Gm_4} \quad \rightarrow \quad H(s) = \frac{K \omega_0}{s^2 + \frac{\omega_0}{Q} s + \omega_0^2}$$

$$\omega_0 = \sqrt{\frac{Gm_3 Gm_4}{C_1 C_2}} \quad Q = \sqrt{\frac{C_1 Gm_3 Gm_4}{C_2 Gm_2^2}} \quad K = \sqrt{\frac{Gm_1^2 Gm_3}{C_1 C_2 Gm_4}}$$

Proposed Digitally-controllable BPF and LPF



$$Gm_1 = N_1 gm$$

$$Gm_2 = N_2 gm$$

$$Gm_3 = N_3 gm$$

$$Gm_4 = N_4 gm$$

$$C_1 = M_1 C$$

$$C_2 = M_2 C$$

$$\omega_0 = \frac{N_3 N_4}{\sqrt{M_1 M_2}} \cdot \frac{gm}{C}$$

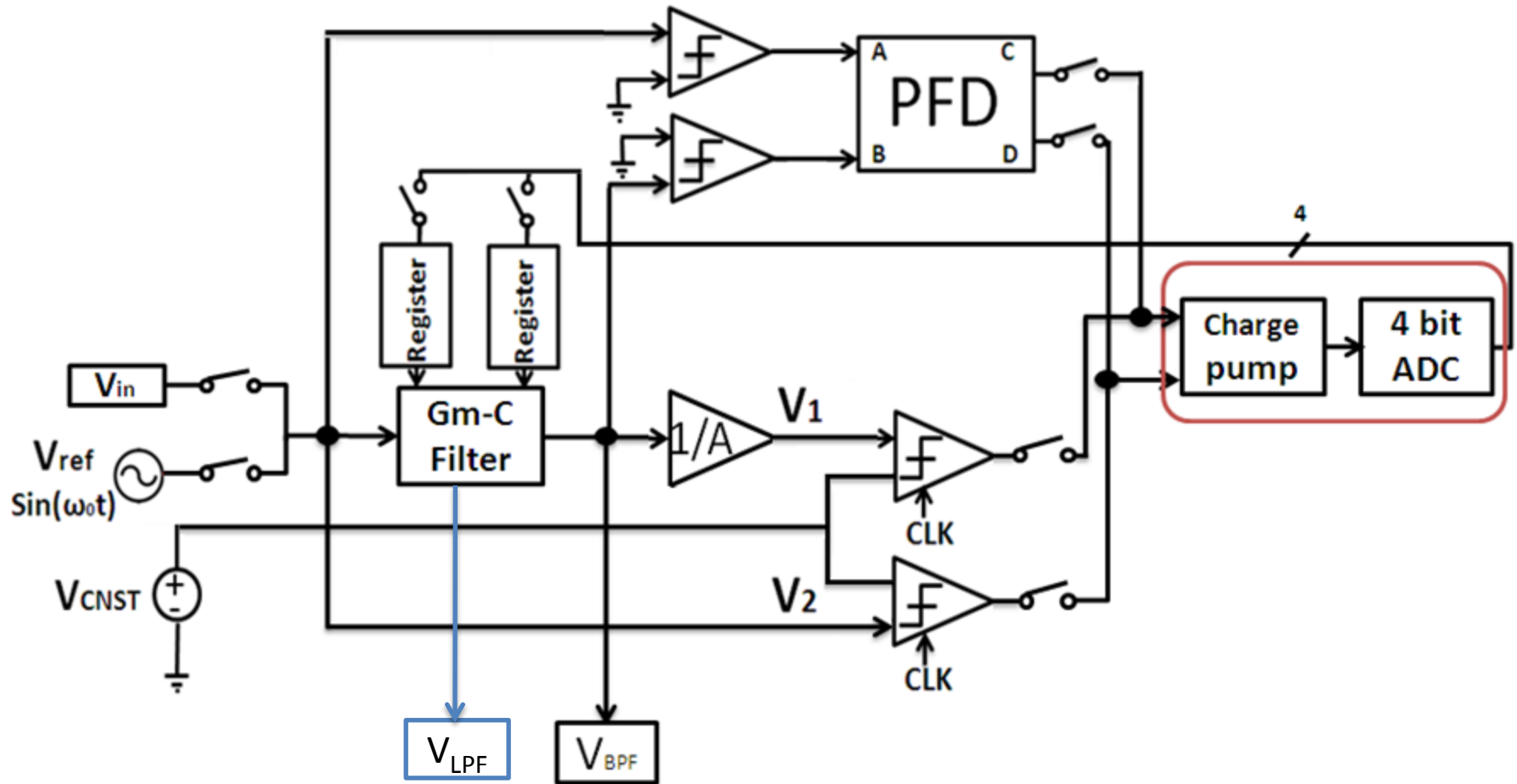
$$Q = \sqrt{\frac{M_1 N_3 N_4}{M_2 N_2^2}}$$

$$K = \sqrt{\frac{M_2 N_1^2}{M_1 N_3 N_4}}$$

Outline

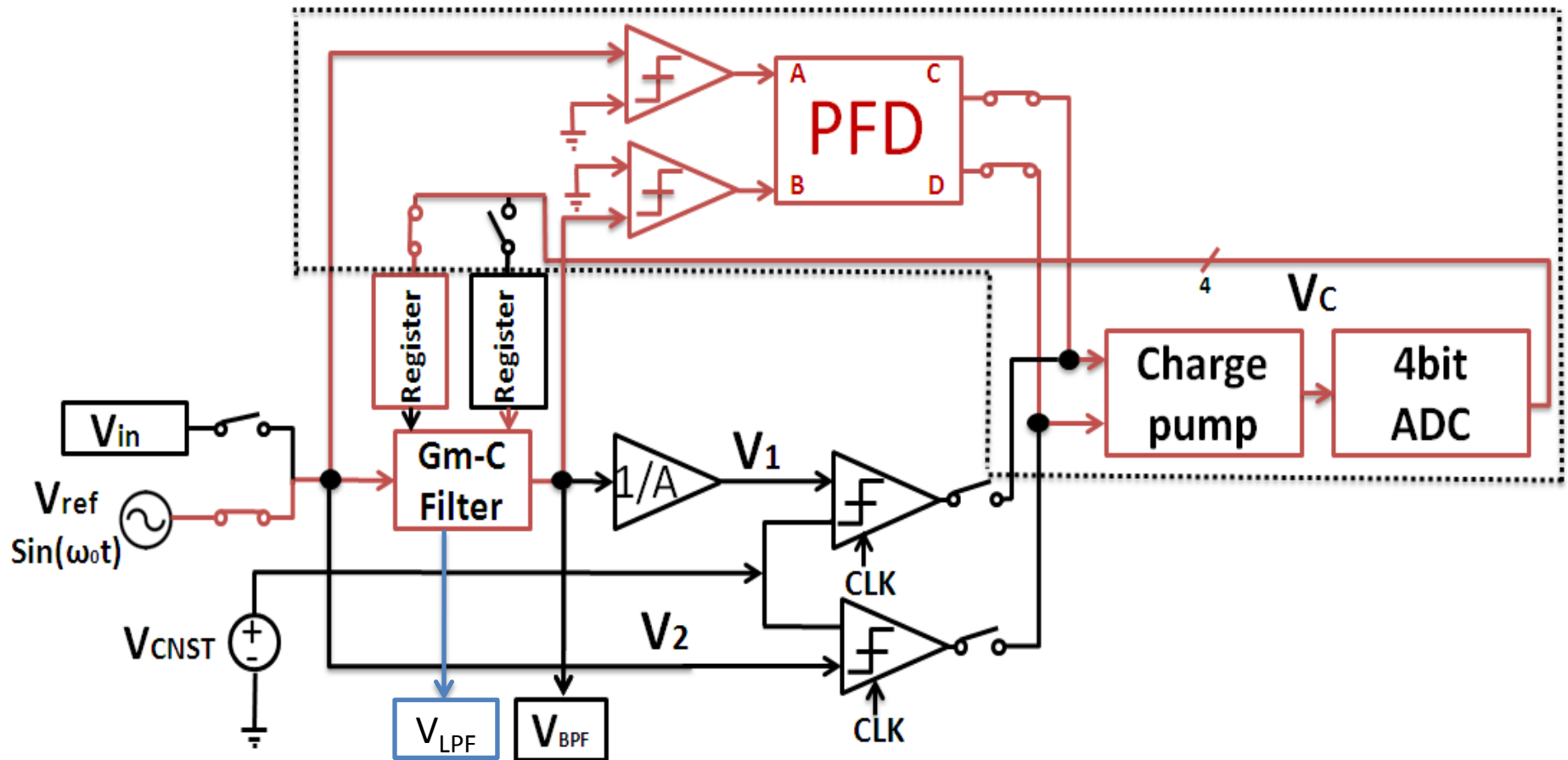
- *Research Objective*
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- *Center Frequency Tuning*
- *Q-Value Tuning*
- *Conclusion*

Whole Tuning Scheme

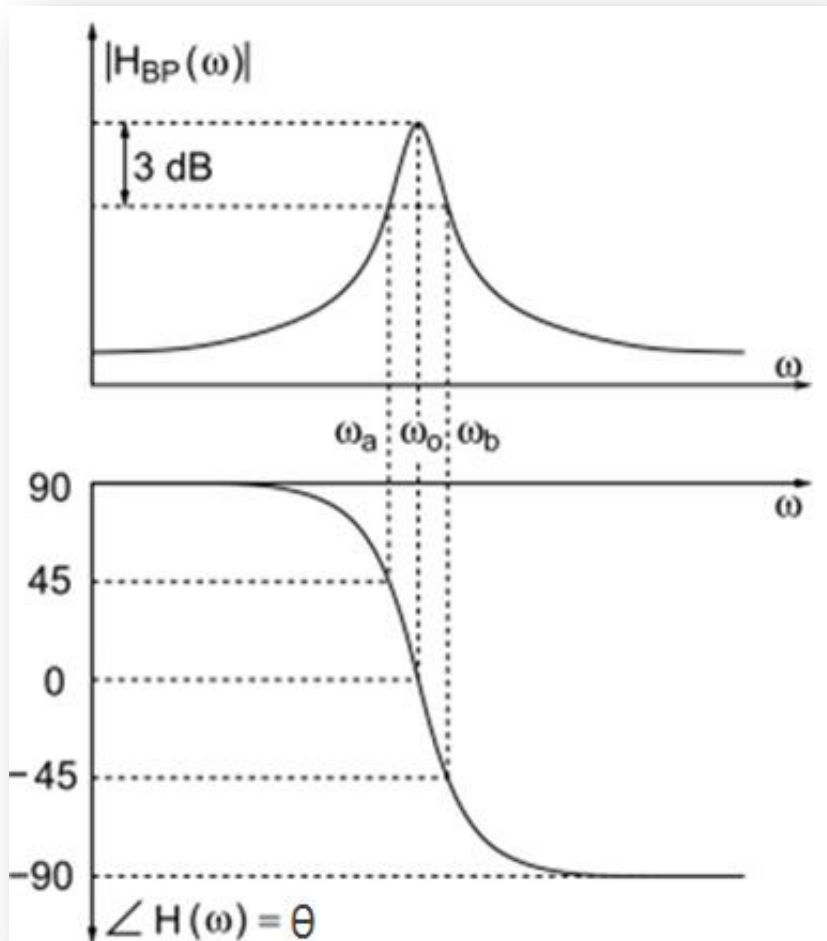


- Suitable for digital low voltage implement
- Require a reference frequency

Center Frequency Tuning Part



Proposed Center Frequency Tuning Method



Magnitude characteristics

$$H(s) = \frac{Gm_1 C_2 s}{s^2 C_1 C_2 + s C_2 Gm_2 + Gm_3 Gm_4}$$

Phase characteristics

$$\theta = \frac{\pi}{2} - \arctan \frac{\omega_i \omega_0}{Q(\omega_0^2 - \omega_i^2)}$$

ω_0 : Center Frequency
 ω_i : Input Frequency

$\theta = 0 \rightarrow$ Center frequency tuning is done

Principle for Using Phase Characteristics

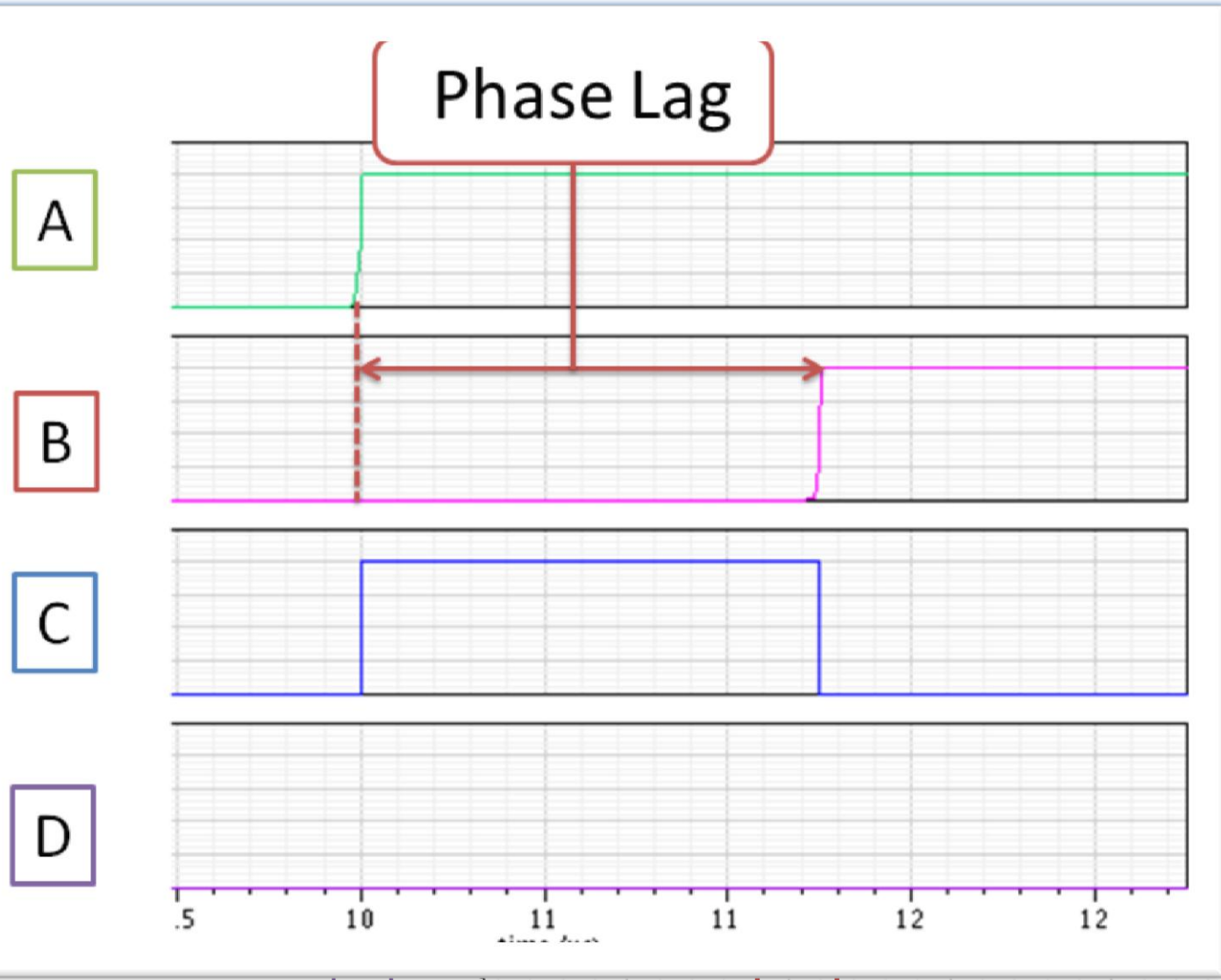
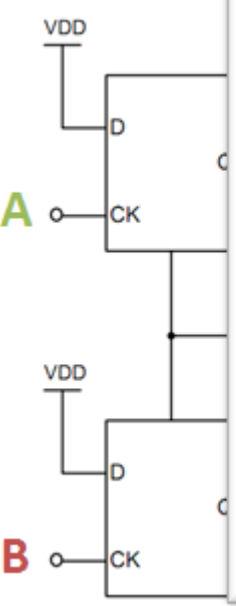
$$\theta = \frac{\pi}{2} - \arctan \frac{\omega \omega_0}{Q(\omega_0^2 - \omega_i^2)} \quad \rightarrow \quad \omega_0 = \sqrt{\frac{Gm_3 Gm_4}{C_1 C_2}}$$

- ① $\theta < 0 \rightarrow \omega_0 < \omega_i$ Gm_3, Gm_4 **bigger** \rightarrow ω_0 is adjusted **bigger**
- ② $\theta = 0 \rightarrow \omega_0 = \omega_i$ Done
- ③ $\theta > 0 \rightarrow \omega_0 > \omega_i$ Gm_3, Gm_4 **smaller** \rightarrow ω_0 is adjusted **smaller**

Signals of PFD

$V_{ref} = \sin(\omega t)$

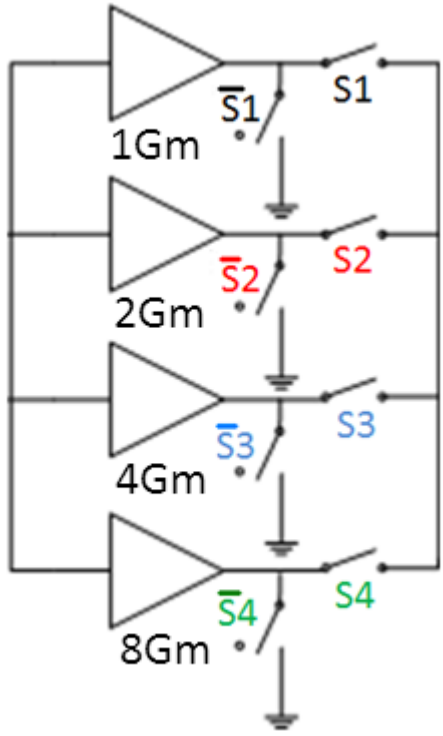
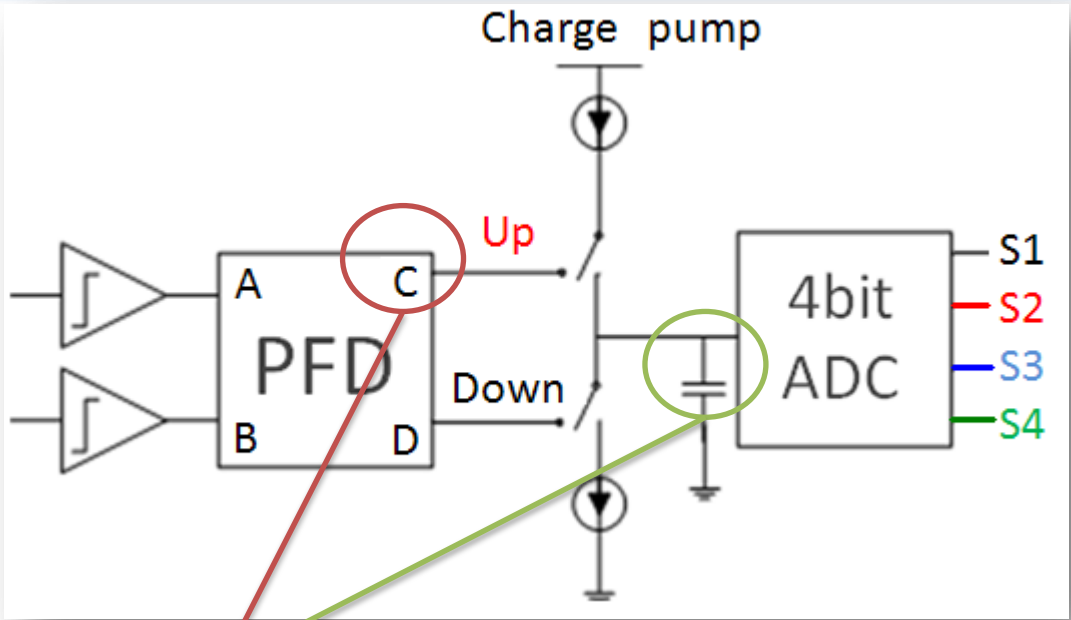
ω_0 is desired center frequency



4bit ADC

PFD(Phase Frequency Detector)

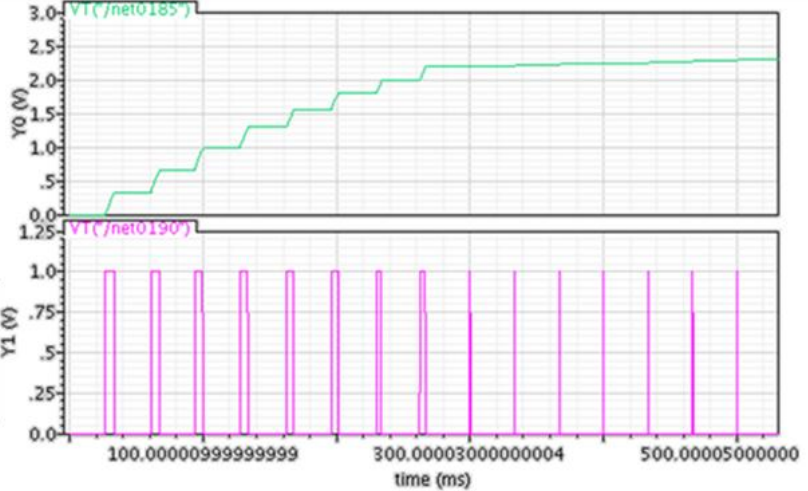
Operation of Charge Pump



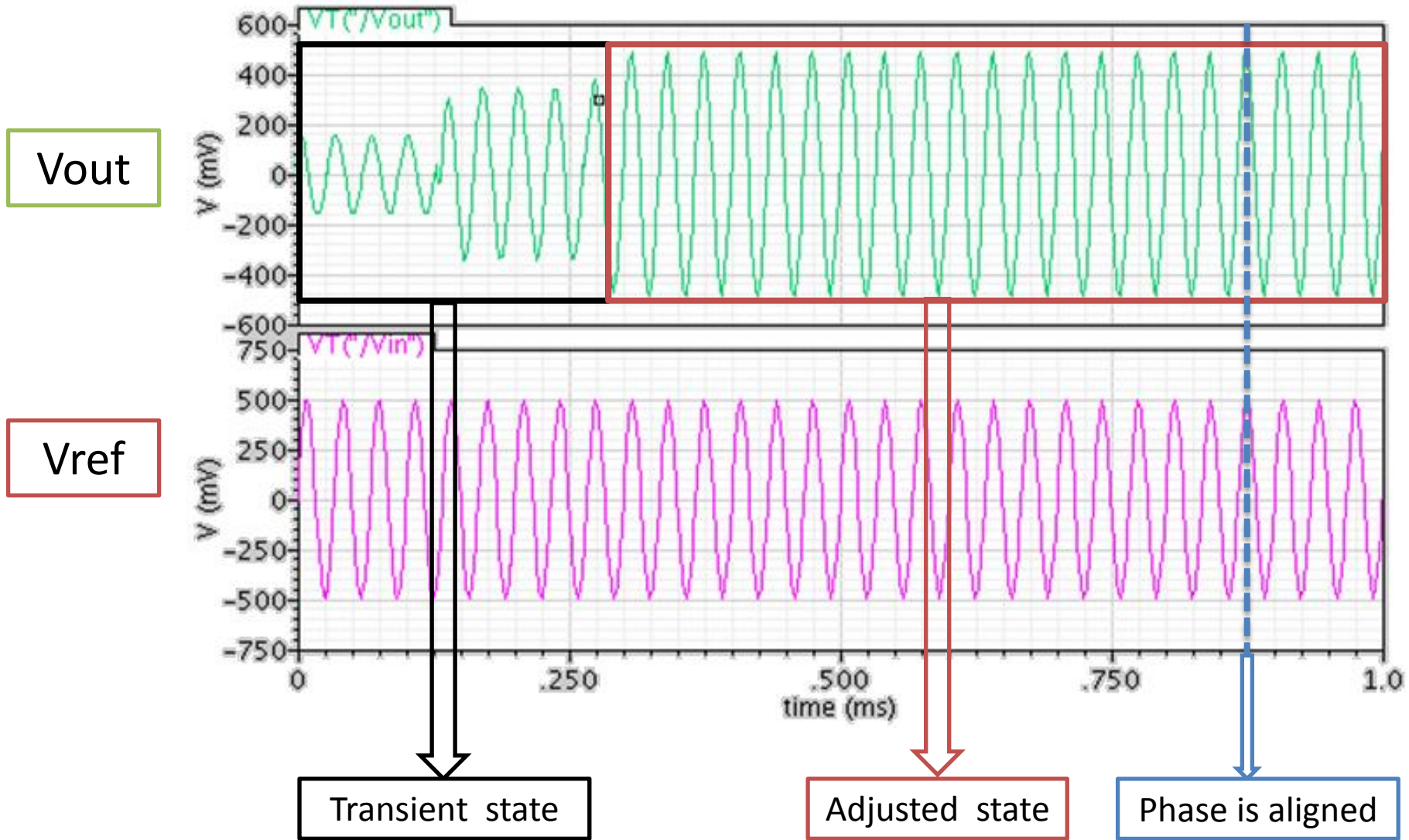
Adjust the Gm_3, Gm_4 values

Output of charge pump V_C

Output of PFD



Input and Output Waves of BPF

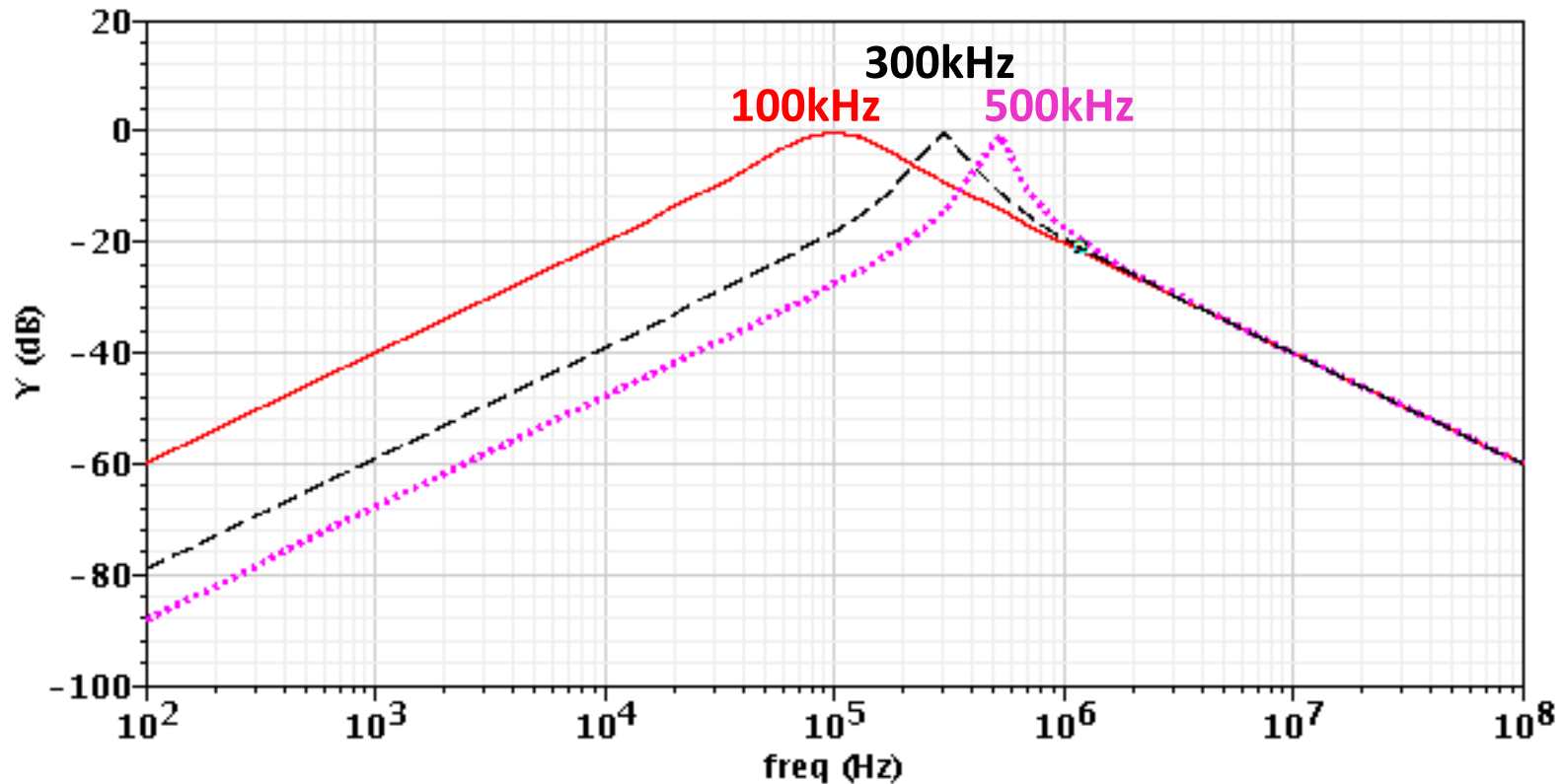


Center Frequency Tuning Simulation Result of BPF

Simulation
parameters

$$G_m = 5 \times 10^{-5} S \quad C = 1.59 \text{ pF}$$

$$N_1 = N_2 = 2 \quad M_1 = M_2 = 1 \quad 0 \leq N_3 = N_4 \leq 15$$

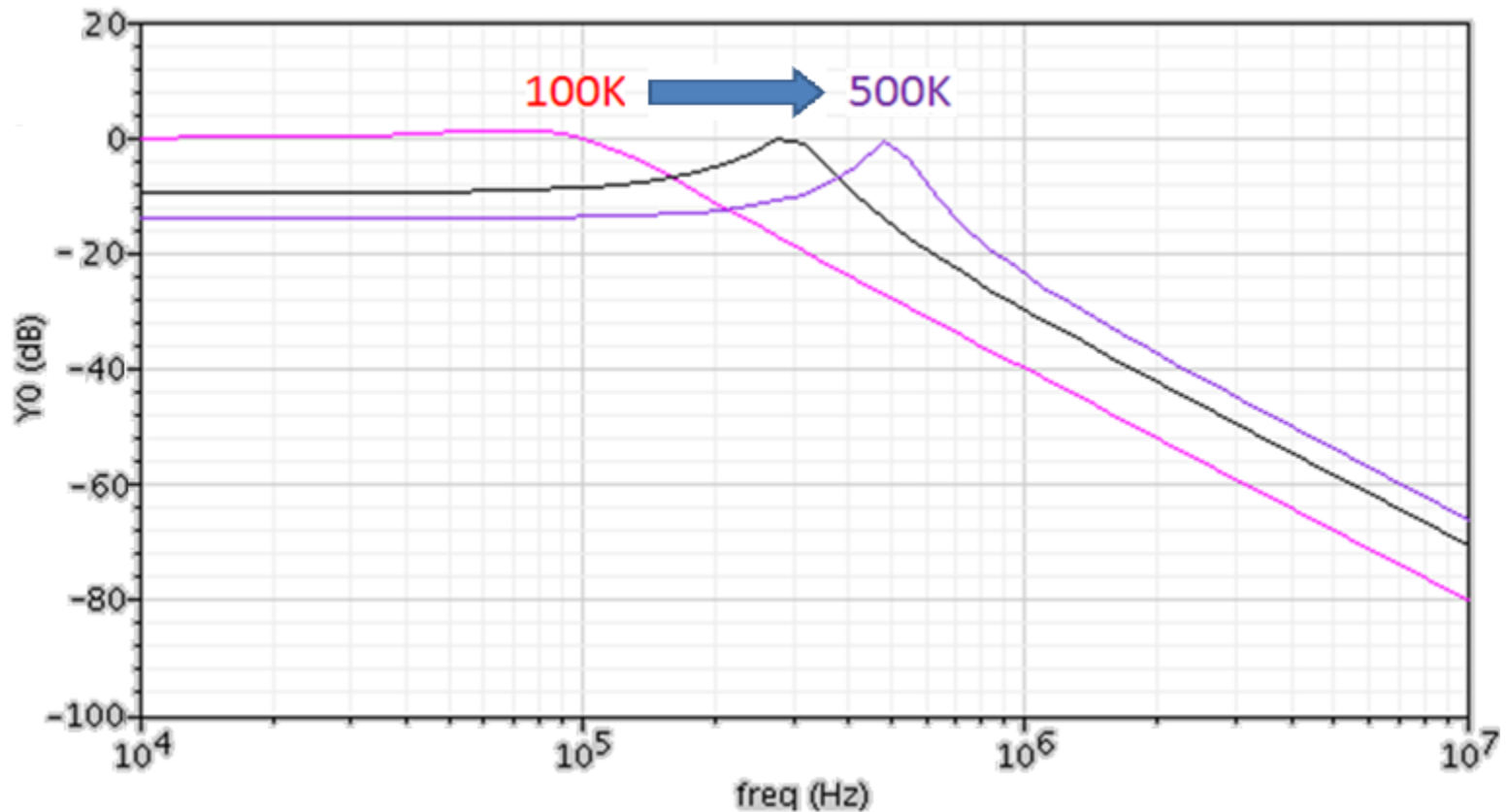


Center Frequency Tuning Simulation Results of LPF

Simulation
parameters

$$G_m = 5 \times 10^{-5} \text{ S} \quad C = 1.59 \text{ pF}$$

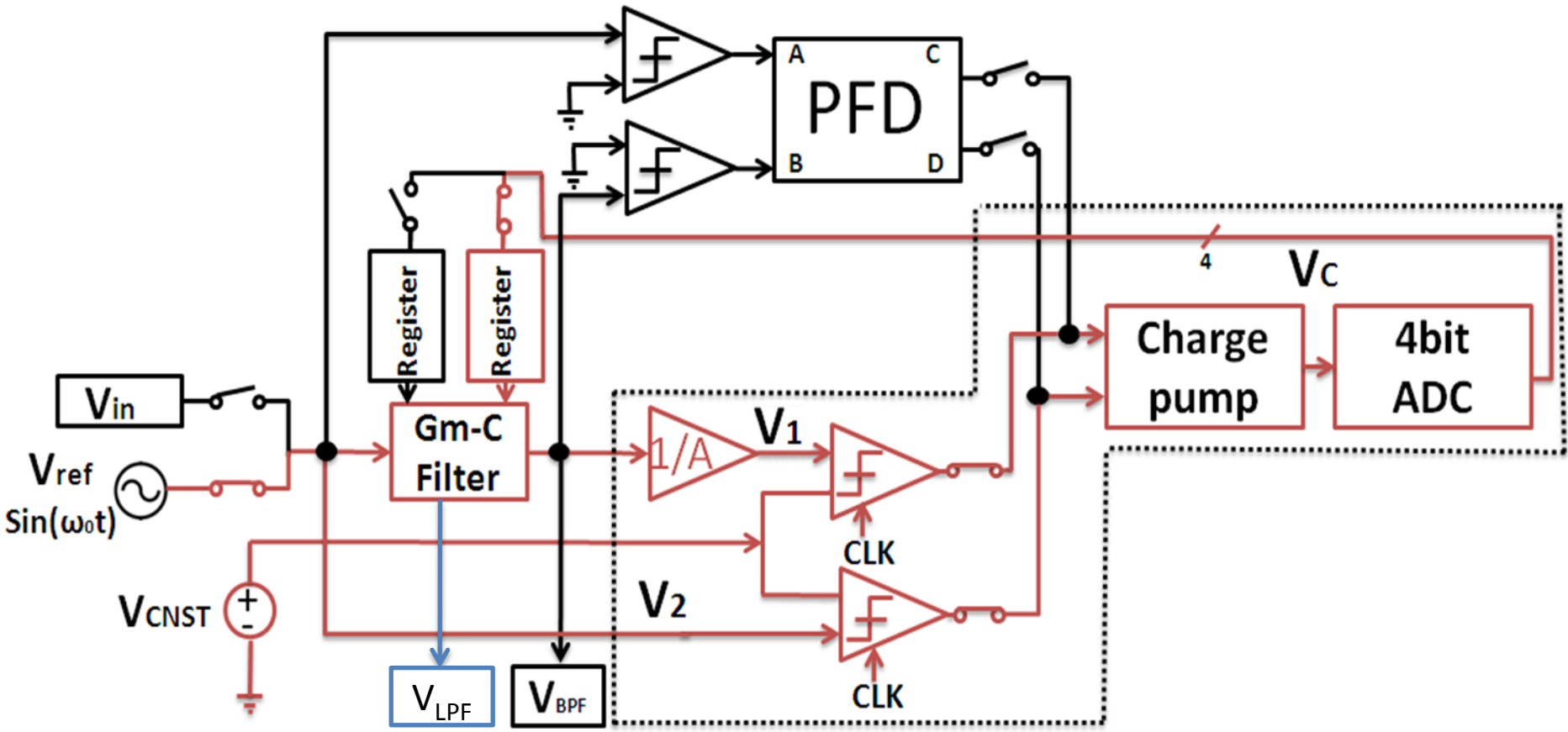
$$N_1 = N_2 = 2 \quad M_1 = M_2 = 1 \quad 0 \leq N_3 = N_4 \leq 15$$



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- *Research Objective*
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- *Center Frequency Tuning*
- **Q-Value Tuning**
- *Conclusion*

Q-value Tuning Part



Q-value Tuning Method

proposed method

$$H(\omega_0) = \frac{Gm_1}{Gm_2} = \sqrt{\frac{Gm_1^2 C_2}{Gm_3 Gm_4 C_1}} \cdot \sqrt{\frac{Gm_3 Gm_4 C_1}{Gm_2^2 C_2}} = KQ$$

ω_0 : Center frequency

$$\omega_0 = \sqrt{\frac{Gm_3 Gm_4}{C_1 C_2}} \quad Q = \sqrt{\frac{C_1 Gm_3 Gm_4}{C_2 Gm_2^2}} \quad K = \sqrt{\frac{Gm_1^2 Gm_3}{C_1 C_2 Gm_4}}$$

ω_0 determined by
 Gm_3, Gm_4

K determined by
 Gm_1, Gm_3, Gm_4

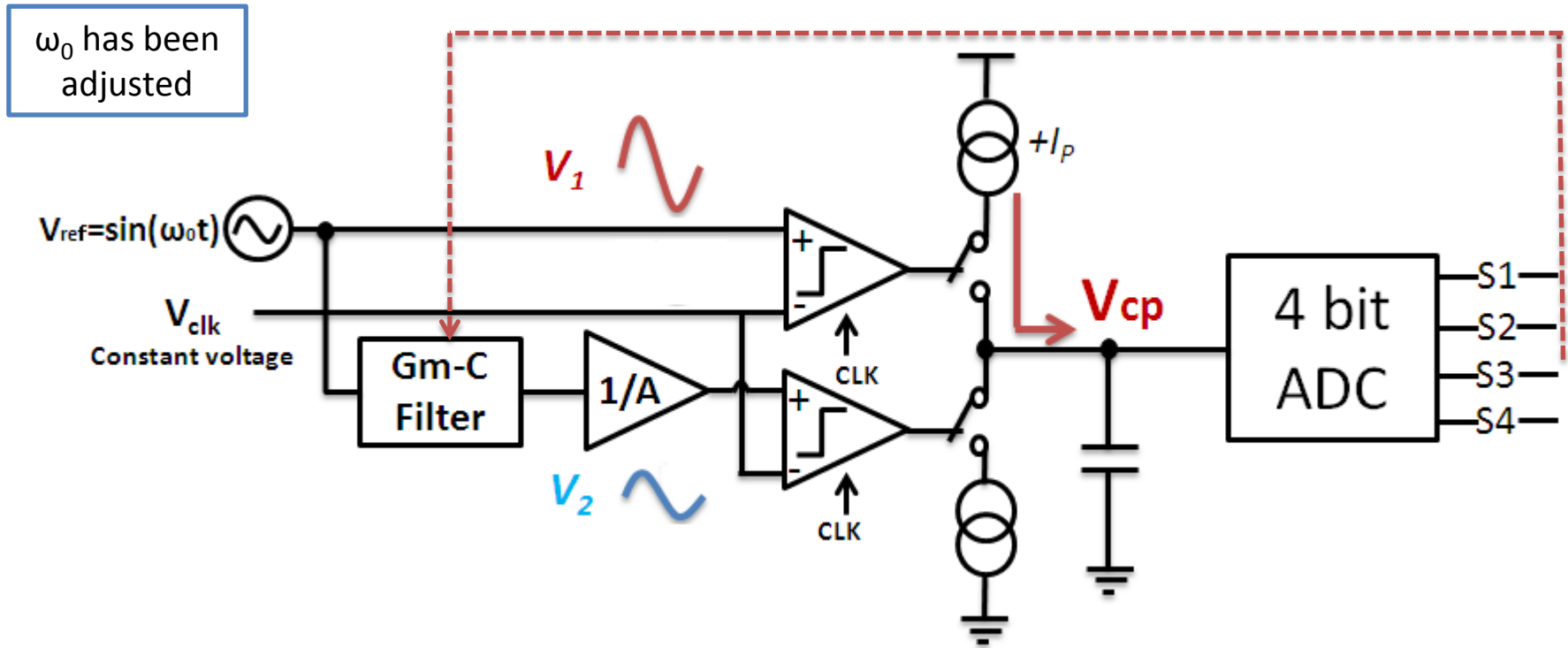
Fix Center frequency
and K



Q-value is proportional
to **gain**

$$|H(j\omega_0)| = K \cdot Q$$

In Case Q is Smaller than Desired Value



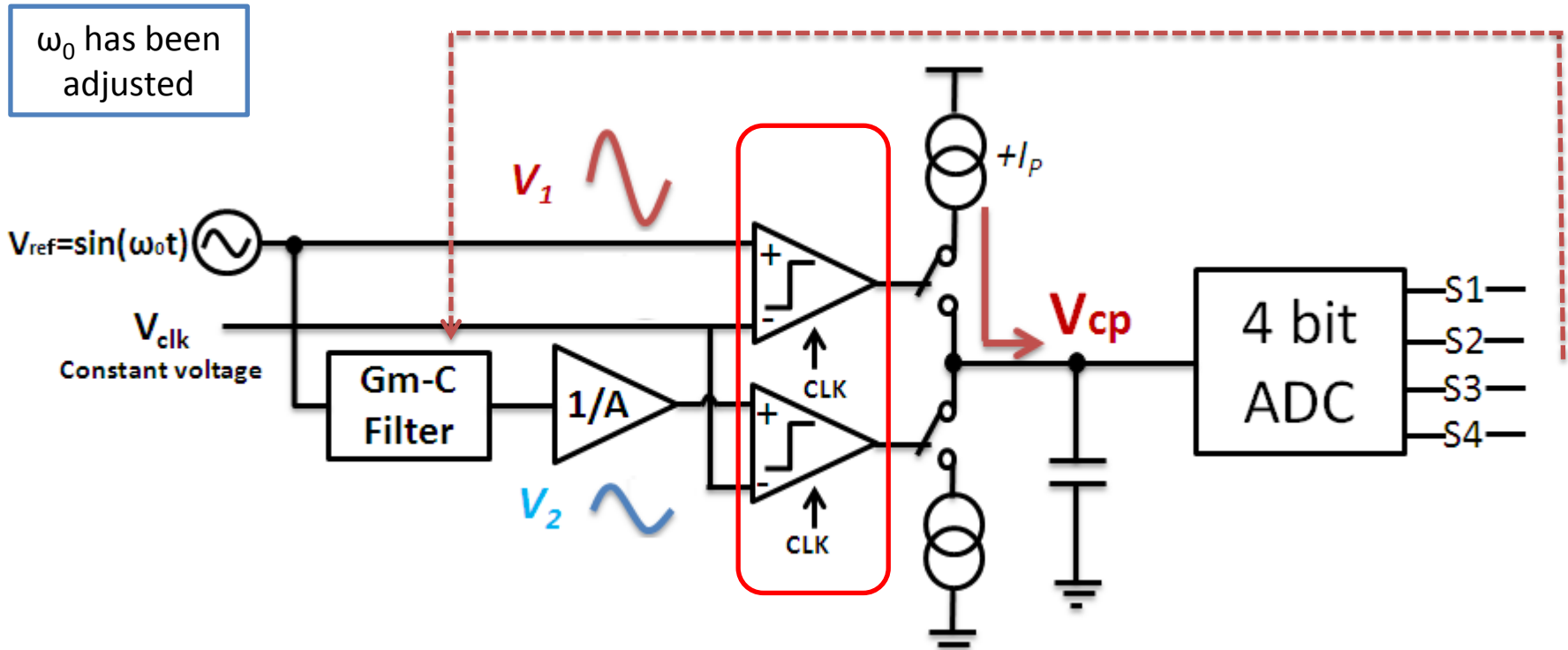
$V_1 > V_2$ \longrightarrow V_{cp} is tuned bigger \longrightarrow Gm_2 is tuned smaller

$$Q = \sqrt{\frac{C_1 Gm_3 Gm_4}{C_2 Gm_2^2}}$$

$$Q \propto (1/Gm_2)$$

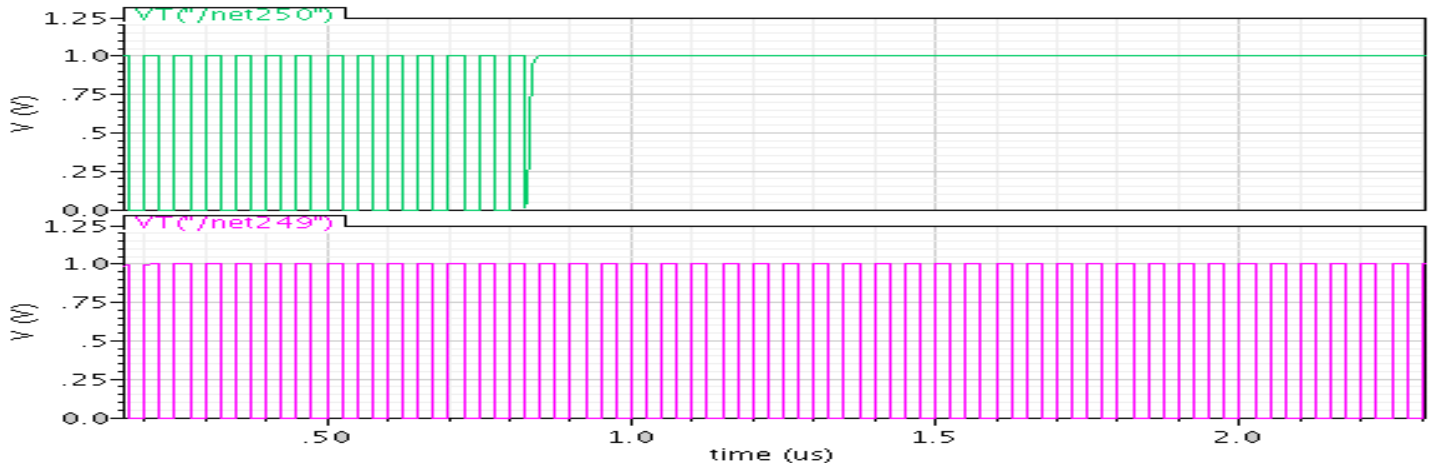
$Q \rightarrow$ bigger

Output of Comparator

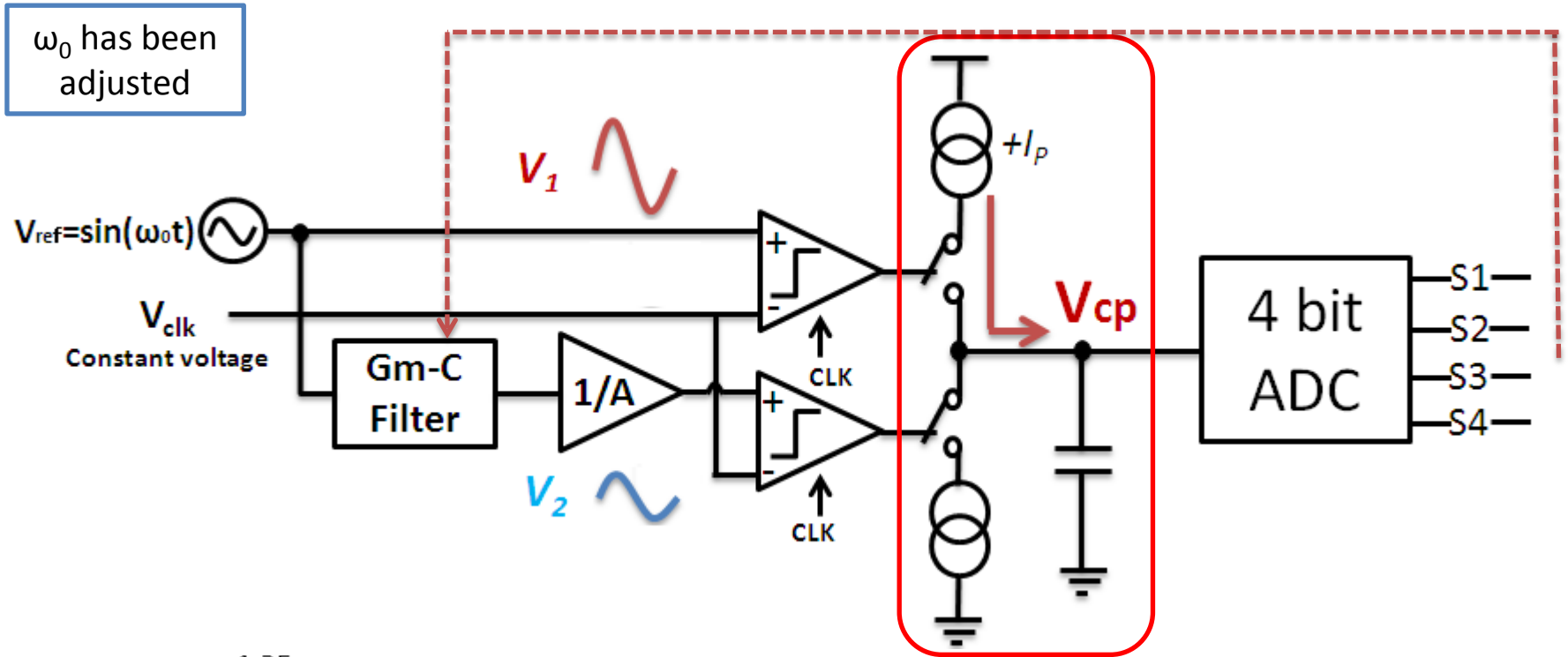


COMP_{UP}

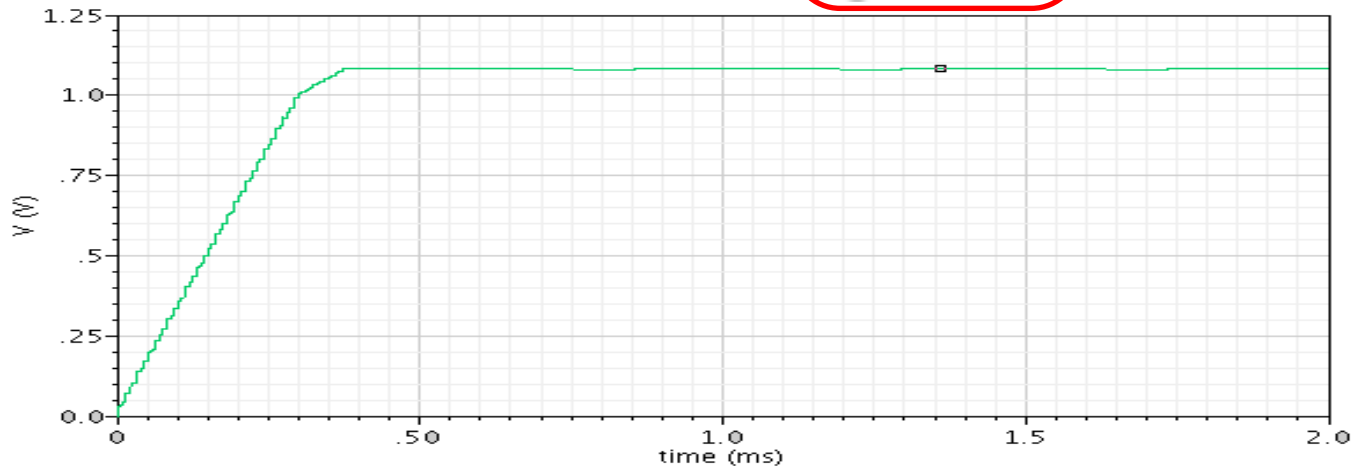
COMP_{DOWN}



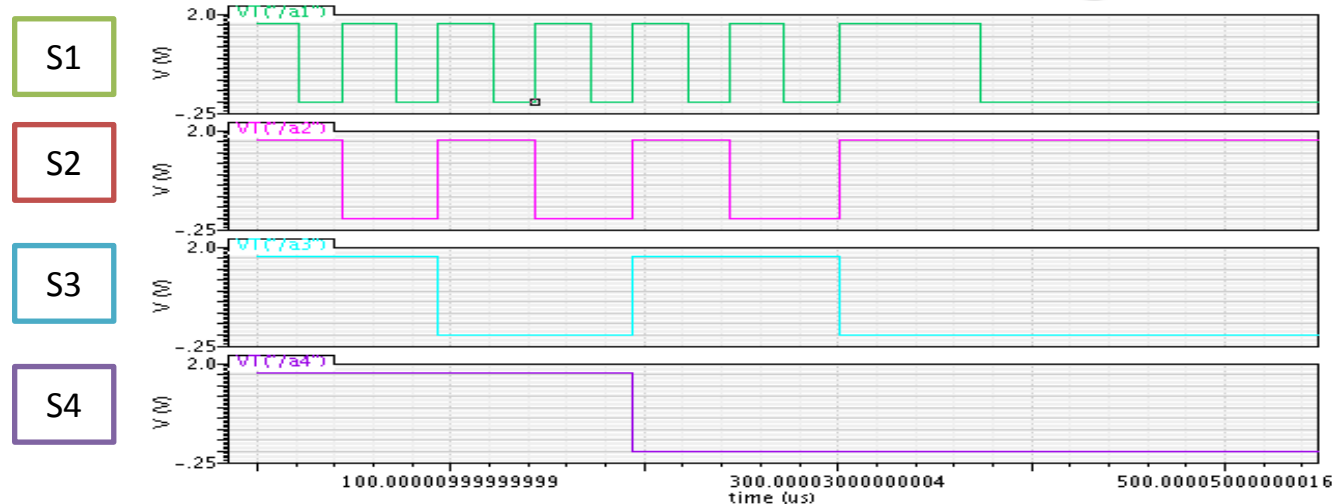
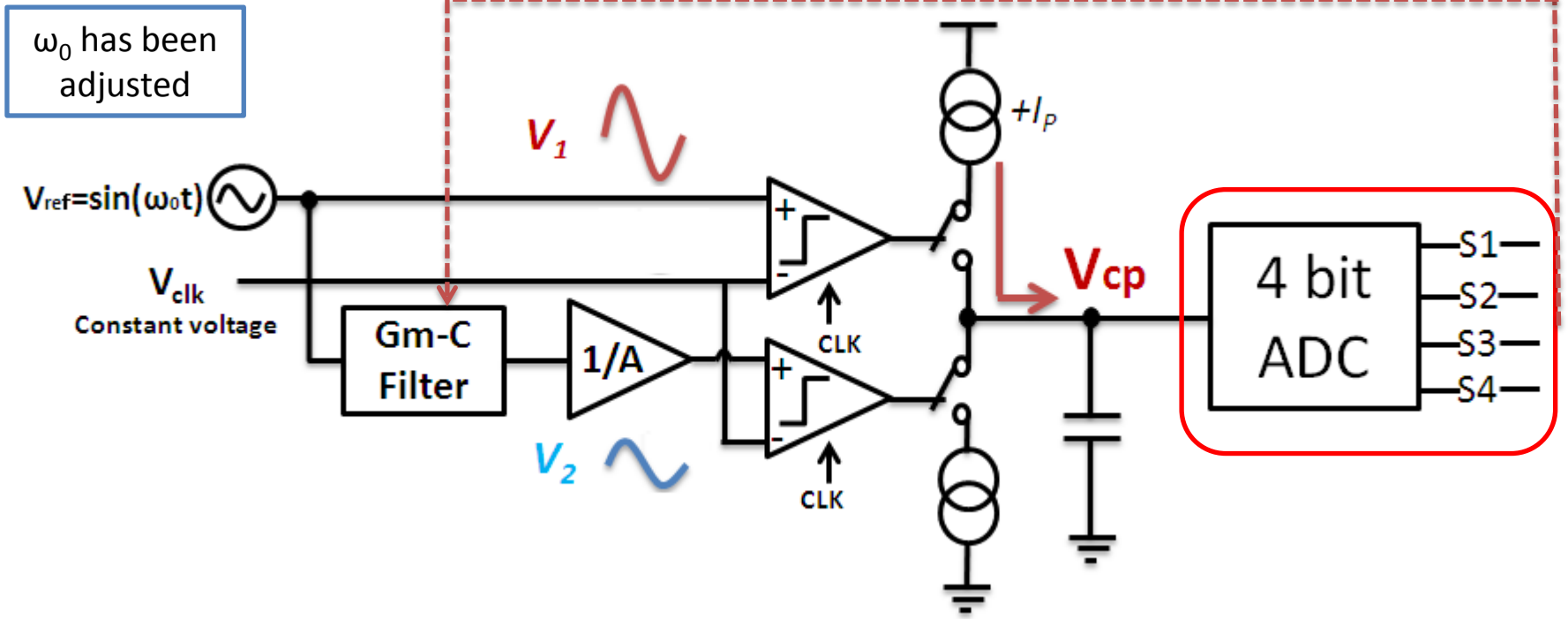
Output of Charge pump



V_{CP}

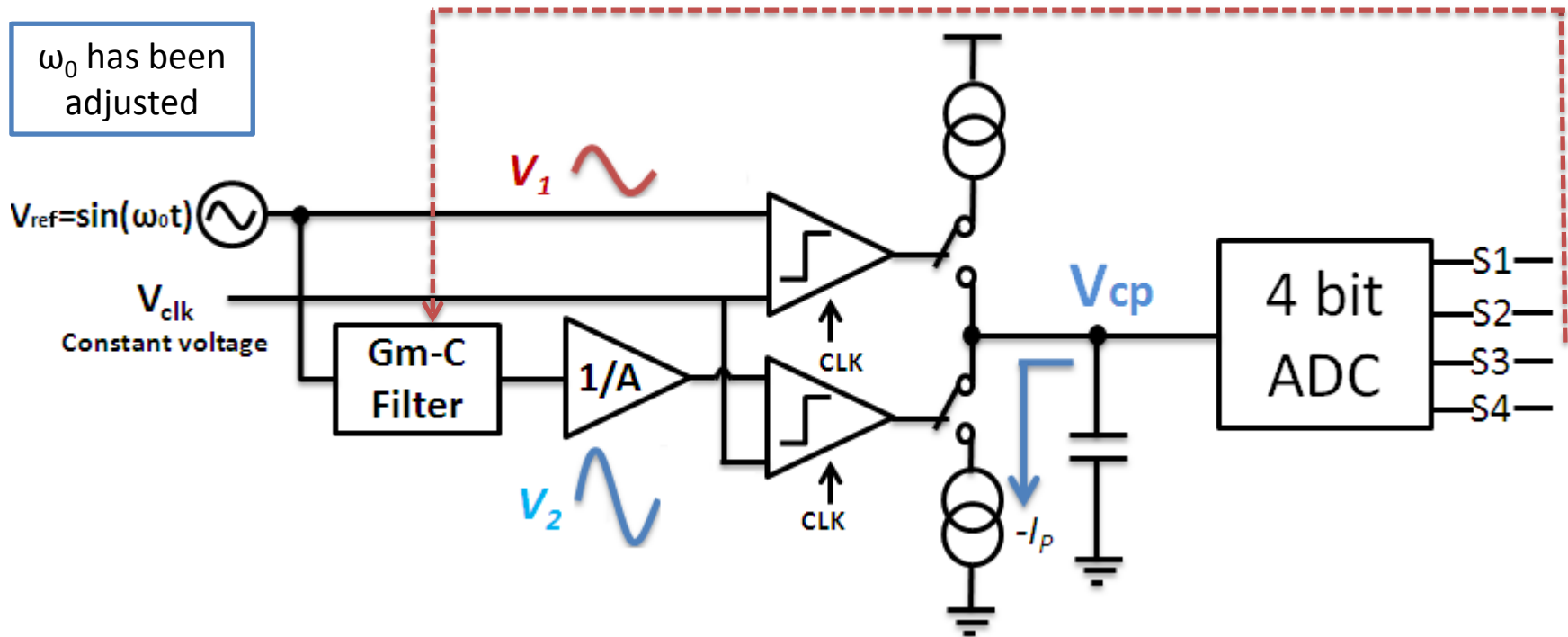


Output of ADC



$Q \infty (1/Gm_2)$

In Case Q is Bigger than the Desired Value



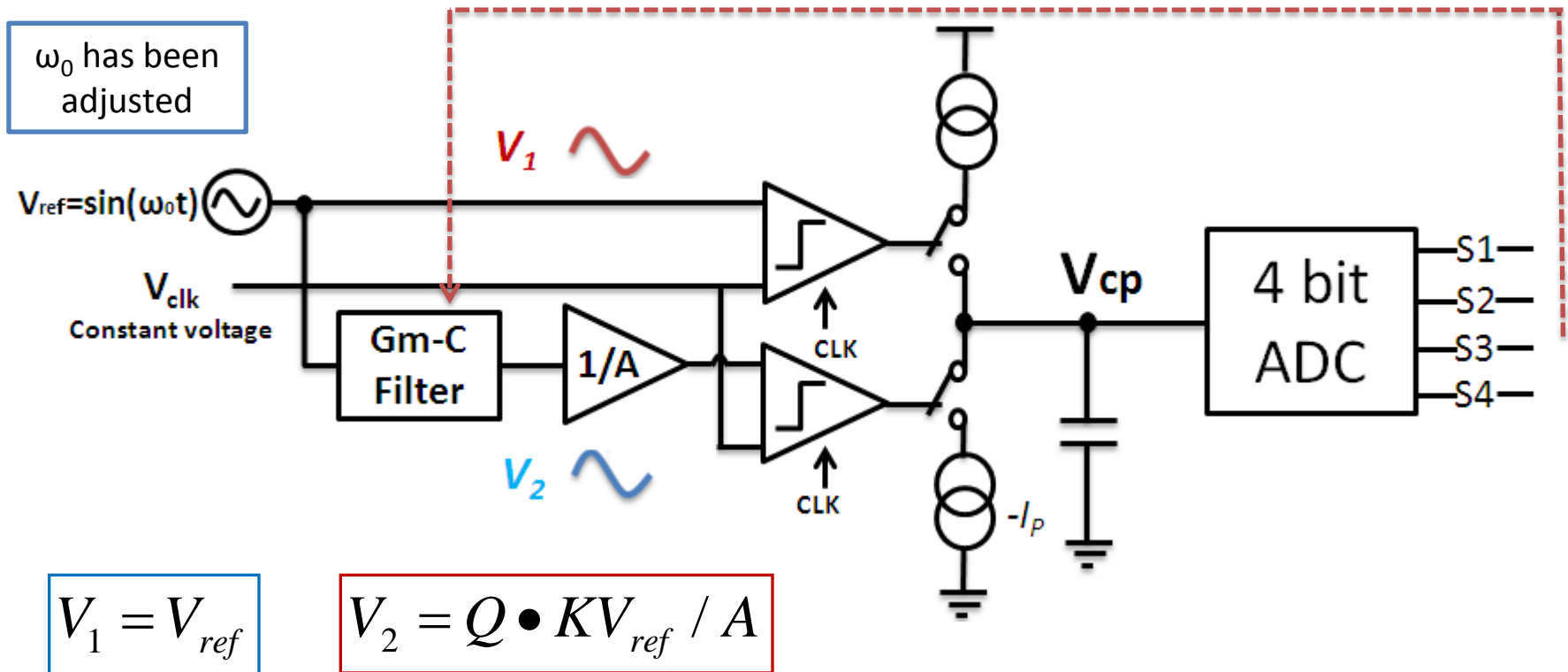
$V_1 < V_2$ \longrightarrow V_{cp} is tuned smaller \longrightarrow Gm_2 is tuned bigger

$$\left(Q = \sqrt{\frac{C_1 Gm_3 Gm_4}{C_2 Gm_2^2}} \right)$$

$$Q \propto (1/Gm_2)$$

$Q \rightarrow$ smaller

Algorithm of Q-value Tuning



When $V_1 = V_2$

K is fixed

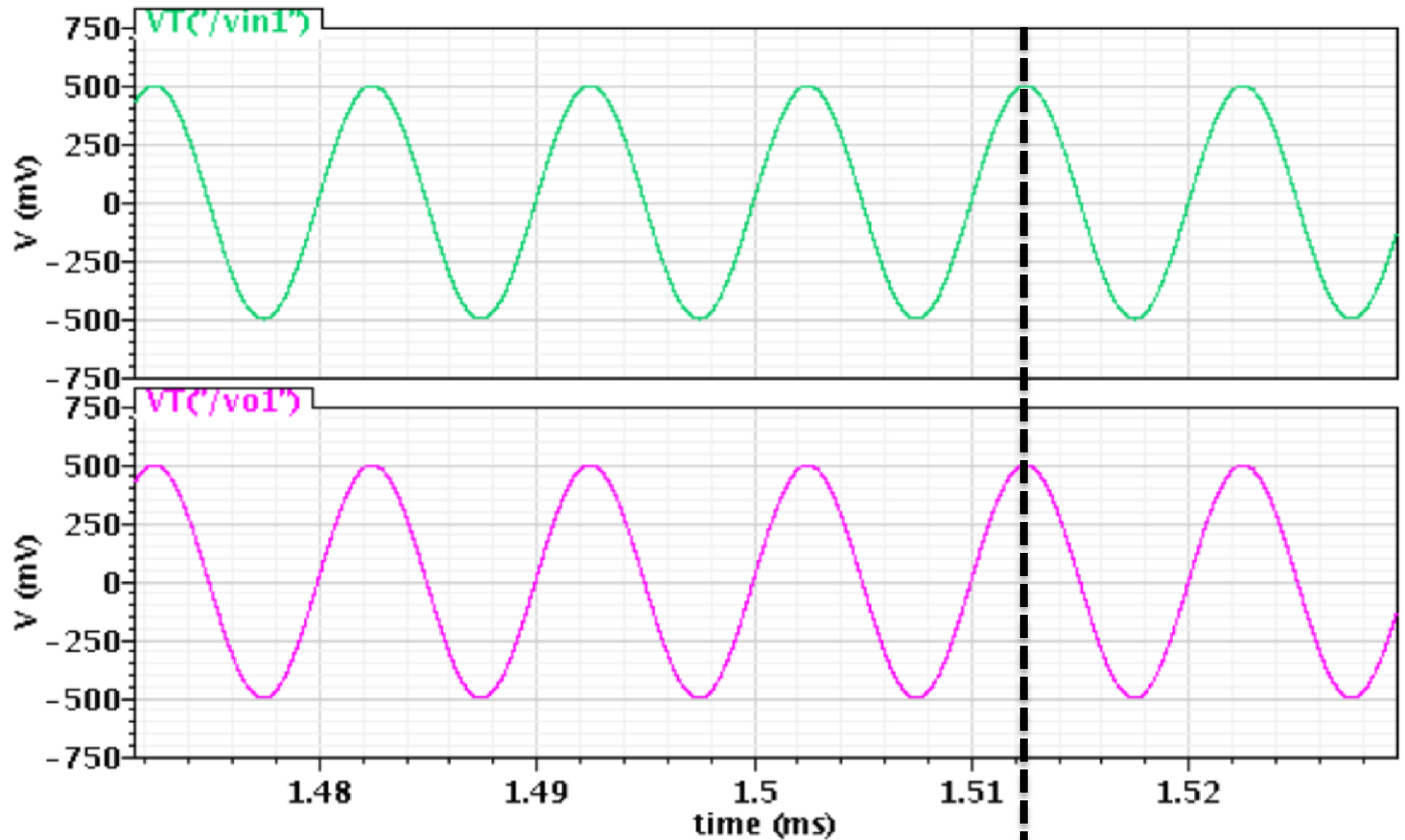
$$Q \cdot KV_{ref} / A = V_{ref} \Rightarrow Q = \frac{1}{K} A$$

$Q \rightarrow$ Determined by A

Input and Output Waves of BPF

V_{ref}

V_{out}



Phase is aligned

$$(|H(j\omega_0)| = Q)$$

Q-value Tuning Simulation Result of BPF

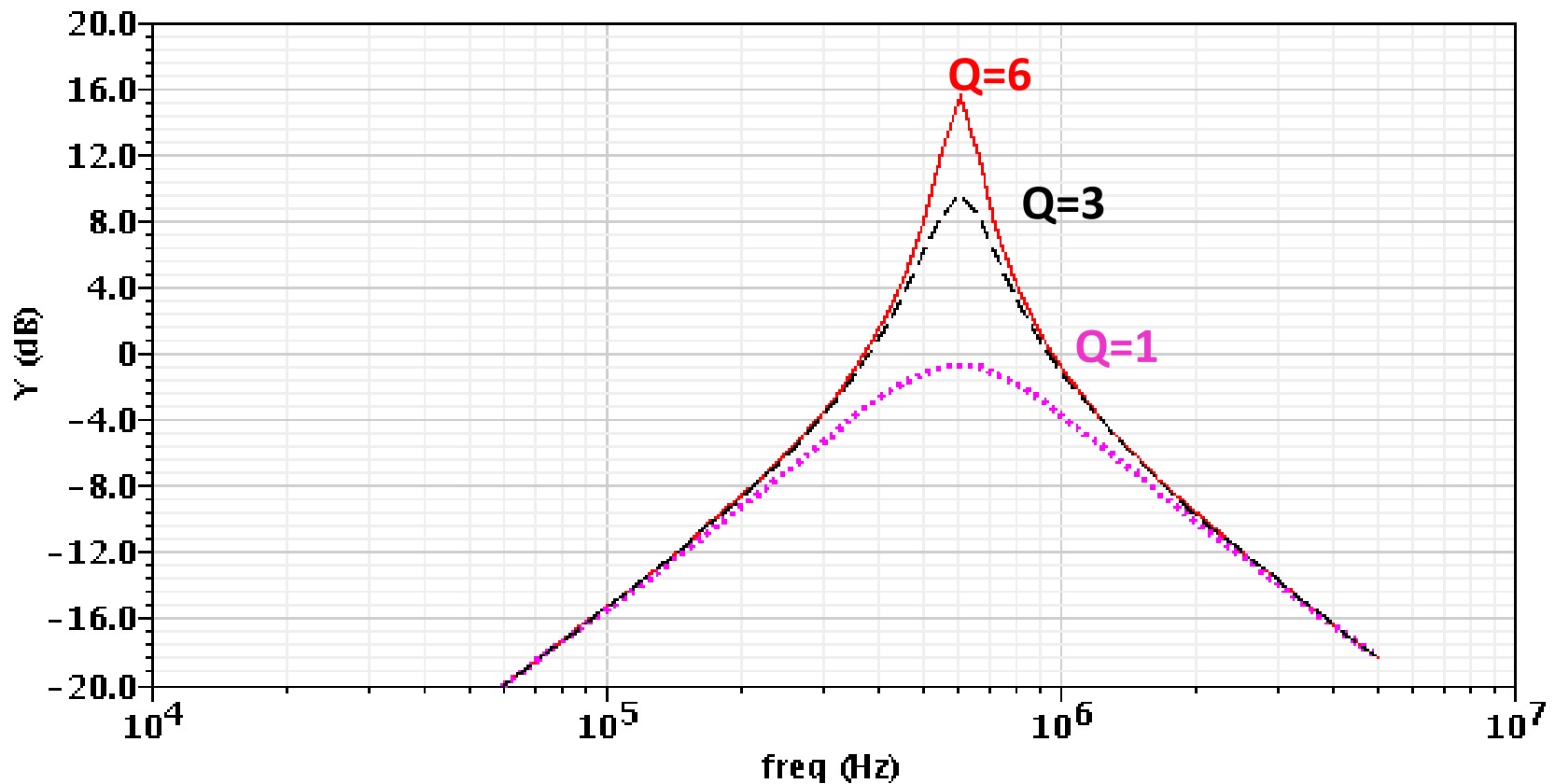
Simulation
parameters

$$G_m = 5 \times 10^{-5} S$$

$$C = 1.59 \text{ pF}$$

$$f_0 = 600 \text{ kHz}$$

$$M_1 = M_2 = 1$$



Q-value Tuning Simulation Result of LPF

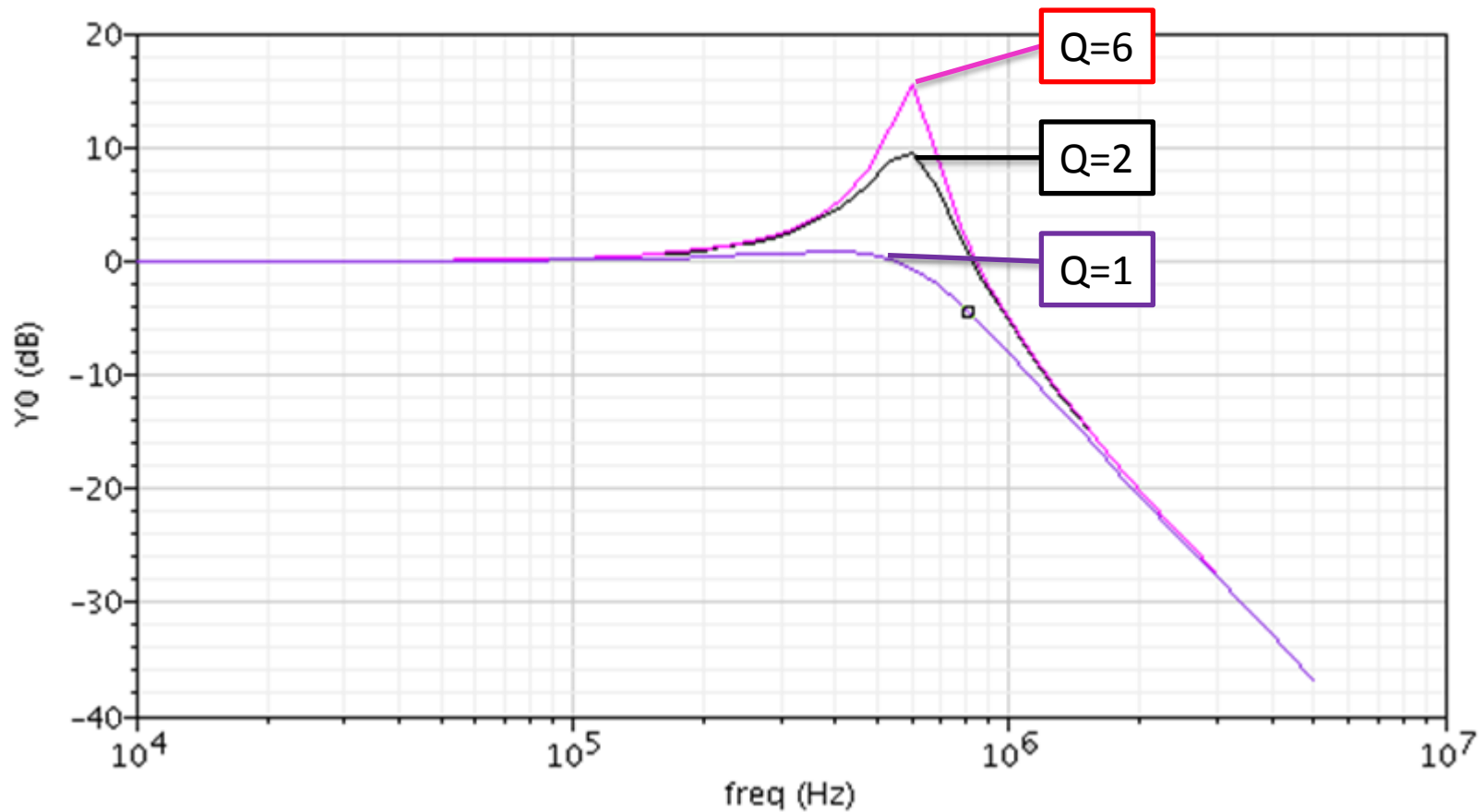
Simulation
parameters

$$G_m = 5 \times 10^{-5} S$$

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$$f_0 = 600 \text{ kHz}$$

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- *Center Frequency Tuning*
- *Q-Value Tuning*
- **Conclusion**

- Propose a digitally-controlled Gm-C band-pass filter using switched Gm arrays
 - Fine CMOS  Low voltage
 - Digital tuning schemes
 - Center Frequency  Phase property
 -  Determined by Gm3, Gm4
 - Q-value  Gain property (Center frequency has been adjusted)
 -  Determined by Gm2
- Present SPICE simulation results