

Phase Noise Measurement with Sigma-Delta TDC

Daiki Hirabayashi, Yusuke Osawa, Naohiro Harigai, Haruo Kobayashi

Osamu Kobayashi †, Kiichi Niitsu ‡, Takahiro J. Yamaguchi, Nobukazu Takai

Division of Electronics and Informatics, Gunma University, Kiryu 376-8515 Japan email: k_haruo@el.gunma-u.ac.jp

† Semiconductor Technology Academic Research Center (STARC), Yokohama 222-0033 Japan

‡ Graduate School of Engineering, Nagoya University, Nagoya 464-8603 Japan

Background

Conventional Phase Noise Measurement

- Spectrum analyzers are necessary
- Measurement for a long time

Very costly!!

In order to test many LSIs in parallel...



Many spectrum analyzers

Research Objective

Measure the phase noise without a spectrum analyzer

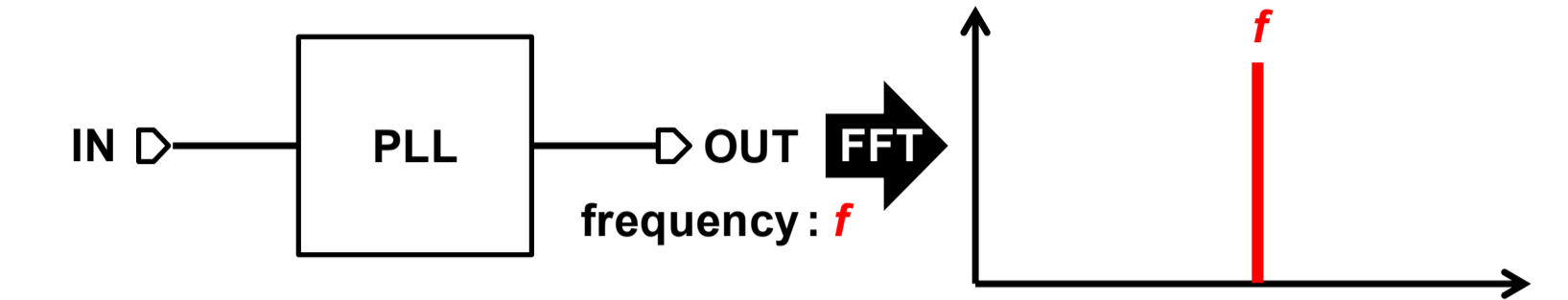
Sigma-Delta TDC

- Good linearity
 - Small circuitry
 - Fine time resolution
- The phase noise power spectrum can be obtained by FFT of $\Sigma\Delta$ TDC output

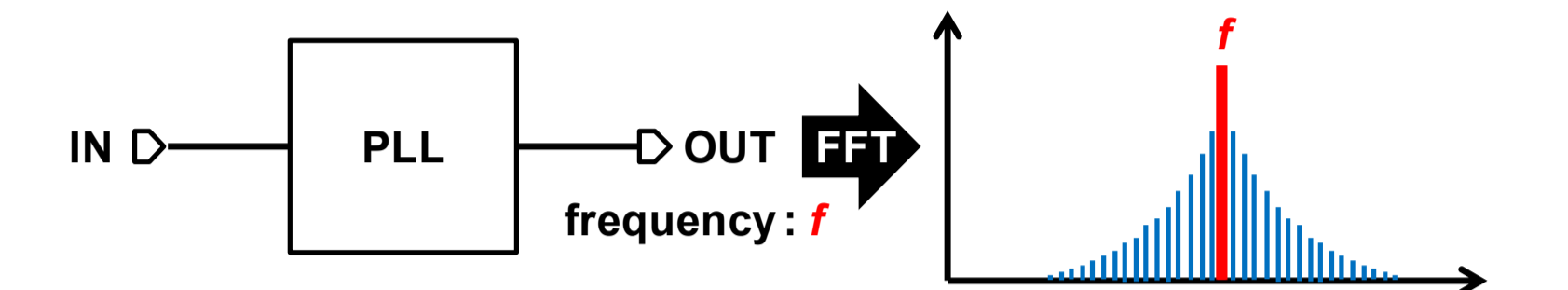
Low Cost & High Quality Test

Phase Noise

Ideal PLL



Actual PLL



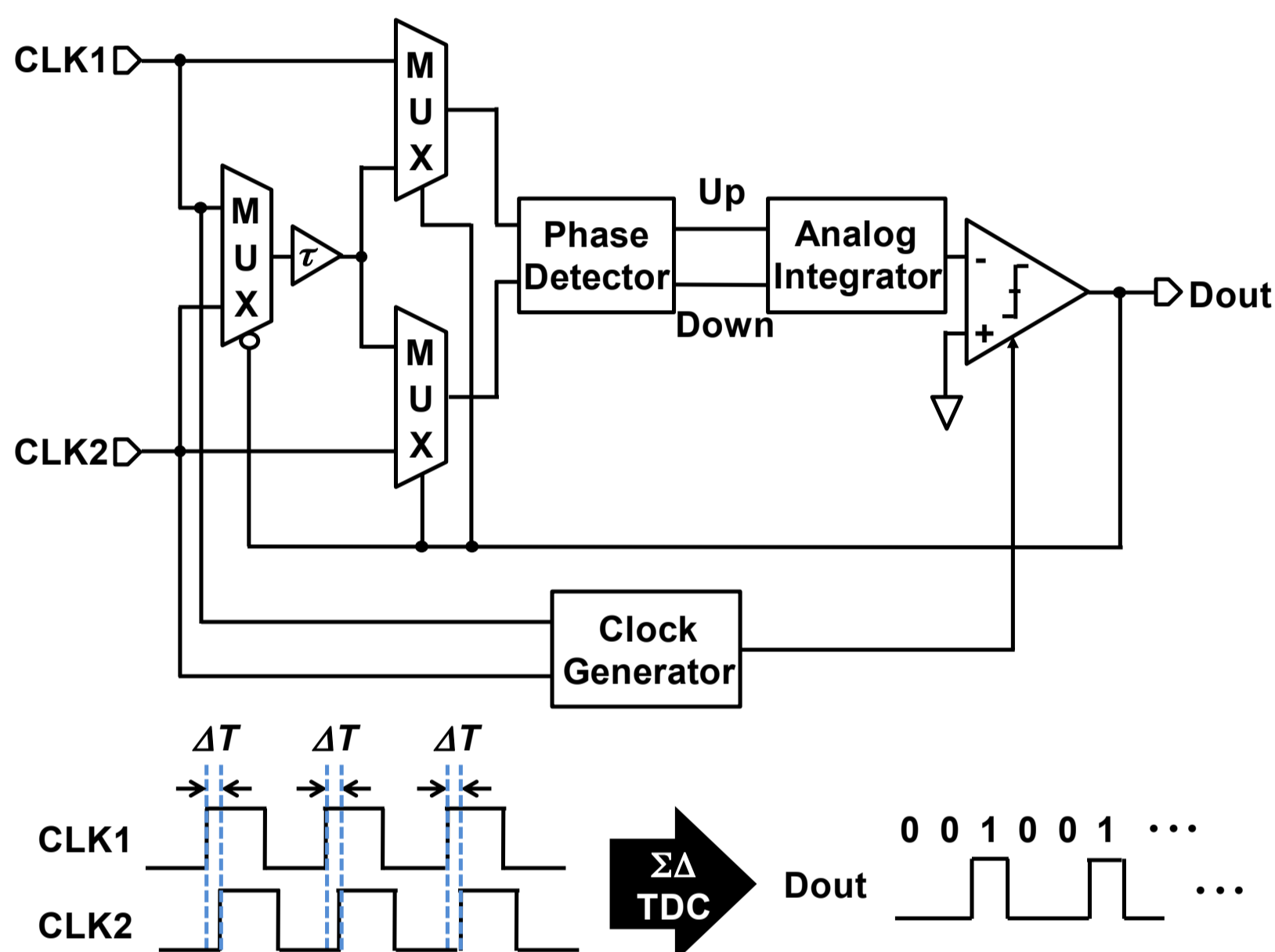
Phase Noise = Various frequencies

cause errors in communication systems

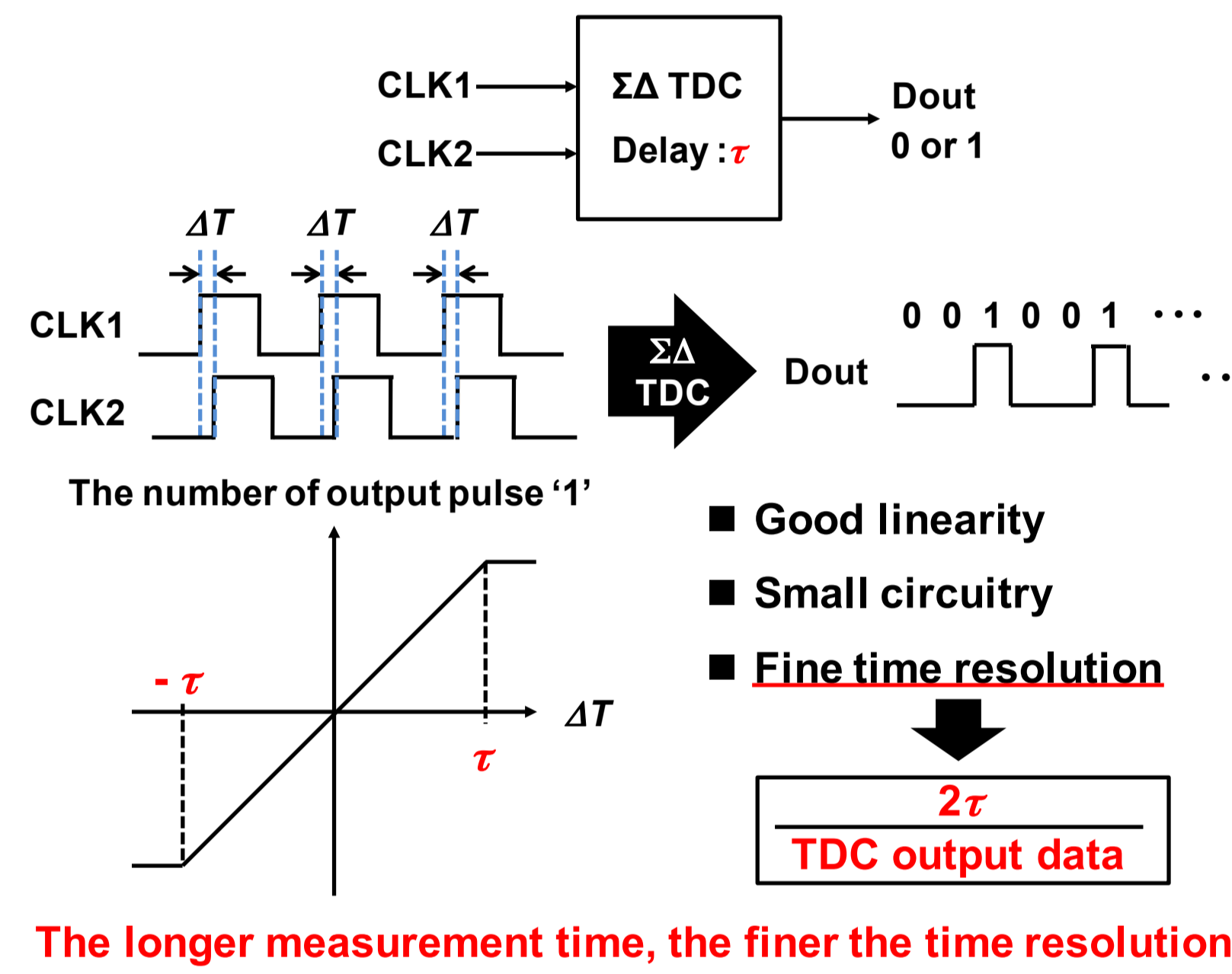
Its measurement & testing is necessary

Introduction

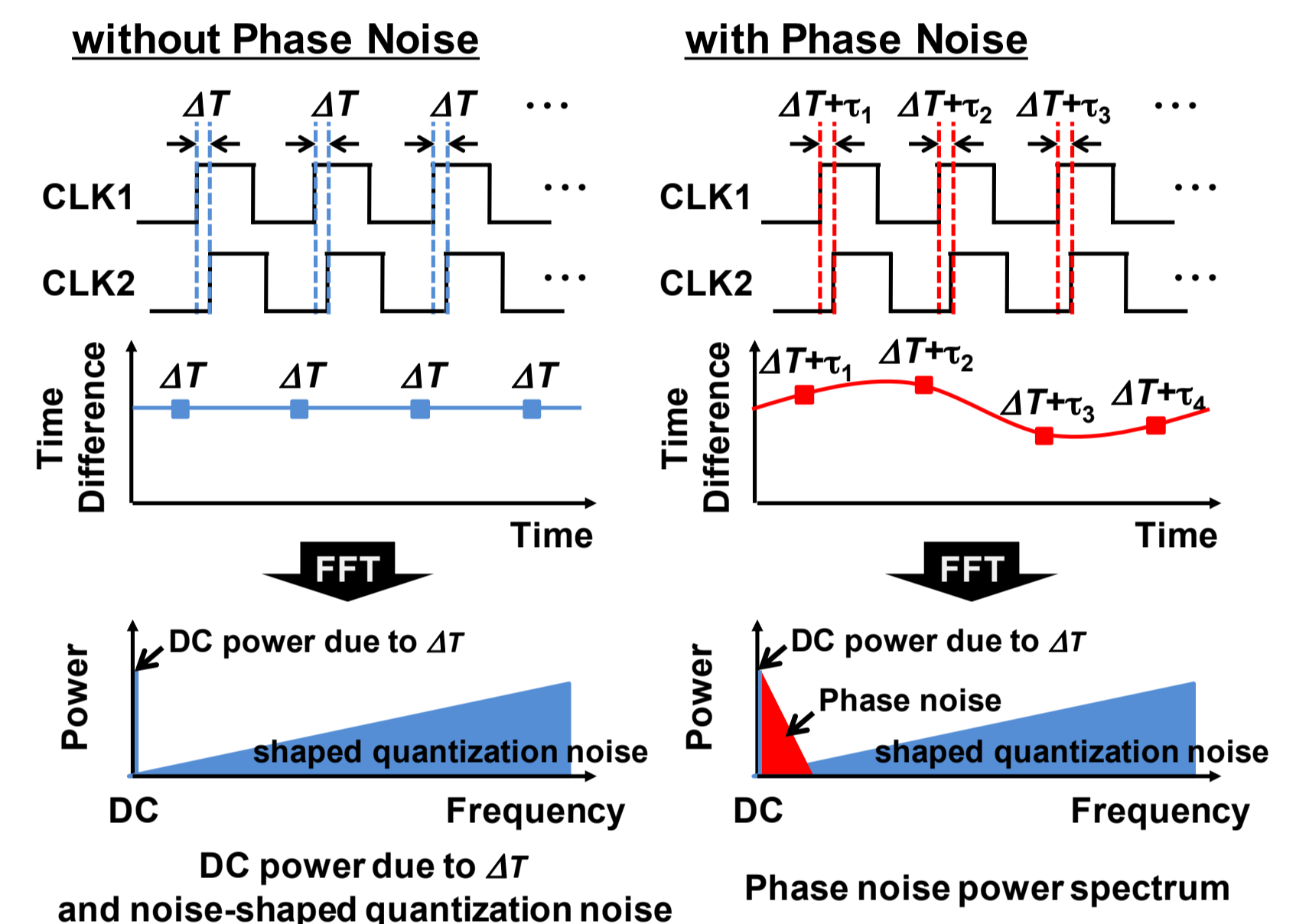
Block Diagram of Sigma-Delta TDC



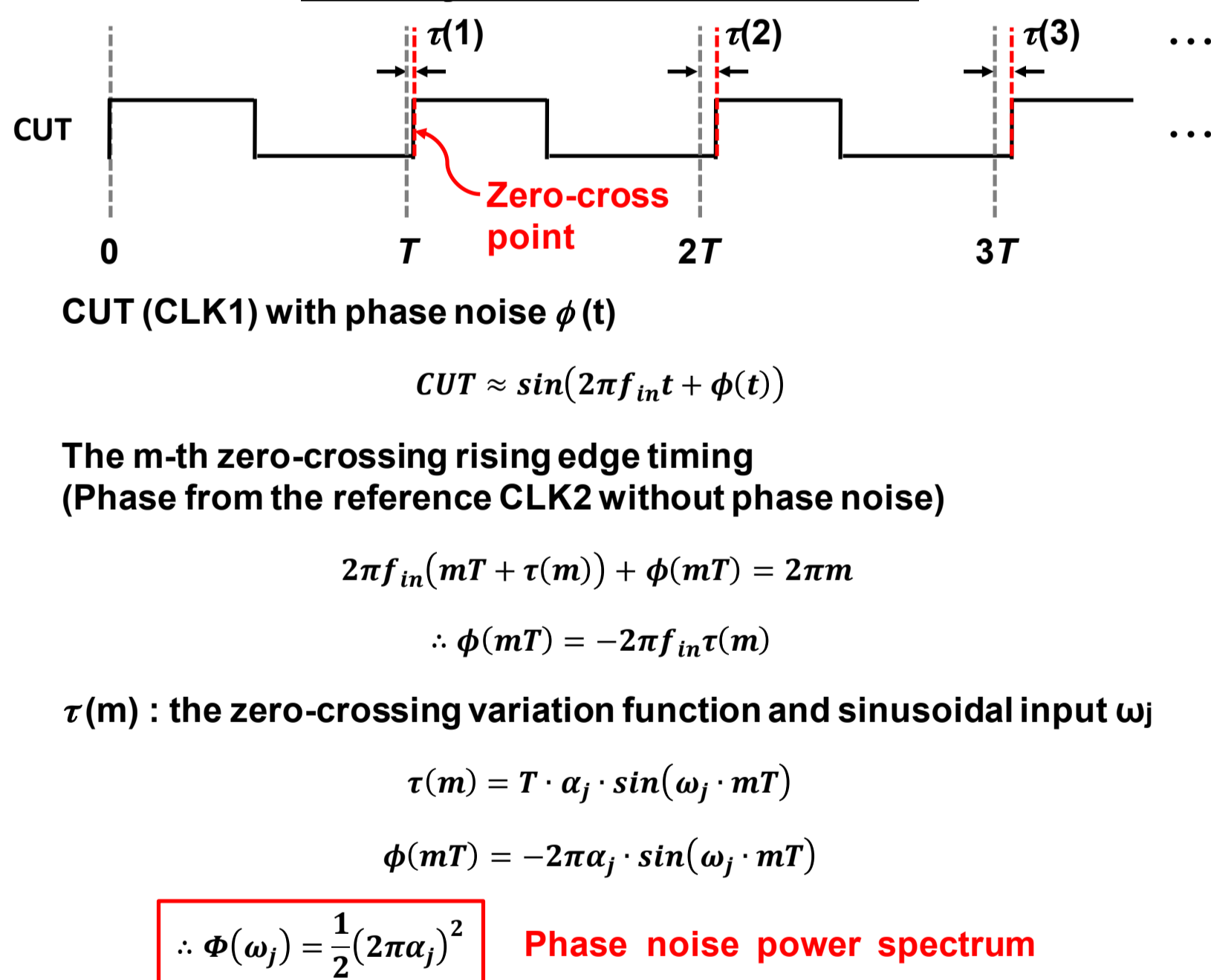
Operation of Sigma-Delta TDC



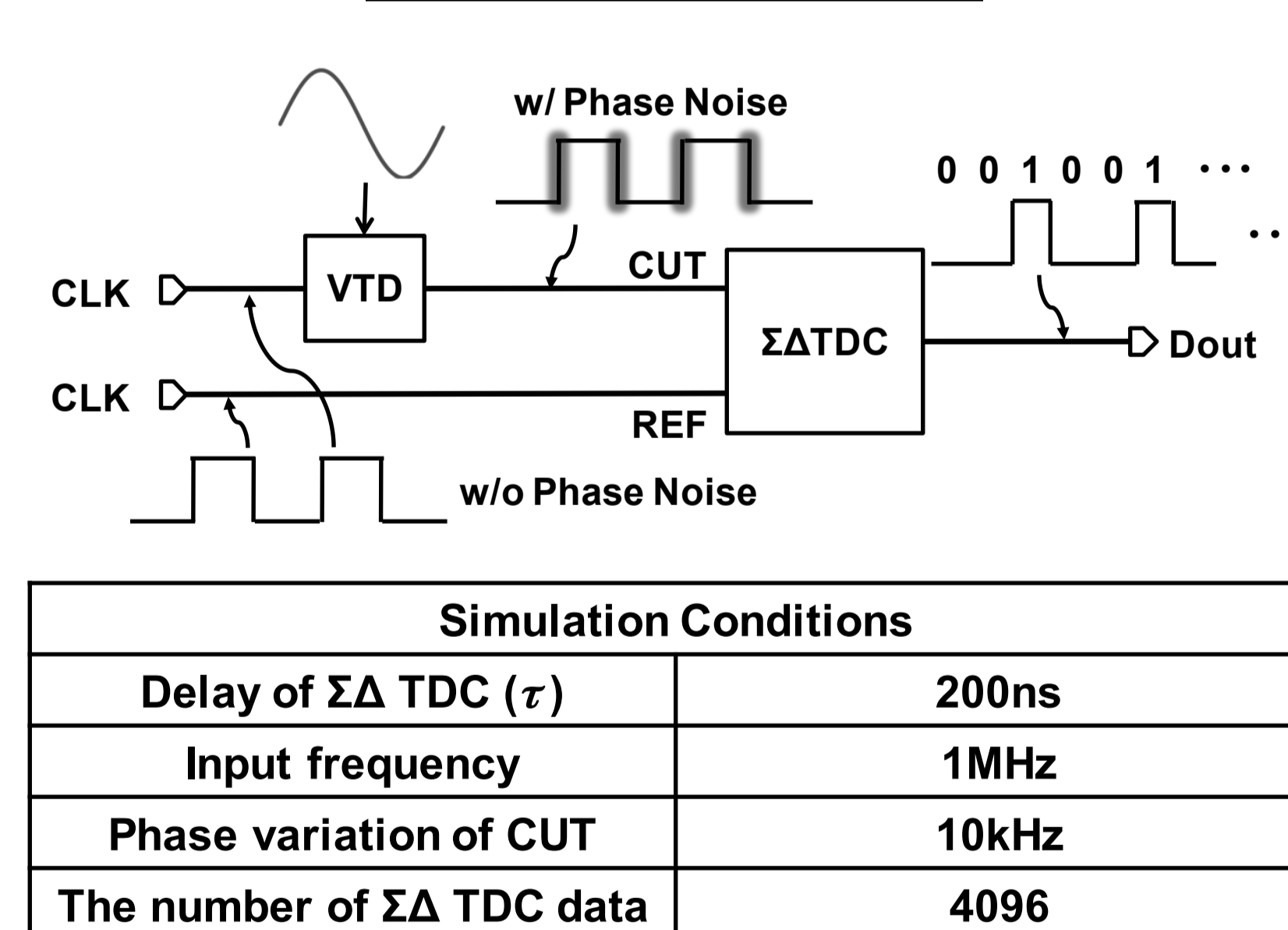
Principle of Phase Noise Measurement



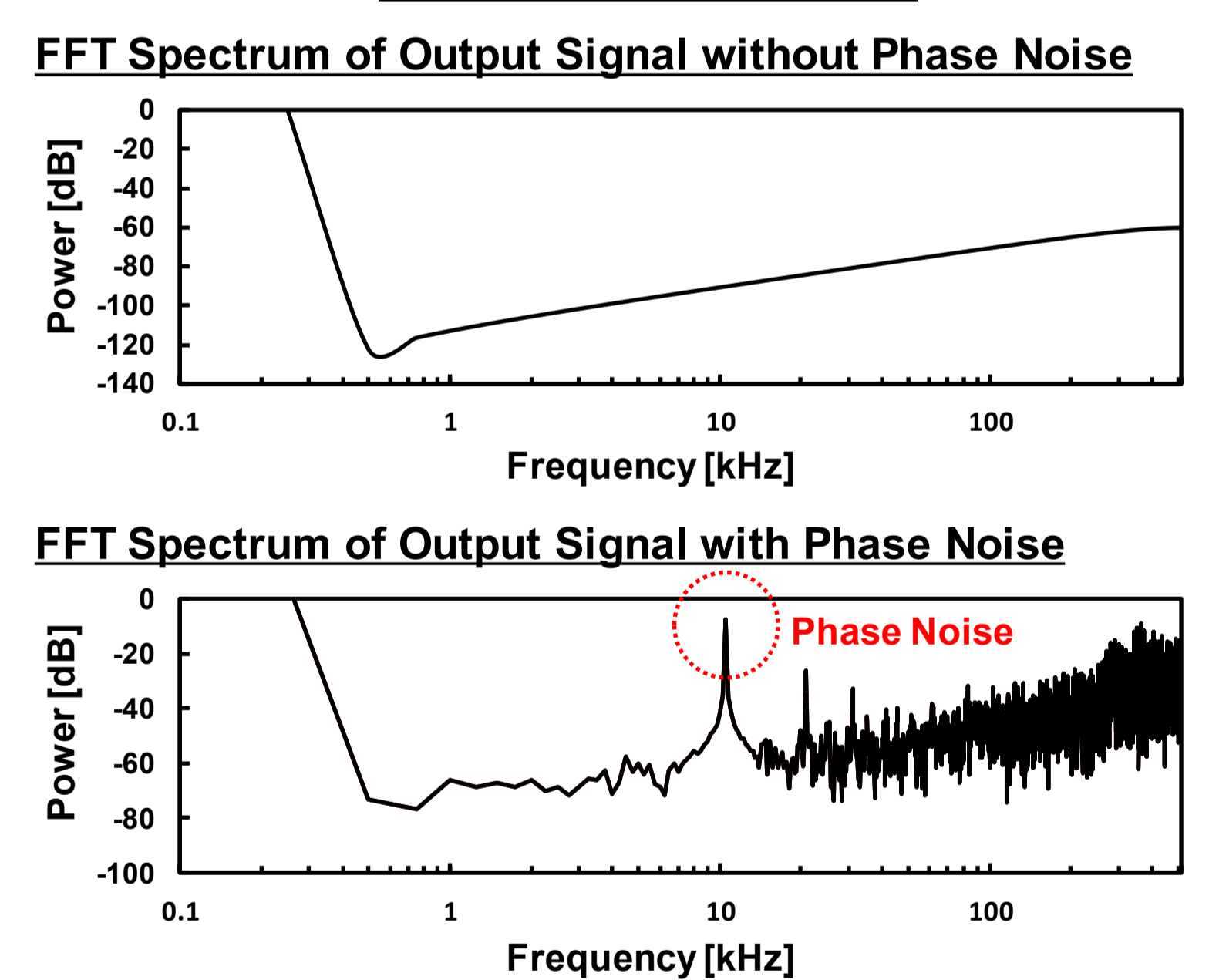
Analytical Discussion



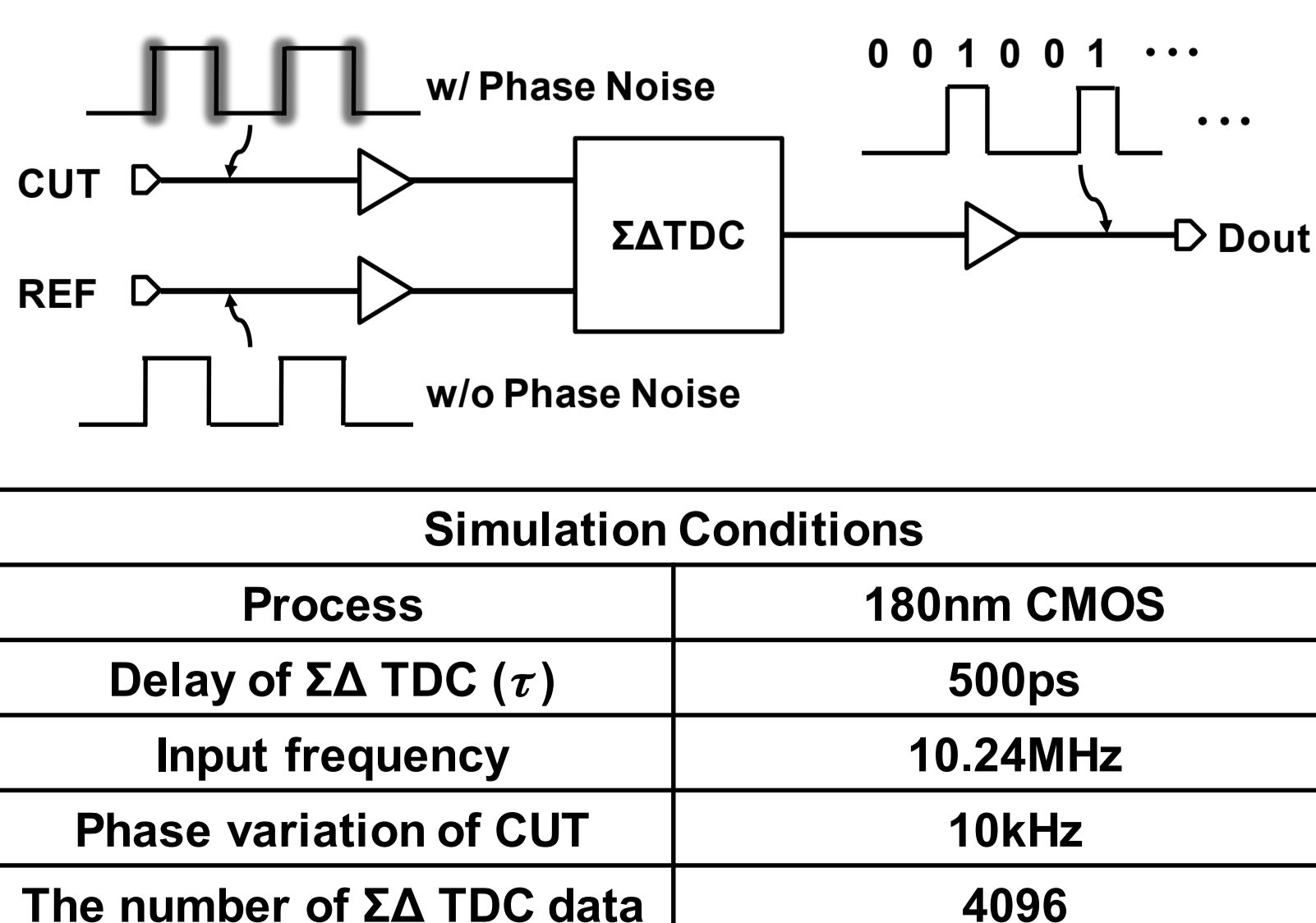
MATLAB Simulation



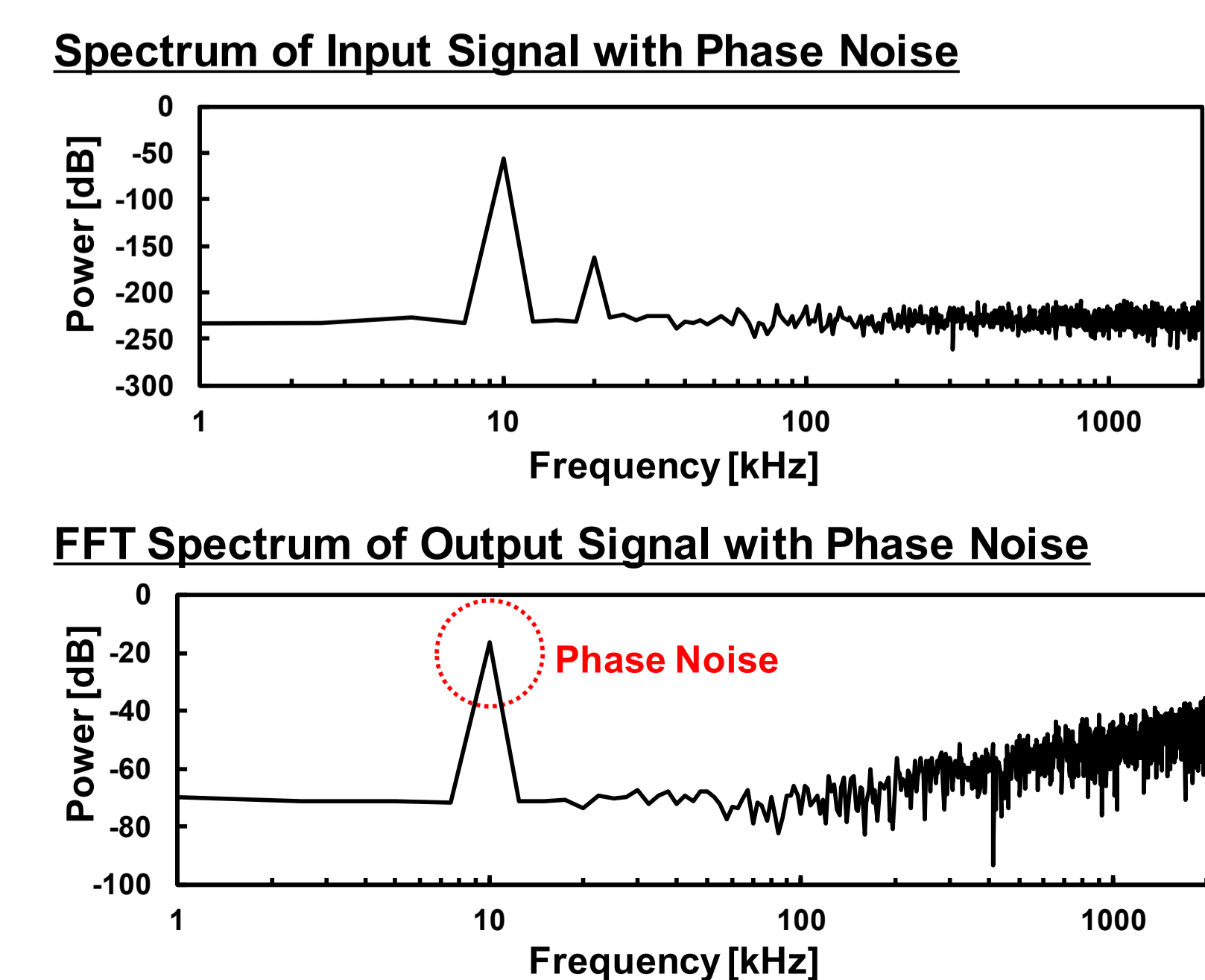
Simulation Result



SPECTRE Simulation



Simulation Result



Conclusion

- Proposal of phase noise measurement using sigma-delta TDC.
- Verification by MATLAB and SPECTRE simulations.
- A multi-bit sigma-delta TDC with linearization techniques can further reduce the measurement time.
- Low cost and high quality phase noise test can be realized.