AD/DA 変換器のディジタル自己校正・補正技術

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キーワード  ADC, DAC, デジタル補正, 兀長性, 自己校正, デジタルアシスト・アナログ技術

Digital Error Correction Techniques in ADCs and DACs
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Abstract  This paper describes some aspects of digitally assisted analog technology, (i) Non-binary successive approximation ADC, (ii) Oversampling technology, (iii) Delta-Sigma ADC/DAC, especially DWA algorithms for multi-bit architectures. We address that the reliability design of ADCs/DACs is important and digital error correction techniques using redundancy are expected to improve the reliability.

Keyword  ADC, DAC, Digital Error Correction, Redundancy, Self-Calibration, Digitally-Assisted Analog Technology