

# Study of Complex Multi-Bandpass DWA algorithm for I-Q Signal Generation

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The DAC circuits in transmitter parts (which often generate I-Q signals) of communication devices become more complicated and challenging<sup>1,2,3</sup>. On the other hand, their testing cost increases due to the circuit complexity and high specification requirements. Their testing requires high quality I-Q signals at low cost<sup>4</sup>.

This paper discusses applicability of a complex bandpass (BP)  $\Delta\Sigma$  DA modulator to generate I-Q signals with digital rich configuration. Also we newly derive complex multi-BP Data-Weighted-Averaging (DWA) algorithms for complex multi-tone signal generation.

Fig.1 shows complex multi-bandpass  $\Delta\Sigma$  DA modulator and implementation of proposed complex Multi-BP DWA algorithm. Fig.2 shows our Matlab simulation results with and without the proposed DWA algorithm, and we see that our DWA algorithm is effective.

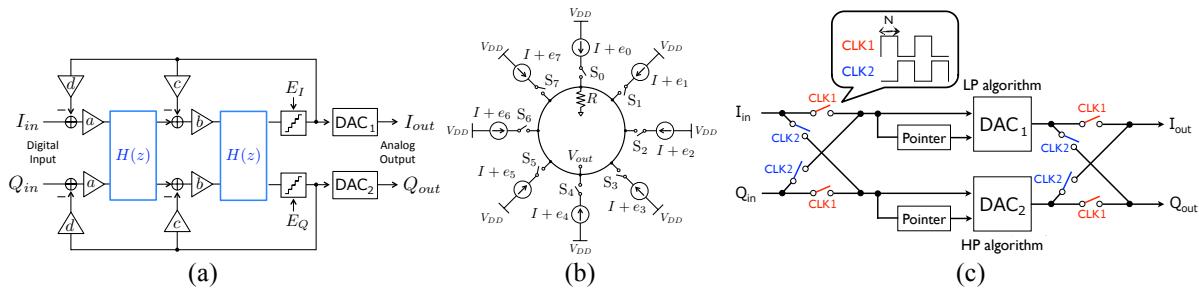


Fig.1 (a) 2nd-order complex multi-bandpass  $\Delta\Sigma$  DA modulator.  
 (b) 8-unit segmented current steering DAC in ring arrangement.  
 (c) Implementation of proposed complex Multi-BP DWA algorithm.

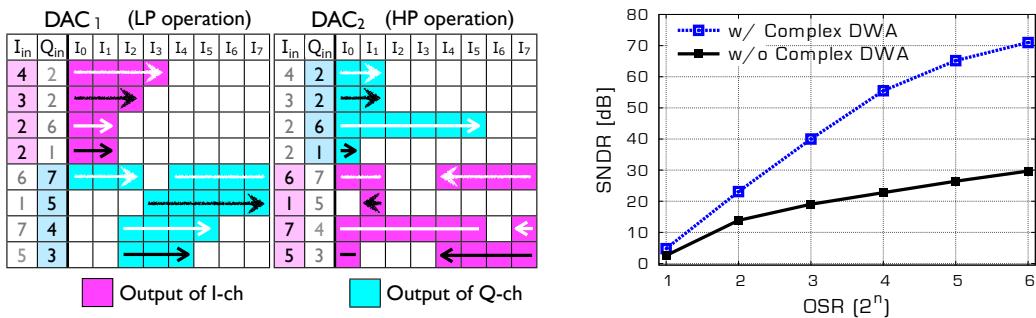


Fig.2 (a) Proposed complex multi-BP DWA algorithm ( $N = 4$ ).  
 (b) Simulation results with and without DWA.

<sup>1</sup> K. W. Martin, "Complex Signal Processing is NOT Complex," IEEE Trans. on Circuits and Systems I, vol.51, no.9 pp.1823-1836 (Sept. 2004). <sup>2</sup> J. Otsuki, H. San, H. Kobayashi, T. Komuro, Y. Yamada, A. Liu, "Reducing Spurious Output of Balanced Modulators by Dynamic Matching of I, Q Quadrature Paths", IEICE Trans. on Electronics, E88-C, no.6, pp.1290-1294 (June 2005). <sup>3</sup> H. Kobayashi, J. Kang, T. Kitahara, S. Takigami, H. Sadamura "Explicit Transfer Function of RC Polyphase Filter for Wireless Transceiver Analog Front-End", 2002 IEEE Asia-Pacific Conference on ASICs, pp.137-140, Taipei, Taiwan (Aug. 2002). <sup>4</sup> H. Kobayashi, T. J. Yamaguchi, "Digitally-Assisted Analog Test Technology - Analog Circuit Test Technology in Nano-CMOS Era -," IEICE Technical Report, ICD, Osaka (July 2010).