

ADC/DAC Redundancy Design Using Fibonacci Sequence

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Number Theory

Fibonacci sequence

$$F_0 = 0, F_1 = 1$$

$$F_n = F_{n-1} + F_{n-2}$$



Fibonacci sequence numbers

0, 1, 1, 2, 3, 5, 8, 13, 21, 34, 55, 89...

Famous property

$$\lim_{n \rightarrow \infty} \frac{F_n}{F_{n-1}} = \underline{1.61803398...}$$

Golden ratio

Nature



Plant life
Creatures



Humanity



Arts, Architectures
Economics

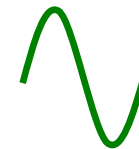
Our world-first
approach

Engineering

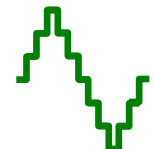


ADC/DAC design

Analog



Digital



**High performance
converter design !**