Automatic Synthesis of Comparator Circuit
Using Genetic Algorithm and SPICE Optimizing Function

Takayuki Negishi, Naoki Arai, Nobukazu Takai, Masato Kato, Hiroaki Seki, Haruo Kobayashi
Department of Electronic Engineering, Gunma University, JAPAN

The more complicated the circuit topology, the more difficult for an engineer to design.

**Automatic design by computer** is demanded.
(Designed in a short time than engineers)

- Determines circuit parameters using HSPICE optimization function
- Evolve circuit topology using Genetic Algorithm

We have developed

**Automatic synthesis program for comparator circuit**

Genetic Algorithm based on “Darwin’s Evolution Theory”