

Analog FPGA Implementation of Multi-bit Delta-Sigma TDC

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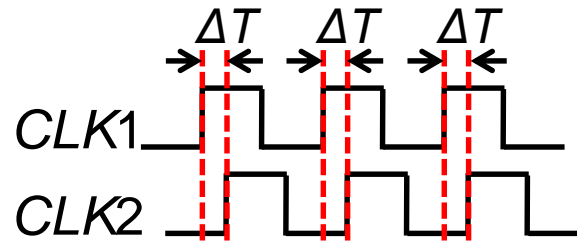
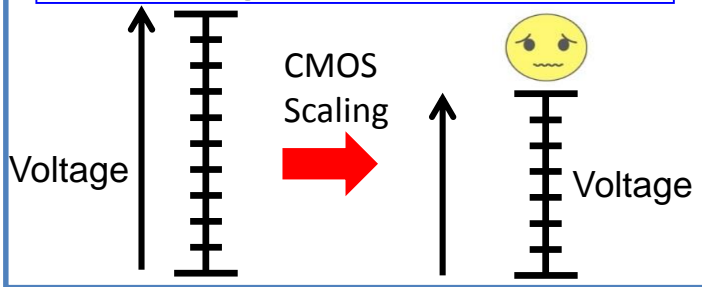
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Miniaturization CMOS LSI

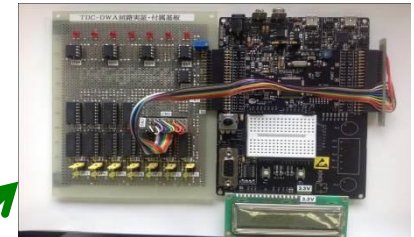


Lower power supply voltage
Faster switching speed

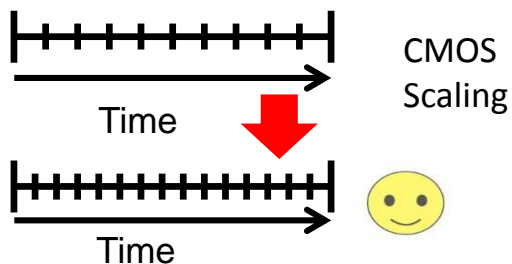
Voltage Resolution



Analog FPGA



Time Resolution



Time
measurement



Digital values
10010