

Efficiency Improvement for Switching Power Supply at Light Load Using DSP Control

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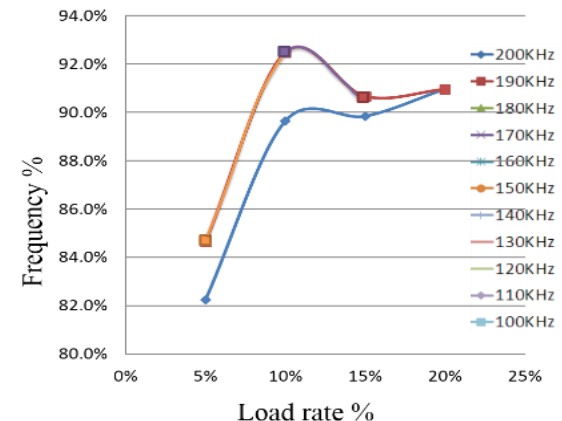
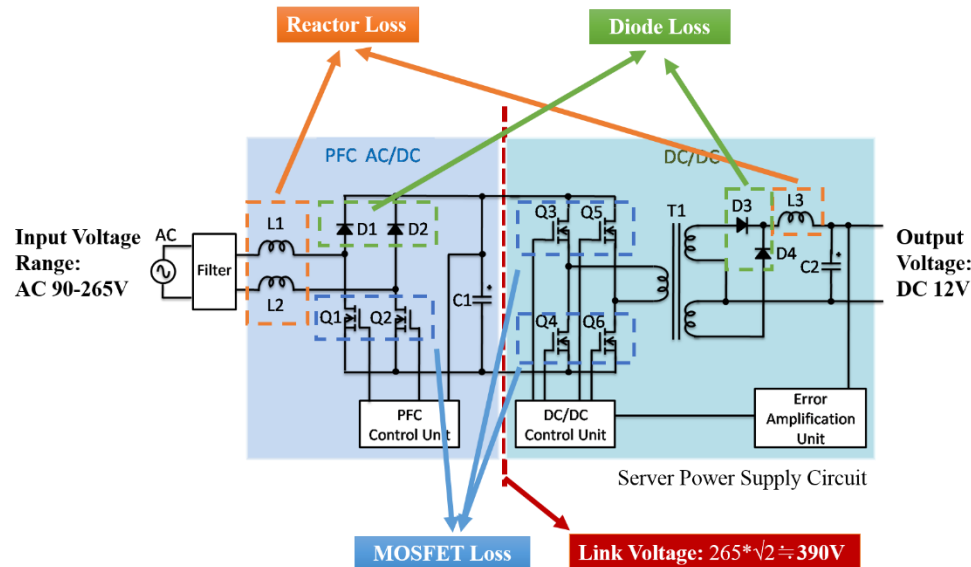
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High Link Voltage and **High Fixed Switching Frequency** in AC/DC and DC/DC circuits

cause unnecessary power losses.



Optimization of PWM Frequency of BLPFC AC/DC at a Load Rate of 5% to 20%

We find that **lowering link voltage** and **choose appropriate frequency** corresponding to each load range alleviate power losses.