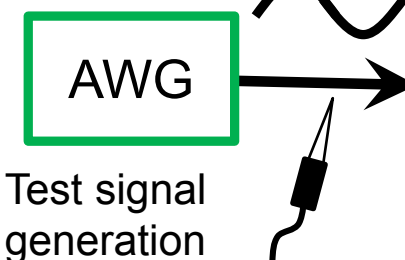
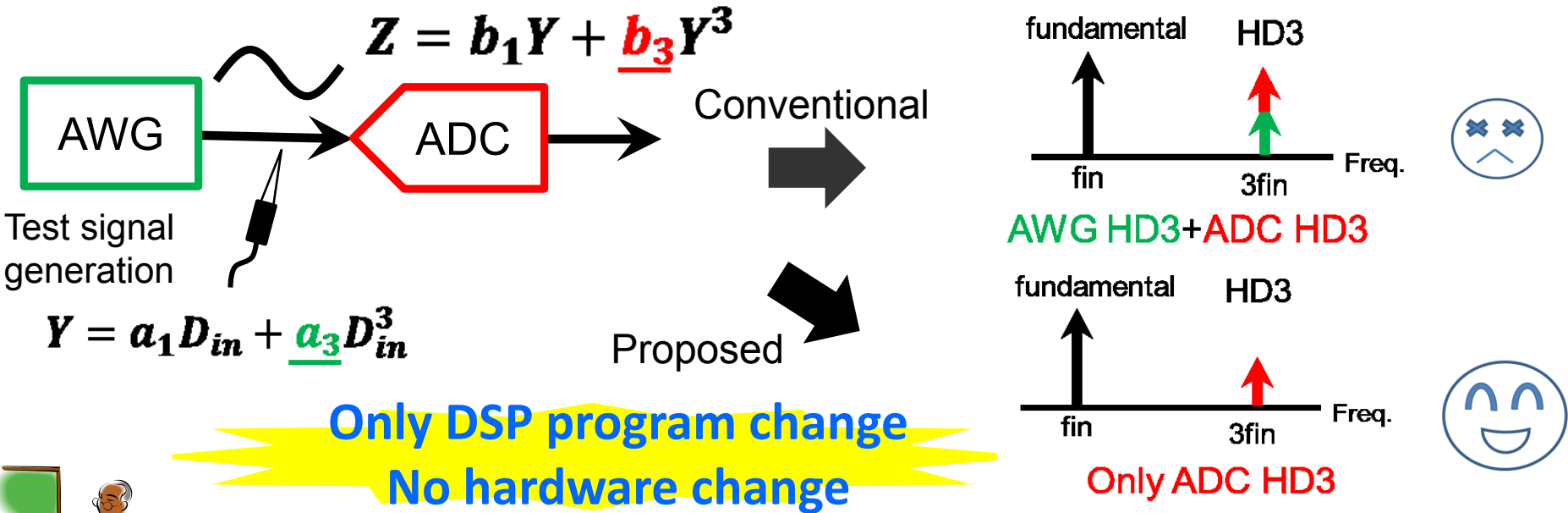


## Pure Sine Signal Generation With Arbitrary Waveform Generator

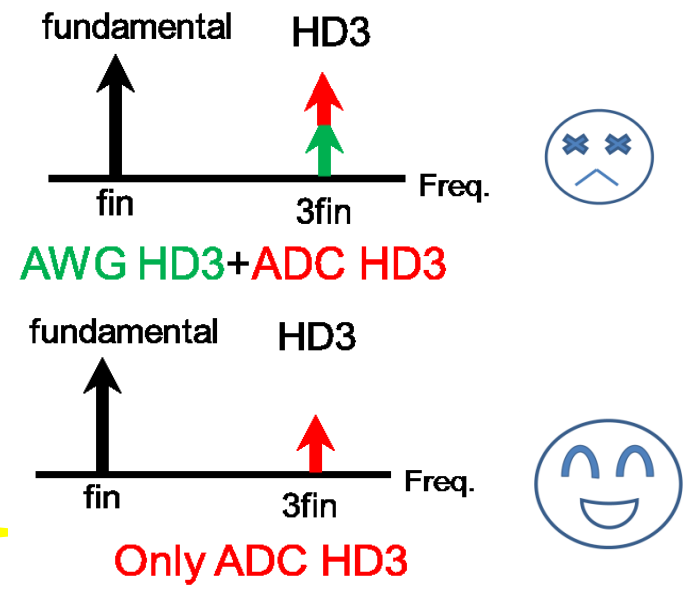
F. Abe, Shohei Shibuya, Y. Kobayashi, S. Sasaki, M. Higashino  
T. Arafune, O. Kobayashi, H. Kobayashi  
*Division of Electronics and Informatics, Gunma University*

**Research Objective:** Development of high quality signal generation algorithms for ADC linearity test with a low cost arbitrary waveform generator (AWG)



$$Z = b_1 Y + \underline{b_3} Y^3$$

$$Y = a_1 D_{in} + \underline{a_3} D_{in}^3$$



**LSI testing is important Without testing, it's a gamble !**