

Reliability Modeling on 90 nm n-channel MOSFETs with BSIM4 Dedicated to HCI Mechanisms

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The final purpose of this study is to model the drain current and 1/f noise degradation characteristics of n-channel MOSFETs. In this report, we present the implementation of Hot Carrier Degradation into drain current equations of BSIM4 model.

Then, the simulations of the DC drain current degradation will be demonstrated. 1/f noise voltage density simulation affected by the drain current degradation will also be shown.

BSIM4 model parameters have been extracted extensively with measured data including I-V and 1/f noise measurement of our TEGs.

Fig.1 and Fig.2's measured value, which is dot marked, is identical. Solid line represents fresh and degraded simulation data.

In this way, we show the characterizations of before and after the degradation.

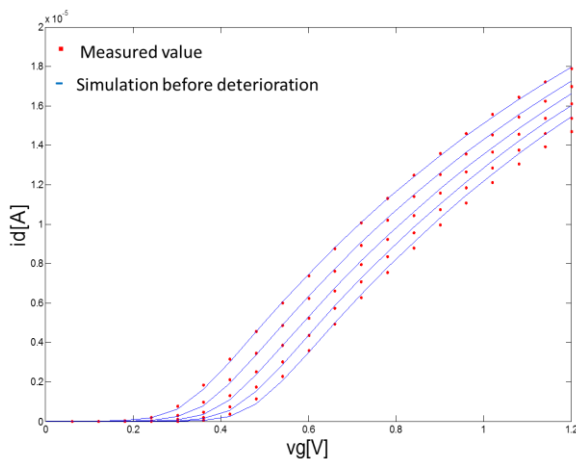


Fig. 1. I_{DS} vs. V_{GS} characterizations of fresh n-MOSFET

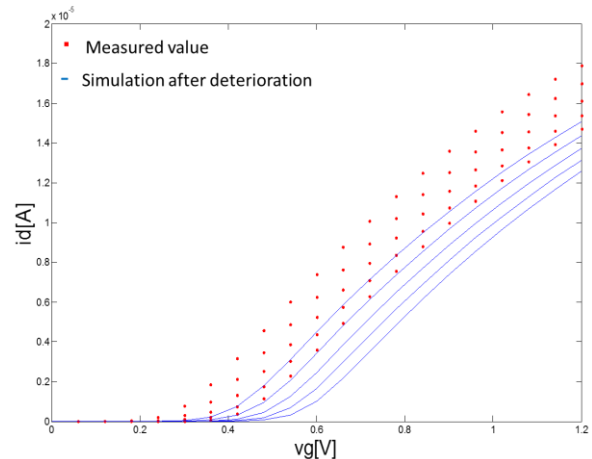


Fig. 2. I_{DS} vs. V_{GS} characterizations of degraded n-MOSFET

¹ S. Todoroki, H. Aoki, F. Abe, K. Ramin, Y. Arai, M. Kazumi, T. Totsuka, H. Kobayashi, "1/f Noise Variance Modeling of Gate Voltage Dependence with n-channel MOSFETs," Institute of Electrical Engineers Japan (IEEJ). ECT-14-010 Kanazawa (Jan, 2014).

² H. Aoki, M. Shimasue, Y. Kawahara, *CMOS Modeling Technology*, Maruzen Publishing, (2006).

³ H. Aoki, "Bias and Geometry Dependent Flicker Noise Characterization for n-MOSFETs," IEICE Trans. Electronics, vol. E85-C, no.2 pp.408-414(2002).

⁴ Information on <http://www-device.eecs.berkeley.edu/bsim/>

⁵ C. Hu, et al, "Hot-electron induced MOSFET degradation model, monitor, and improvement," Trans. Electron Devices, 32(2), 375-385, 1985.

⁶ E. Maricaud and G. Gielen, *Analog IC Reliability in Nanometer CMOS*, Springer Science Business Media New York, 2013.

⁷ H. Kufluoglu and M. A. Alam, "A unified modeling of NBTI and hot carrier injection for MOSFET reliability," 10th International Workshop on Computational Electronics, pp. 28-29, Oct. 2004.

⁸ H. Aoki and M. Shimasue, "Noise Characterization of MOSFETs for RF Oscillator Design," Proc. 1999 IEEE MTT-S International Microwave Symposium, Anaheim CA, (Jun. 1999).