

アメリカ(University of California, LA)

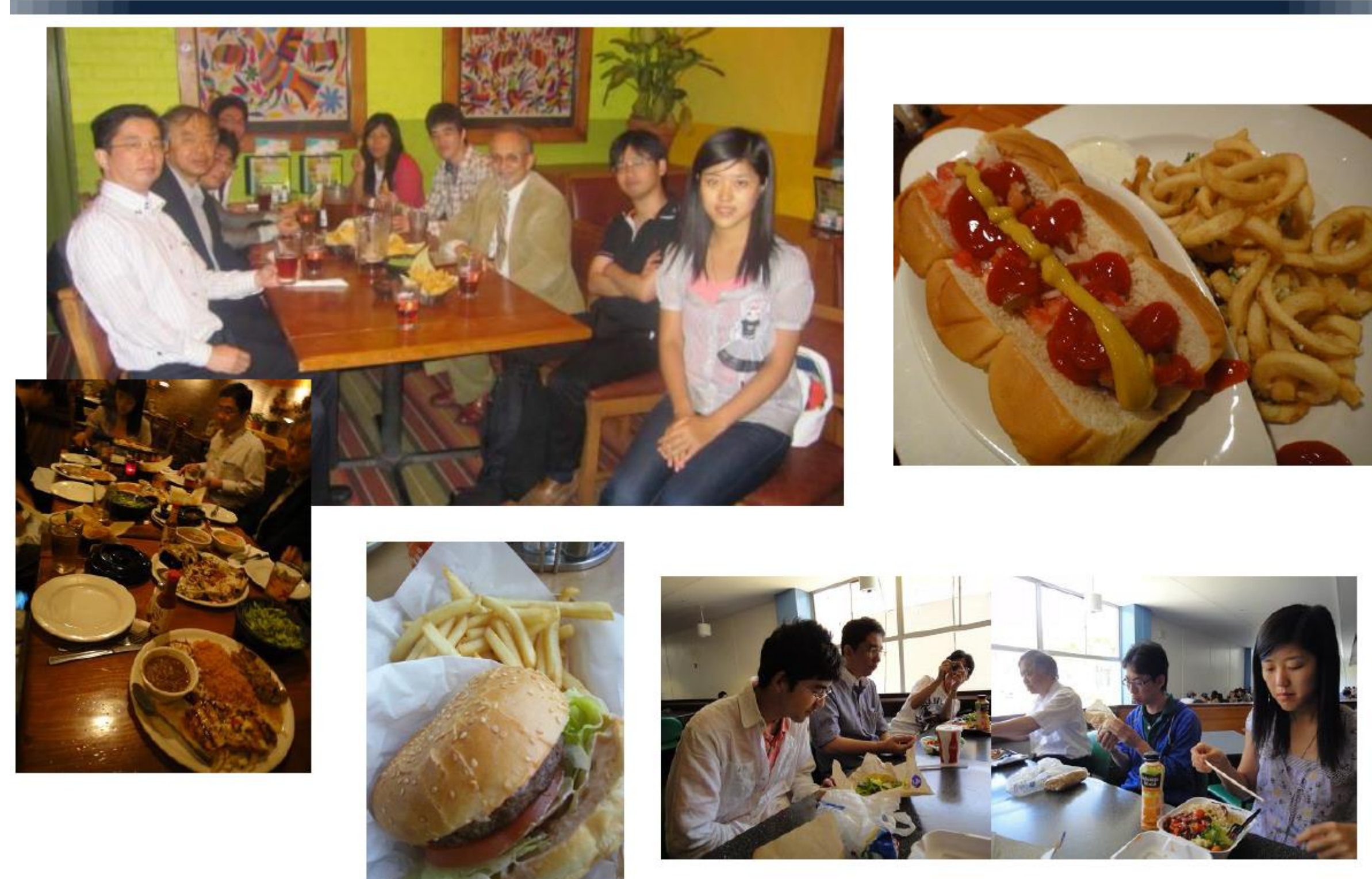
教授方との対面



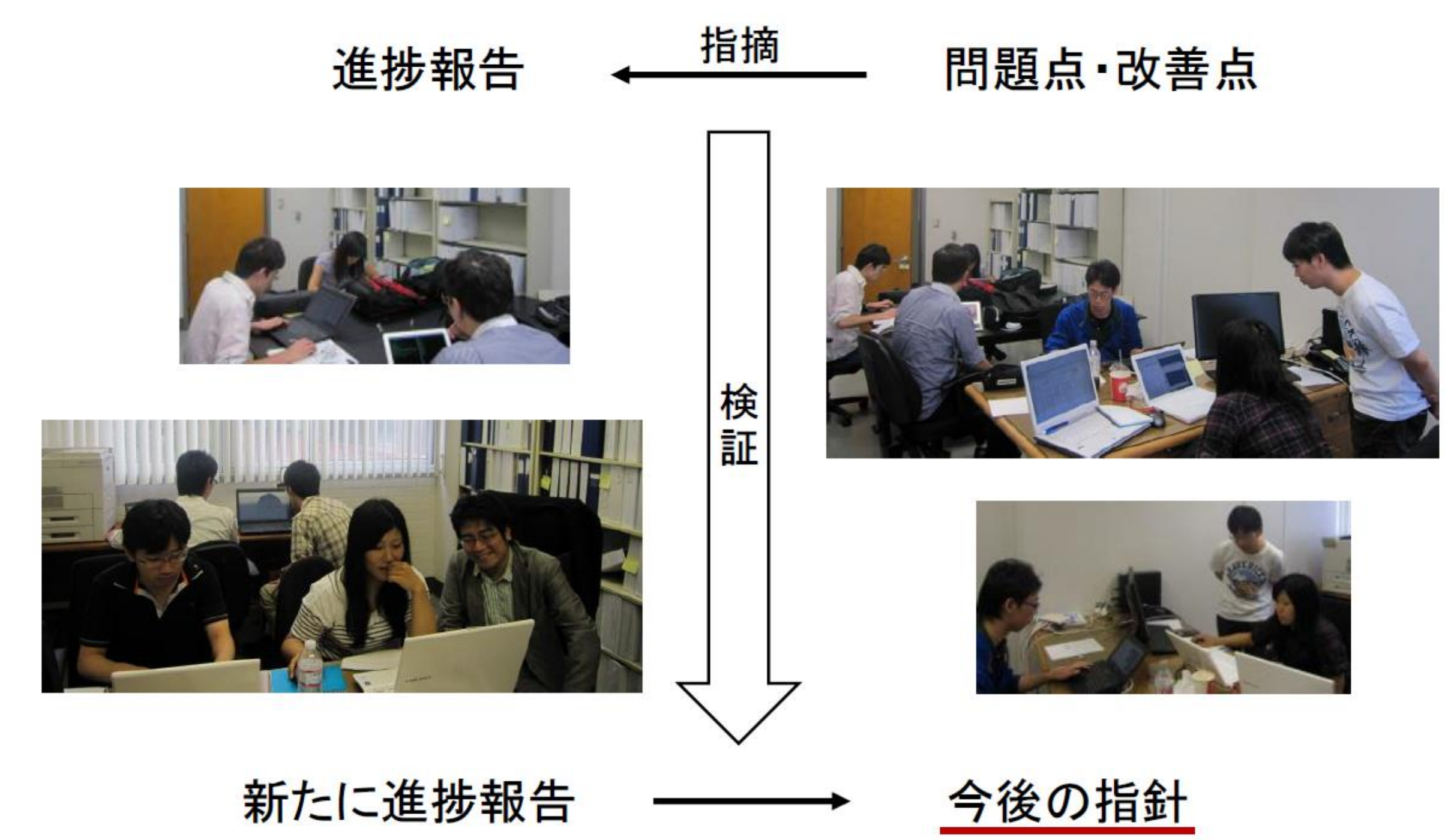
校内の様子



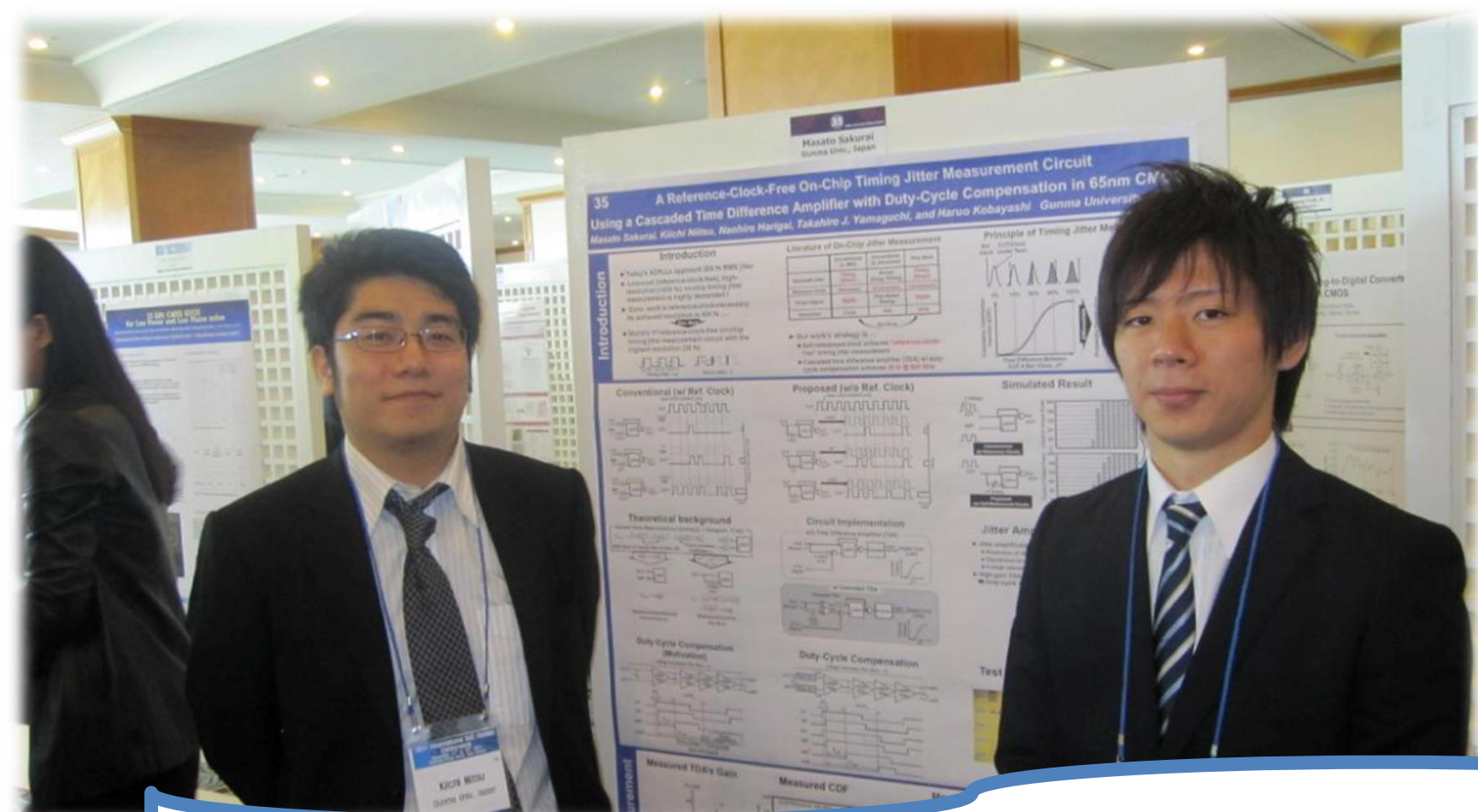
食事



研究打ち合わせ



その他の学会発表



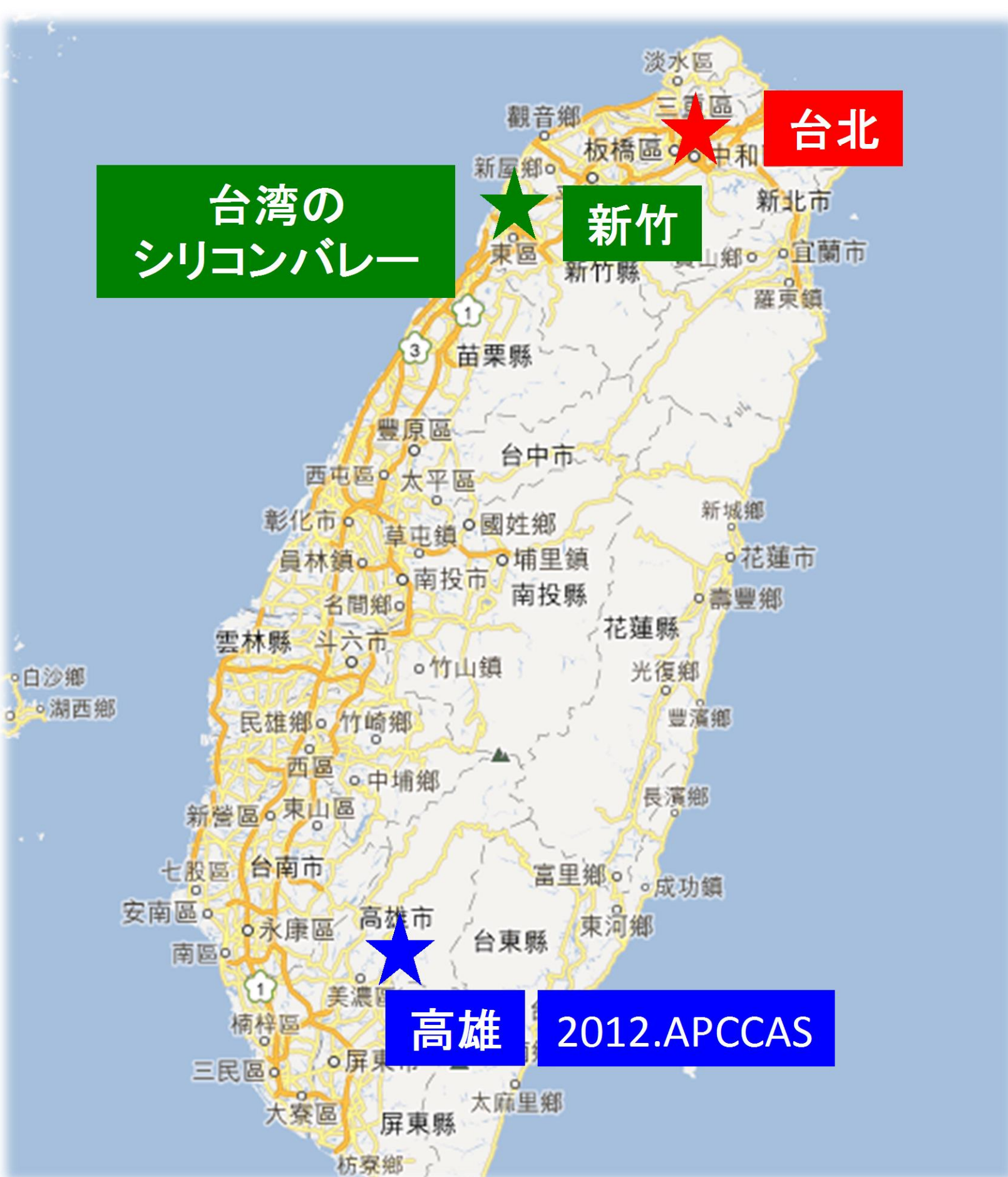
大韓民国 済州島(2011年)



インドネシア バリ島(2011年)



マレーシア クアラルンプール(2010年)



台湾,台北



台湾の大学訪問

国立台湾大学

台湾No.1



国立交通大学

台湾No.2

ポルトガル

アメリカ,シアトル

塩野谷 浩生 君

ICNSP 2009, Lisbon, Portugal

藤井 覚 君

渡辺 隆之 君

ICC 2011, Seattle, Washington, USA

発表会場

378m

高雄85大樓

台湾,高雄

発表風景

Chair man

メイン会場



賑々風景

小籠包



集合写真



その他



スペイン



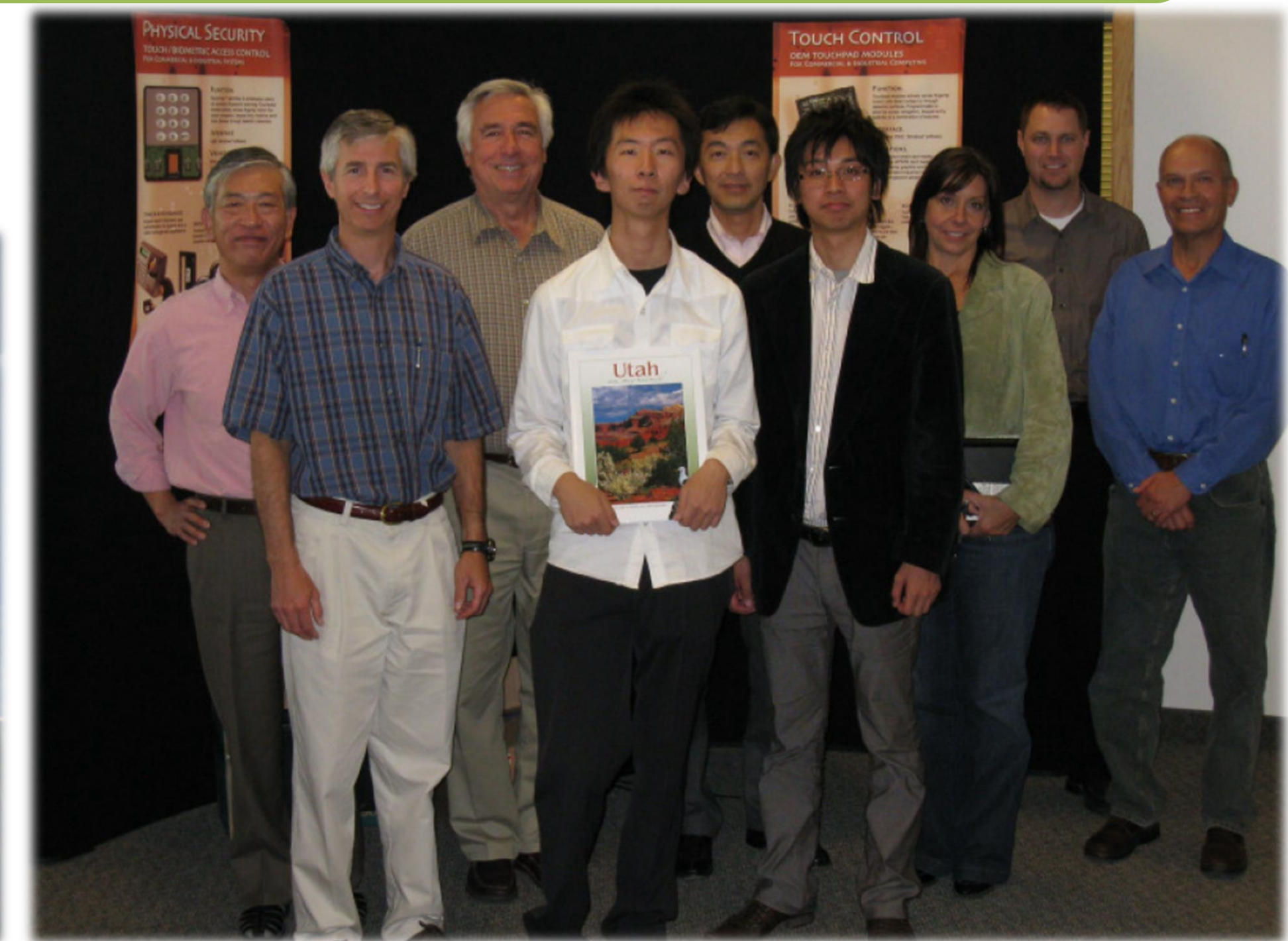
コロッセオ



バレンシア駅



ソルトレイク, Cirque社にてインターンシップ



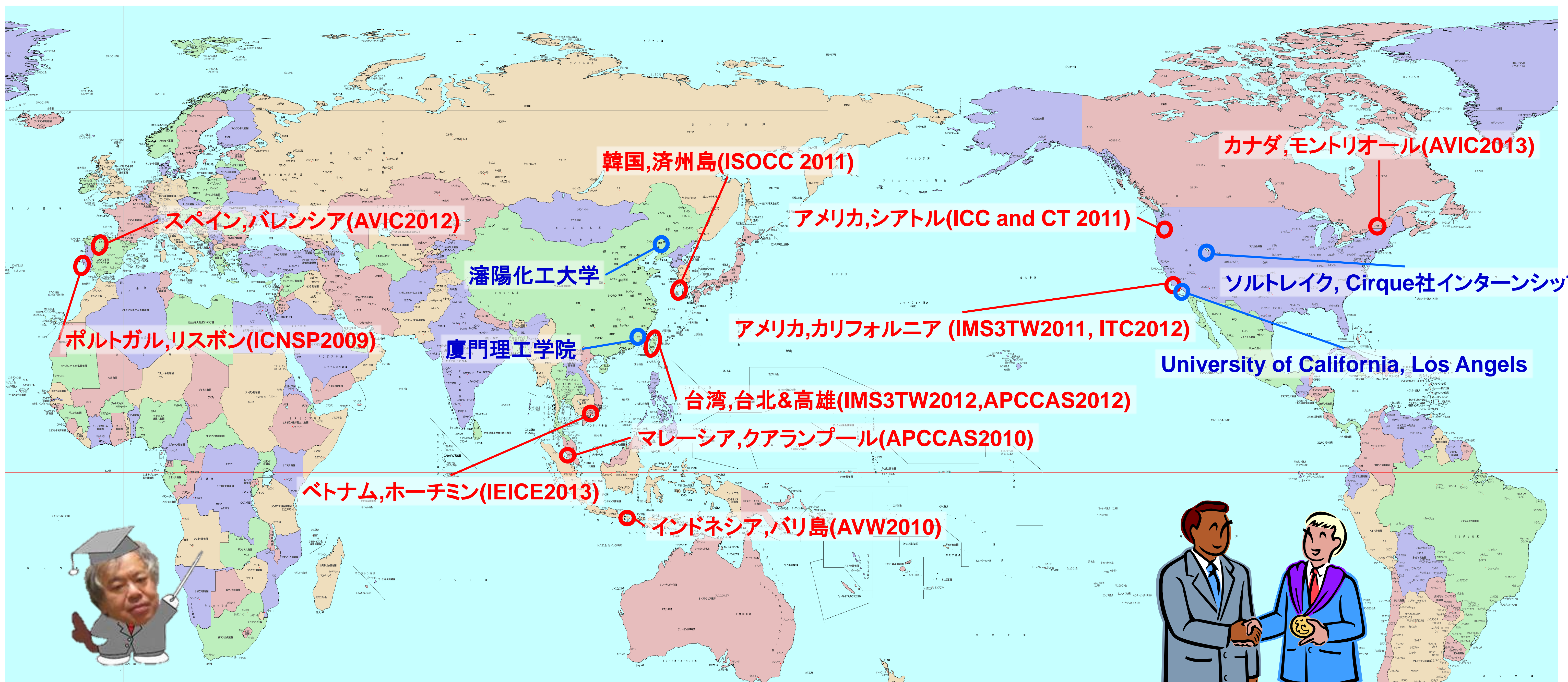
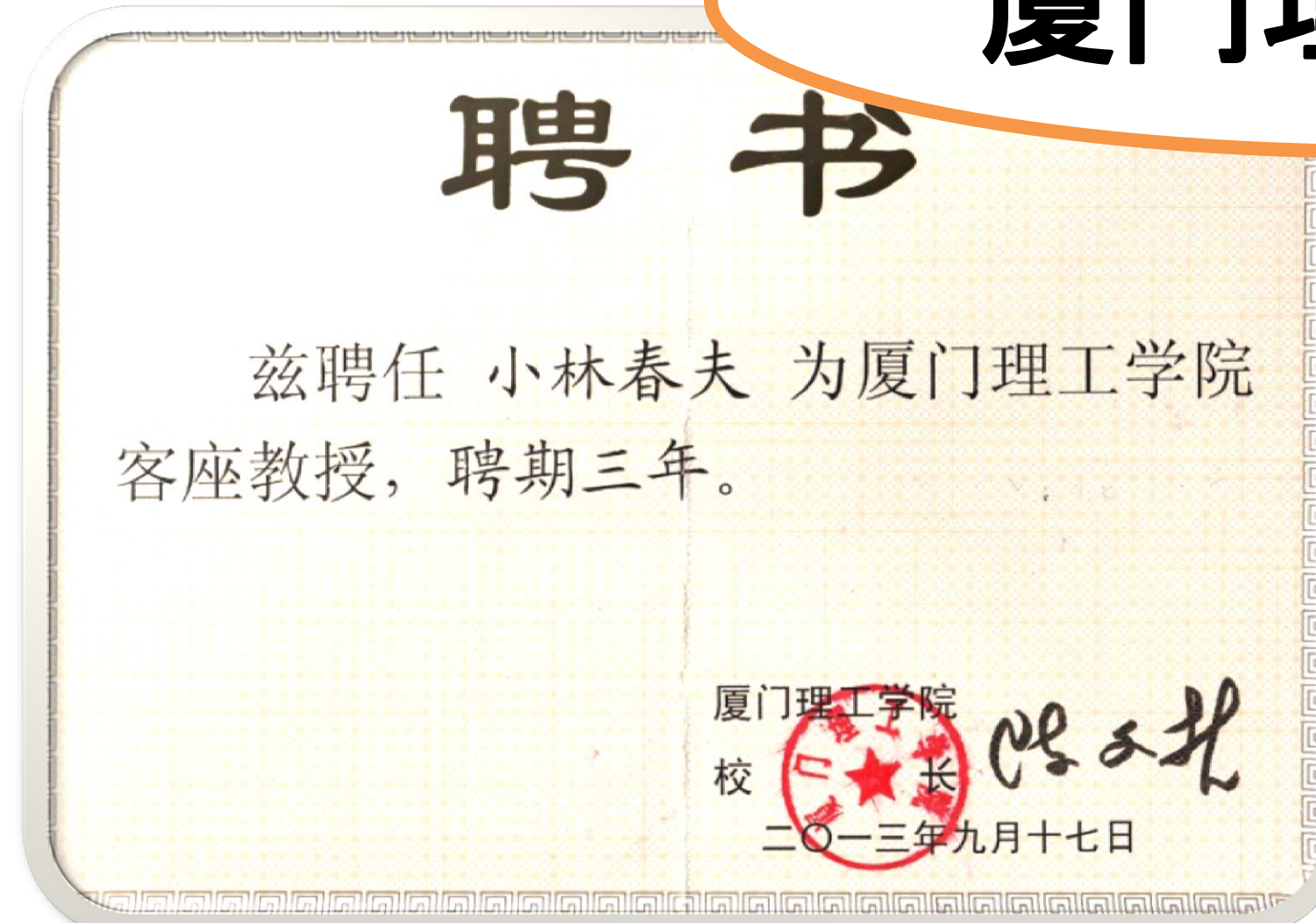
姉妹校との交流



瀋陽化工大学



廈門理工学院



Electrical and Electronic Engineering Sans Frontières

※国境なき電気電子工学

2013年

IEEE International Test Conference, Poster Session, Poster No. 3, Anaheim, CA (Sept. 11, 2013)

- D. Hirabayashi, Y. Osawa, N. Harigai, H. Kobayashi, O. Kobayashi, K. Niitsu, T. Yamaguchi, N. Takai,, "Phase Noise Measurement with Sigma-Delta TDC"

IEEE 3rd International Workshop on Test and Validation of High-Speed Analog Circuits, Anaheim, CA (Sept. 12-13, 2013).Preliminary Program

- Yuta Arakawa, Yusuke Oosawa, Haruo Kobayashi, Osamu Kobayashi, "Linearity Improvement Technique of Multi-bit Sigma-Delta TDC for Timing Measurement"

2013 International Conference on Analog VLSI Circuits, Montreal, Canada, (October 16-18, 2013)

- Toru Kamiyama, Nobukazu Takai, Haruo Kobayashi and Biswas Sumit Kumar, "Current-to-Time Converter Circuit for High Precision System Containing HysteresisCharacteristics"

The 4th IEICE International Conference on Integrated Circuits Design and Verification, Ho Chi Minh City, Vietnam (Nov. 15-16, 2013) 11件発表予定

2012年

IEEE International Mixed-Signals, Sensors, and Systems Test Workshop,Taipei, Taiwan (May 2012)

- Keisuke Kato, Fumitaka Abe, Kazuyuki Wakabayashi, Takafumi Yamada,Haruo Kobayashi, Osamu Kobayashi, Kiichi Niitsu, "Low-IMD Two-Tone Signal Generation for ADC Testing"
- Satoshi Uemori, Masamichi Ishii, Haruo Kobayashi, Yuta Doi, Osamu Kobayashi, Tatsuji Matsuura,Kiichi Niitsu, Fumitaka Abe, Daiki Hirabayashi, "Multi-bit Sigma-Delta TDC Architecture for Digital Signal Timing Measurement"

International Conference on Analog VLSI Circuits,Valencia, Spain (Oct. 24-26, 2012)

- Shintaro Takagi, Biswas Sumit Kumar, Nobukazu Takai, Haruo Kobayashi, "Successive-Approximation-Register Digital Low-Dropout Regulator (SAR digital LDO) with Fast Transient-Response and No Output Ripple"

IEEE International Test Conference, (Panel 2 Poster), Anaheim, CA (Nov. 2012)

- K Kato, M. Murakami, F. Abe, Y. Arai, H. Kobayashi, T. Matsuura,S. Mohyar, K. Ramin, O. Kobayashi, K. Niitsu, N. Takai, "Low-Cost High-Quality Signal Generation for ADC Testing"

IEEE Asia Pacific Conference on Circuits and Systems, Kaohsiung, Taiwan (Dec. 2012)

- Guanglei Jin, Hao Chen, Chuan Gao, Yunpeng ZhangHaruo Kobayashi, Nobukazu Takai, Kiichi Niitsu, Khayrollah Hadidi, "Digitally-Contolled Gm-C Bandpass Filter"
- Satoshi Uemori, Masamichi Ishii, Haruo Kobayashi, Yuta Doi, Osamu Kobayashiy, Tatsuji Matsuura, Kiichi Niitsu,Yuta Arakawa, Daiki Hirabayashi, Yuji Yano, Tatsuhiko Gake, Nobukazu Takai, Takahiro J. Yamaguchi, "Multi-bit Sigma-Delta TDC Architecture with Self-Calibration"
- Yasunori Kobori, Qiulin Zhu, Murong Li, Feng Zhao, Zachary Nosker, Shu Wu, Shaiful N. Mohyar, Masanori Onozawa, Haruo Kobayashi, Nobukazu Takai, Kiichi Niitsu, Takahiro Odaguchi, Isao Nakanishi, Kenji Nemoto, Jun-ichi Matsuda, "Single Inductor Dual Output DC-DC Converter Design with Exclusive Control"
- Hong Gao, Lin Xing, Yasunori Kobori, Zhao Feng, Haruo Kobayashi, Shyunsuke Miwa, Atsushi Motozawa,Zachary Nosker, Kiichi Niitsu, Nobukazu Takai, Takahiro Odaguchi, Isao Nakanishi, Kenji Nemoto, Jun-ichi Matsuda, "DC-DC Converter with Continuous-Time Feed-Forward Sigma-Delta Modulator Control"

2011年

IEEE International Mixed-Signals, Sensors, and Systems Test Workshop,Santa Barbara, CA (May 2011)

- Kazuyuki Wakabayashi, Takafumi Yamada, Satoshi Uemori, Osamu Kobayashi,Keisuke Kato, Haruo Kobayashi, Kiichi Niitsu, Hiroyuki Miyashita, Shinya Kishigami,Kunihito Rikino, Yuji Yano, Tatsuhiko Gake, "Low-Distortion Single-Tone and Two-Tone Sinewave Generation Algorithms Using an Arbitrary Waveform Generator"
- Koji Asami, Takenori Tateiwa, Tsuyoshi Kurosawa, Hiroyuki Miyajima, Haruo Kobayashi, "Digitally-Assisted Compensation Technique for Timing Skew in ATE Systems"

Innovative Confinement Concepts and US-Japan Compact Torus Plasma Workshop, Aug. 16-19, 2011, Seattle, Washington, USA

- T. Watanabe, T. Ohshima, T. Takahashi, N. Fukumoto, N. Nagata, "Computation of neutral gas flow generation from a CT neutralization fuel-injector"

IEEE International Test Conference (poster session) , Anaheim, CA (Sept. 2011)

- T. Yamada, O. Kobayashi, K. Kato, K. Wakabayashi, H. Kobayashi,T. Matsuura, Y. Yano, T. Gake, K. Niitsu, N. Takai, T. J. Yamaguchi, "Low-Distortion Single-Tone and Two-Tone Sinewave Generation Using $\Sigma\Delta$ DAC"

University of California, Los Angelesにて短期留学体験(Sept. 2011)

IEEJ International Analog VLSI Workshop (AVW 2011), Bali, Indonesia (Nov. 2011)

- Zachary Nosker, Yasunori Kobori, Haruo Kobayashi, Kiichi Niitsu, Nobukazu Takai, Takeshi Oomori,Takahiro Odaguchi, Isao Nakanishi, Kenji Nemoto, Jun-ichi Matsuda, "A Small, Low Power Boost Regulator Optimized for Energy Harvesting Applications"
- Jiani Ye, Zachary Nosker, Kazuyuki Wakabayashi, Takuya Yagi, Nobukazu Takai, Kiichi Niitsu,Keisuke Kato, Takao Ootsuki, Osamu Yamamoto, Isao Akiyama, Haruo Kobayashi, "Architecture of High-Efficiency Digitally-Controlled Class-E Power Amplifier"

IEEE Asian Solid-State Circuits Conference (A-SSCC 2011), Jeju, Korea (Nov. 2011)

- Kiichi Niitsu, Masato Sakurai, Naohiro Harigai, Takahiro J. Yamaguchi, Haruo Kobayashi, "An On-Chip Timing Jitter Measurement Circuit Using a Self-Referenced Clock anda Cascaded Time Difference Amplifier with Duty-Cycle Compensation"

International SoC Design Conference (ISOCC 2011), Jeju Korea (Nov. 2011)

- Masato Sakurai, Kiichi Niitsu, Naohiro Harigai, Daiki Hirabayashi, Daiki Oki, Takahiro J. Yamaguchi, and Haruo Kobayashi, "Analysis of Jitter Accumulation in Interleaved Phase Frequency Detectors for High-Accuracy On-Chip Jitter Measurements"
- Masato Sakurai, Kiichi Niitsu, Naohiro Harigai, Takahiro J. Yamaguchi, Haruo Kobayashi, "A Reference-Clock-Free On-Chip Timing Jitter Measurement Circuit Using a Cascaded Time Difference Amplifier with Duty-Cycle Compensation in 65nm CMOS"

2010年

IEEE International Test Conference, Austin, TX (Nov. 2010)

- Koji Asami, Hiroyuki Miyajima, Tsuyoshi Kurosawa, Takenori Tateiwa, Haruo Kobayashi, "Timing Skew Compensation Technique using Digital Filter with Novel Linear Phase Condition"

IEEE Asia Pacific Conference on Circuits and Systems, Kuala Lumpur, Malaysia (Dec. 2010)

- Takuya Yagi, Kunihiko Usui, Tatsuji Matsuura, Satoshi Uemori, Yohei Tan, Satoshi Ito, Haruo Kobayashi, "Background Calibration Algorithm for Pipelined ADC with Open-Loop Residue Amplifier using Split ADC Structure"
- Tomohiko Ogawa, Haruo Kobayashi, Nobukazu Takai, Masao Hotta, Hao San, Tatsuji Matsuura, Akira Abe, Katsuyoshi Yagi, Toshihiko Mori,"Non-binary SAR ADC with Digital Error Correction for Low Power Applications"
- Satoshi Ito, Shigeyuki Nishimura, Haruo Kobayashi, Satoshi Uemori, Yohei Tan,Nobukazu Takai, Takahiro J. Yamaguchi, Kiichi Niitsu, "Stochastic TDC Architecture with Self-Calibration"
- Tomohiko Ogawa, Haruo Kobayashi, Youhei Tan, Satoshi Ito, Satoshi Uemori, Nobukazu Takai, Kiichi Niitsu,Takahiro J. Yamaguchi, Tatsuji Matsuura,Nobuyoshi Ishikawa"SAR ADC That is Configurable to Optimize Yield"
- Satoshi Uemori, Takahiro J. Yamaguchi, Satoshi Ito, Yohei Tan, Haruo Kobayashi, Nobukazu Takai,Kiichi Niitsu, Nobuyoshi Ishikawa "ADC Linearity Test Signal Generation Algorithm," IEEE Asia Pacific Conference on Circuits and Systems, Kuala Lumpur, Malaysia (Dec. 2010).
- Kenji Takahashi, Hajime Yokoo, Shunsuke Miwa, Kengo Tsushida, Hiroyuki Iwase, Kazuki Murakami, Nobukazu Takai, Haruo Kobayashi, Takahiro Odaguchi, Shigeki Takayama,Isao Fukai, Jun-ichi Matsuda,"Single Inductor DC-DC Converter with Bipolar Outputs using Charge Pump"

2009年

21st International Conference on Numerical Simulation of Plasmas 2009

- H. Shionoya, T. Takahashi, H. Itagaki, and T. Asai, "Electron current drive by rotating magnetic field in a radiation shielding plasma for a space elevator"
- S. Fujii, T. Ohshima, T. Takahashi, N. Fukumoto, and M. Nagata, "Monte-Carlo simulation on a neutralization process of an accelerated compact torus plasma"