

# Integration-type Time-to-Digital Converter Using Vernier Oscillators

Pengfei Zhang\*, K. Machida, Y. Sasaki, Y. Ozawa, A. Kuwana, H. Kobayashi

*Division of Electronics and Informatics, Gunma University*

Proposed circuit:

- Measurement of time between Start and Stop rising edges → digital output  $D_{out}$
- Inspired by integration-type ADC
- Simple circuit

