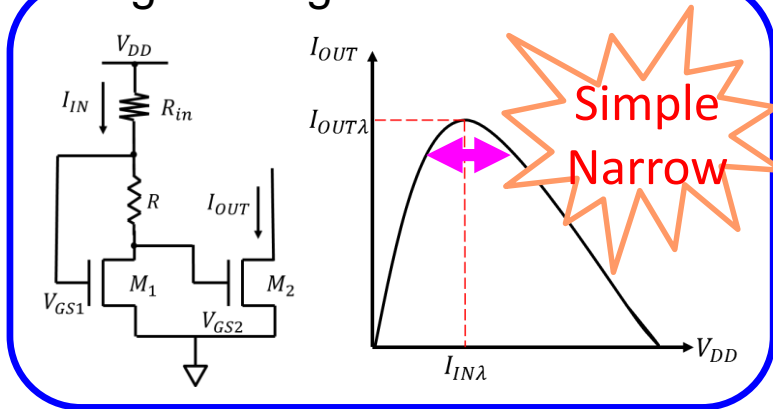


Experimental Verification of Improved Nagata Current Mirrors

M. Hirano, N. Kushita, Y. Motoshima, H. Harakawa, T. Oikawa
N. Tsukiji, T. Ida, Yukiko Shibasaki, H. Kobayashi
Gunma University, ASO Corp. Tokyo

Original Nagata current mirror



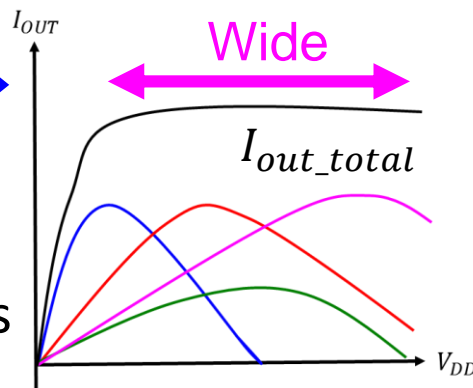
Problem

Every analog IC requires one reference current source stable against supply voltage variation.



Proposed circuit

Proposed:
multiple
different peaks



- ✓ Simple
- ✓ Stable
- with wider range than original
- ✓ Experimentally Verified

Fabricated chip

