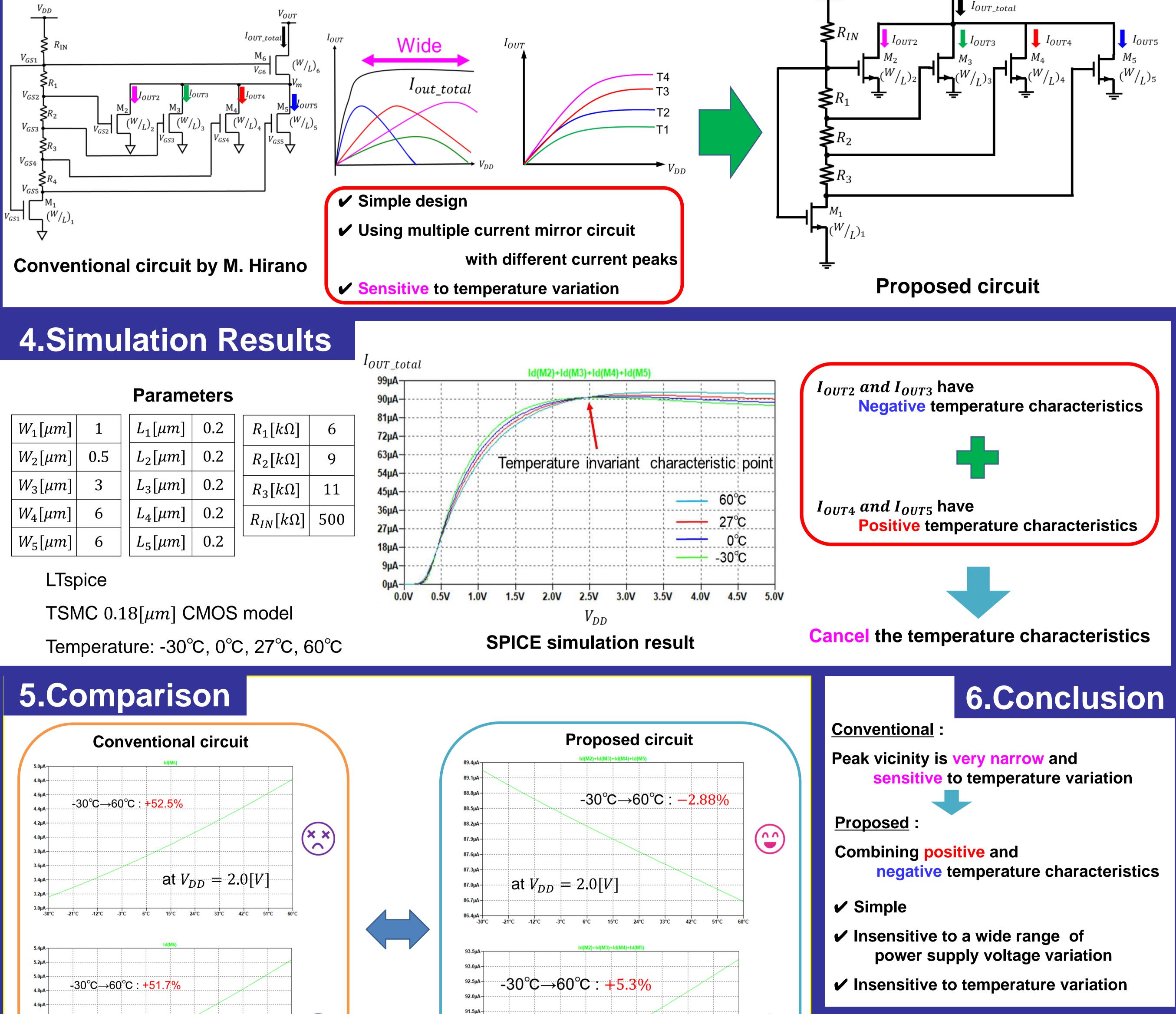
## Improved Nagata Current Mirror Insensitive to Temperature **1C-06** as well as Supply Voltage Takashi Hosono, N. Kushita, Y. Shibasaki, T. Ida, M. Hirano, A. Kuwana, H. Kobayashi, Y. Moroshima, H. Harakawa, T. Oikawa Gunma University, ASO Inc. t160d105@gunma-u.ac.jp 群馬大学 GUNMA UNIVERSITY 1.Objective 2.Background ✓ Simple **Original Nagata Current Mirror Circuit** Most analog ICs require Constant current for reference current/voltage source supply voltage variations IOUT ✓ Widely used in analog ICs Τ4 $I_{OUT}$ ΙΟυτλ Stable against **PVT** variation T3 Peak vicinity is very narrow $I_{OUT}$ **P** : **Process** Sensitive to temperature variation Narrow $M_1$ $M_2$ $V_{GS1}$ V : Supply voltage $V_{GS2}$ **T** : **Temperature** $V_{DD}$ $V_{DD}$ $I_{IN\lambda}$ Focus on supply voltage (V) **Peaking current characteristics MOS Nagata Current Peaking current** and temperature (T) with temperature characteristics **Mirror Circuit**

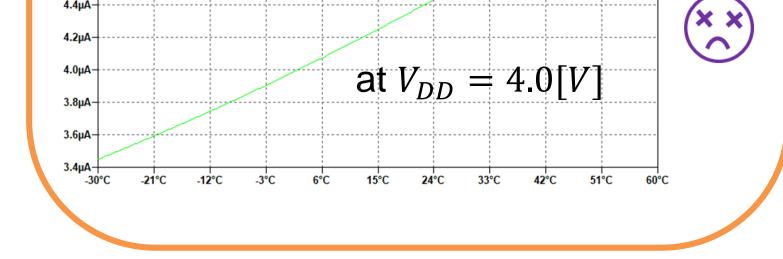
**3. Proposed Circuit** 

 $V_{DD}$ 

νουτ

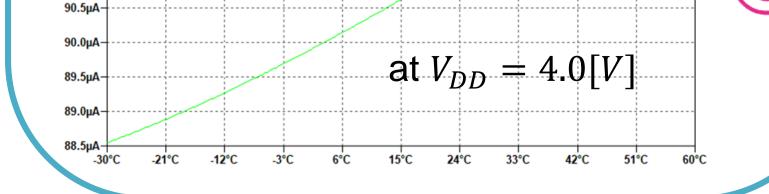


## Reference



4.4uA

4.2µA-



(nn)

## Proposed circuit is less sensitive to temperature

91.0µA

[1]M. Hirano, N. Tsukiji, H. Kobayashi, "Simple Reference **Current Source Insensitive to Power Supply Voltage Variation -Improved Minoru Nagata Current Source'', IEEE 13th International Conference on Solid-State and Integrated Circuit** Technology, Hangzhou, China (Oct. 2016).