

# Digital-to-Analog Converter Architectures Based on Polygonal and Prime Numbers

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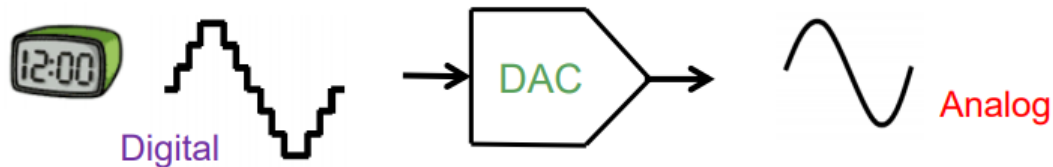
**Oyama National College** : Kazuyoshi Kubo

# Research Objective

- Interesting properties of integers



- Possibility of new configurations of DAC



# Outline

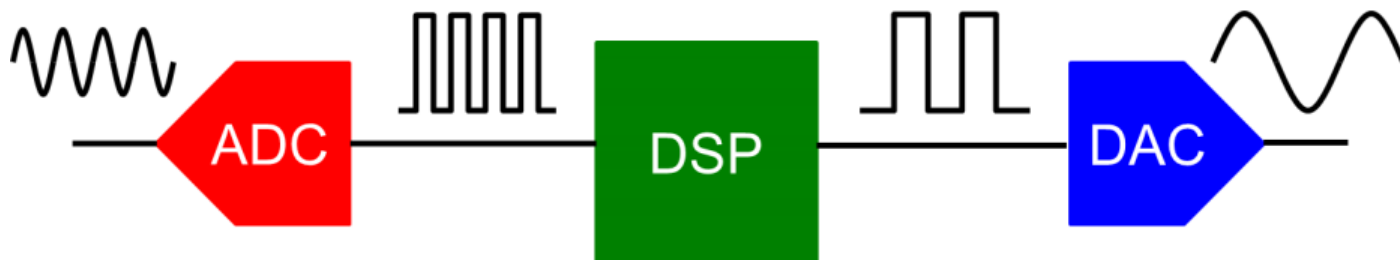
- **Research Background**
- **Triangular Number DAC**
- **Polygonal number DAC**
- **Prime number DAC**
- **Summary**

# Outline

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# Importance of ADC / DAC

- Rapid development of digital electronics technology
- A natural signal is analog



# DACs are Everywhere !



**Communication  
equipment**



**Electronic measuring  
instrument**



**Audio  
systems**

# Integer Theory and Electronic Circuit Design

Many interesting properties of Integers



Currently  
No Link

Electronic circuit designs

Our research here makes their links !



Carolus Fridericus Gauss  
(1777-1855)

**Integer theory is  
the queen of Mathematics**

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# What is Triangular Number ?

**Triangular Number:** 1, 3, 6, 10, 15, 21, 28, 36, 45, 55, 66, 78, 91, ..n(n+1)/2

									1					
							2	3						
						4	5	6						
						7	8	9	10					
					11	12	13	14	15					
				16	17	18	19	20	21					
			22	23	24	25	26	27	28					
		29	30	31	32	33	34	35	36					
	37	38	39	40	41	42	43	44	45					
	46	47	48	49	50	51	52	53	54	55				
56	57	58	58	60	61	62	63	64	65	66				
67	68	69	70	71	72	73	74	75	76	77	78			
79	80	81	82	83	84	85	86	87	88	89	90	91		

# Theory of Trigonometric Numbers

Any natural number

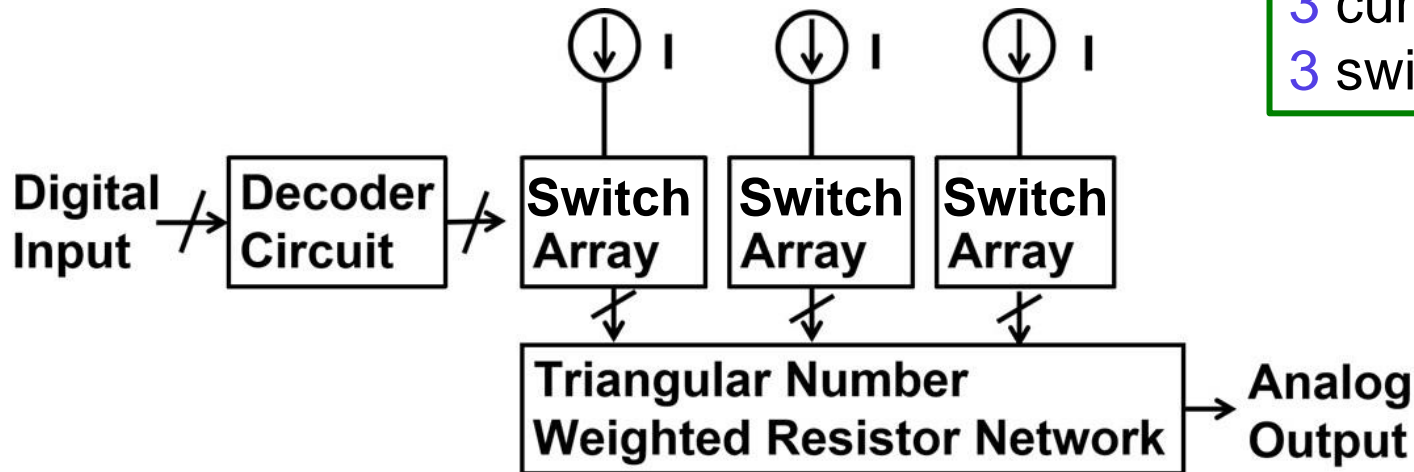


expressed by

Sum of 3 triangular numbers

<b>1:</b>	1	<b>16:</b>	1+15
<b>2:</b>	1+1	<b>17:</b>	1+1+15
<b>3:</b>	3	<b>18:</b>	3+15
<b>4:</b>	1+3	<b>19:</b>	1+3+15
<b>5:</b>	1+1+3	<b>20:</b>	10+10
<b>6:</b>	6	<b>21:</b>	21
<b>7:</b>	1+6	<b>22:</b>	1+21
<b>8:</b>	1+1+6	<b>23:</b>	1+1+21
<b>9:</b>	3+6	<b>24:</b>	3+21
<b>10:</b>	10	<b>25:</b>	10+15
<b>11:</b>	1+10	<b>26:</b>	1+10+15
<b>12:</b>	1+1+10	<b>27:</b>	1+10+21
<b>13:</b>	3+10	<b>28:</b>	28
<b>14:</b>	1+3+10	<b>29:</b>	1+28
<b>15:</b>	15	<b>30:</b>	1+1+28

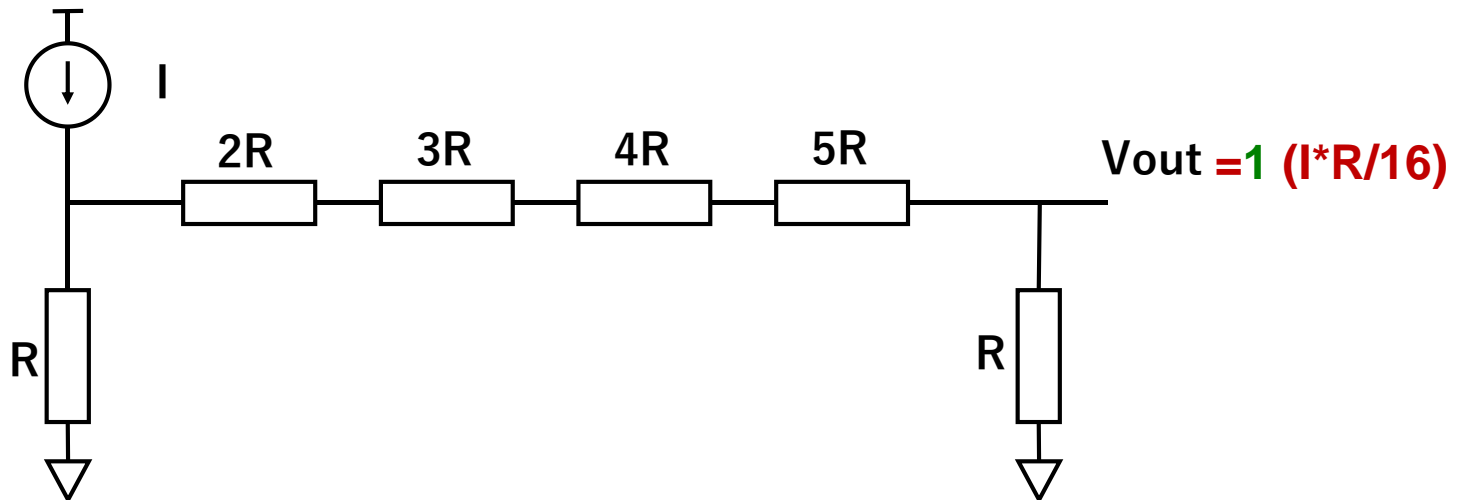
# Proposed Triangular Number DAC



3 current sources  
3 switch arrays

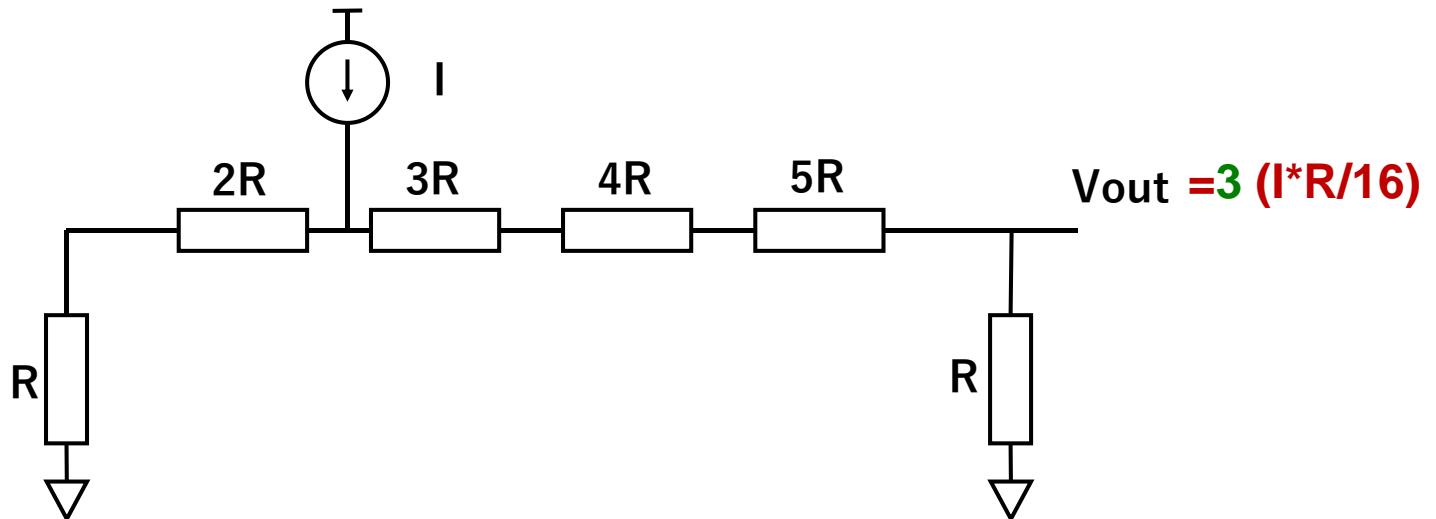
# Triangular Number Weighted Voltage (1)

**Triangular Number:** 1, 3, 6, 10, 15, 21, 28, 36, 45, 55, 66, 78, 91, .. $n(n+1)/2$



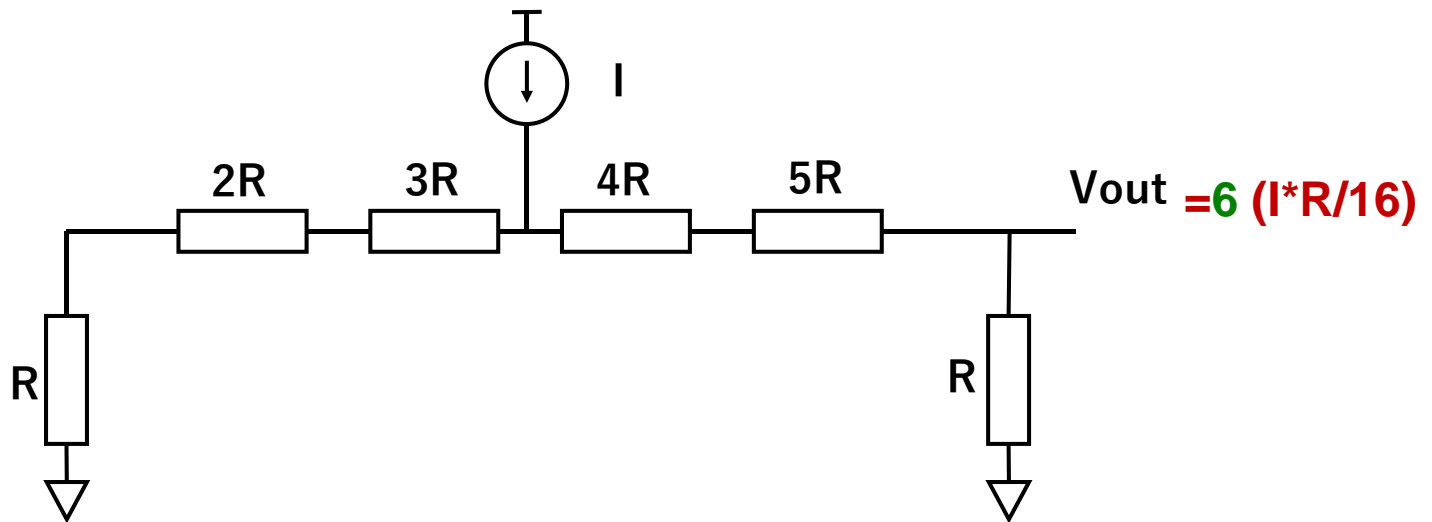
# Triangular Number Weighted Voltage (2)

**Triangular Number:** 1, 3, 6, 10, 15, 21, 28, 36, 45, 55, 66, 78, 91, .. $n(n+1)/2$



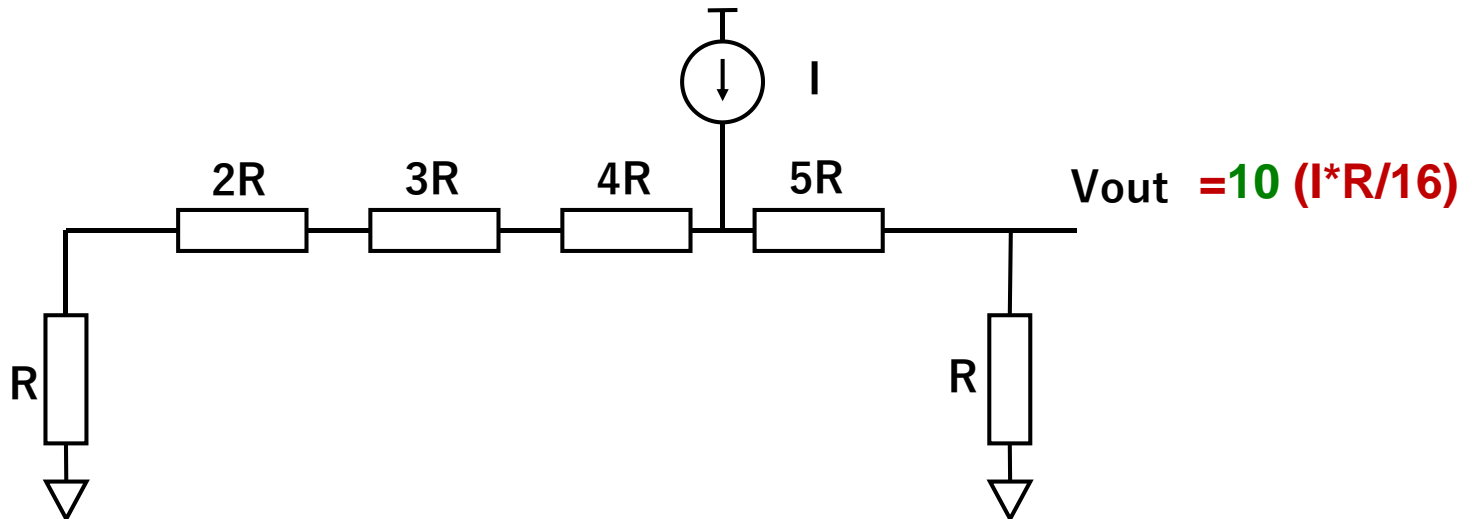
# Triangular Number Weighted Voltage (3)

Triangular Number: 1, 3, 6, 10, 15, 21, 28, 36, 45, 55, 66, 78, 91, .. $n(n+1)/2$



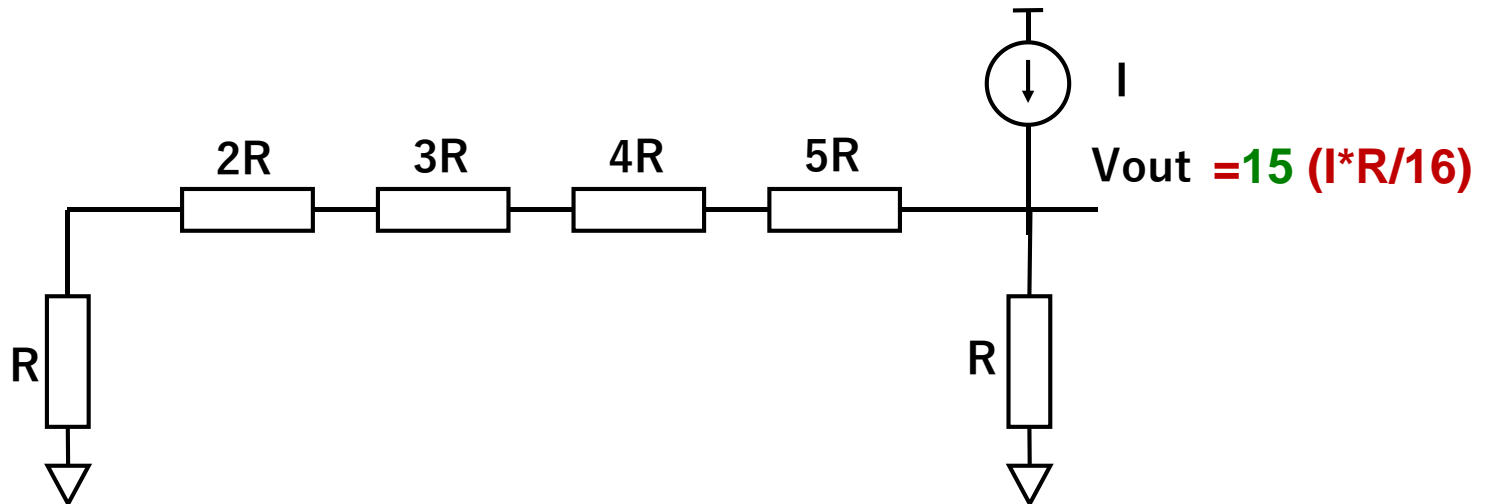
# Triangular Number Weighted Voltage (4)

Triangular Number: 1, 3, 6, **10**, 15, 21, 28, 36, 45, 55, 66, 78, 91, .. $n(n+1)/2$



# Triangular Number Weighted Voltage (5)

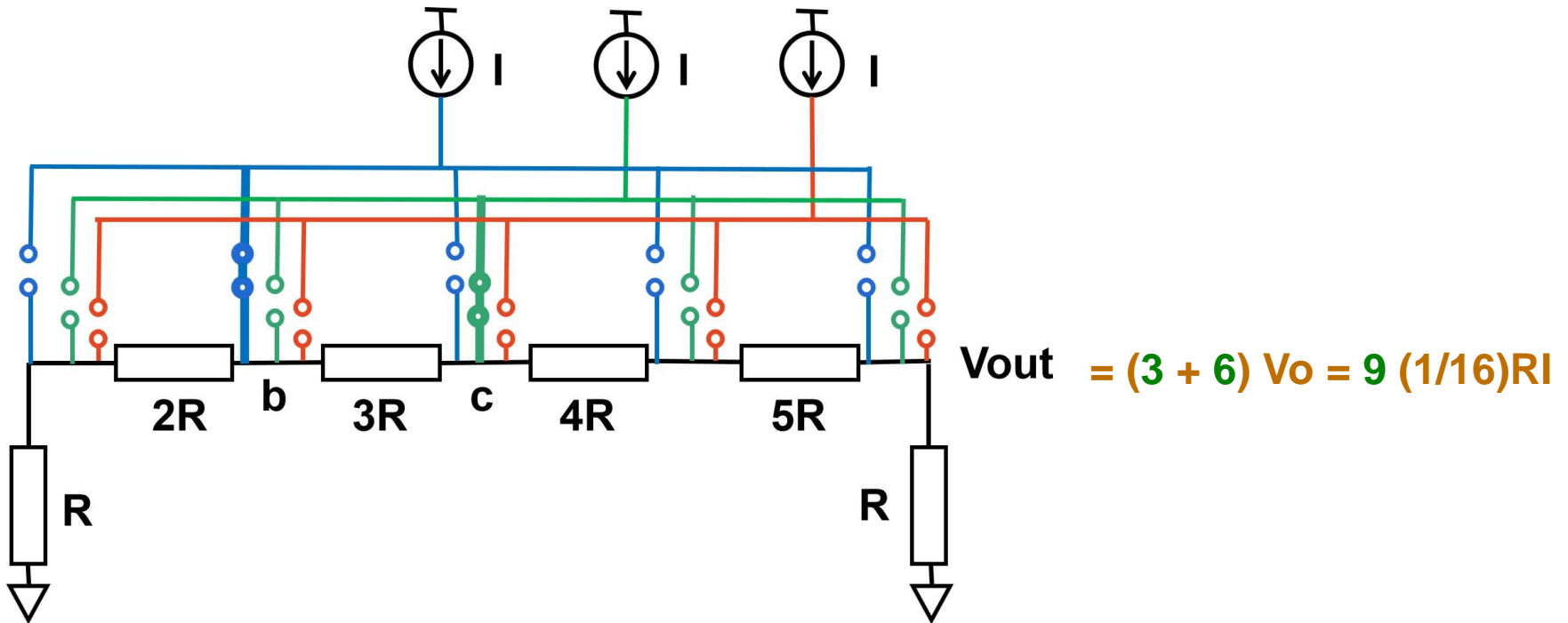
**Triangular Number:** 1, 3, 6, 10, 15, 21, 28, 36, 45, 55, 66, 78, 91, .. $n(n+1)/2$



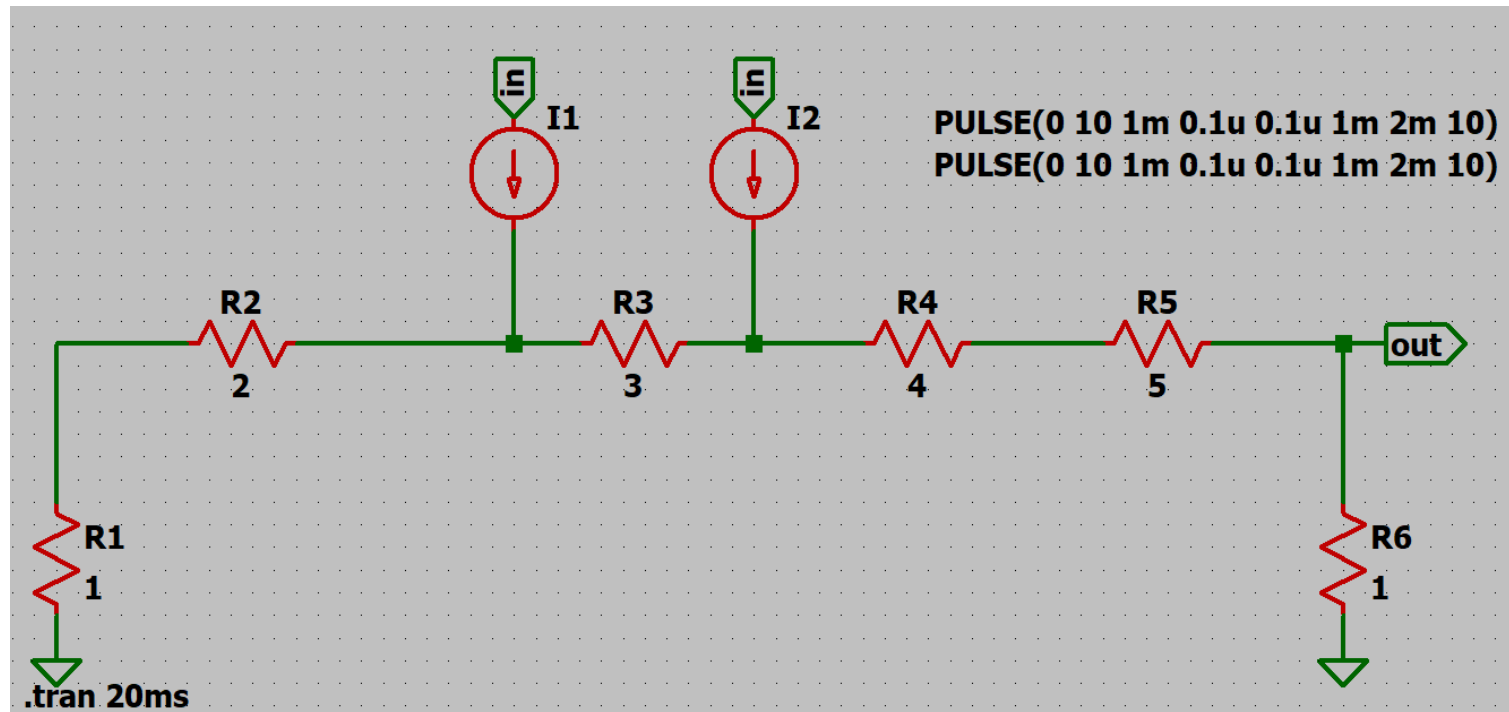


# When Digital Input is 9: Calculation

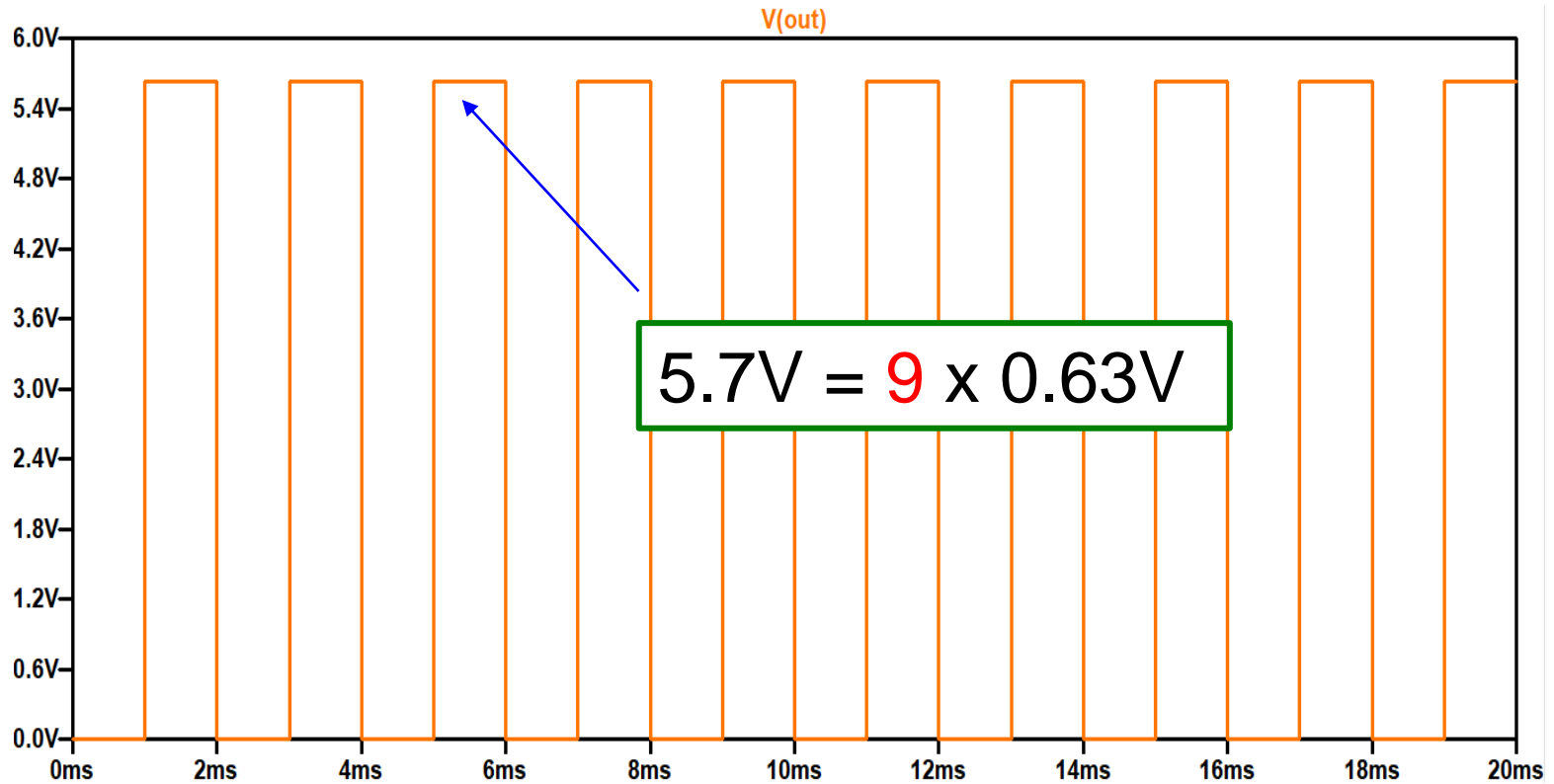
Triangular Number: 1, 3, 6, 10, 15, 21, 28, 36, 45, 55, 66, 78, 91, .. $n(n+1)/2$



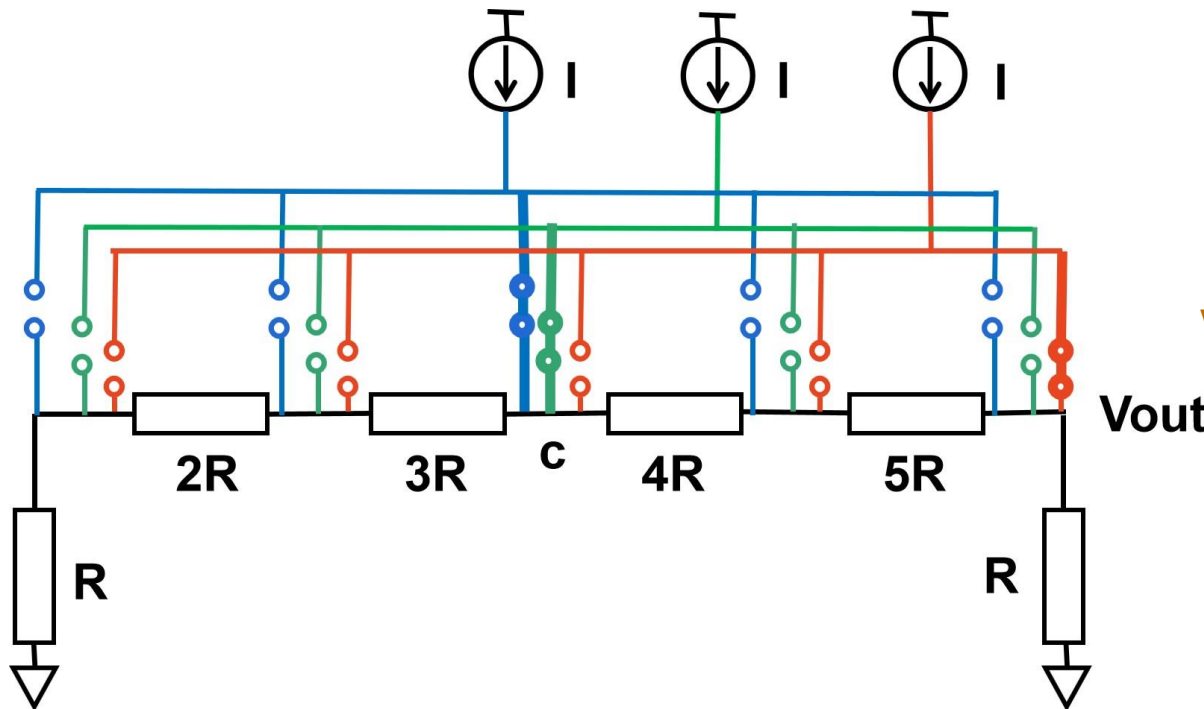
# When Digital Input is 9: Simulation



# When Digital Input is 9: Simulation Result

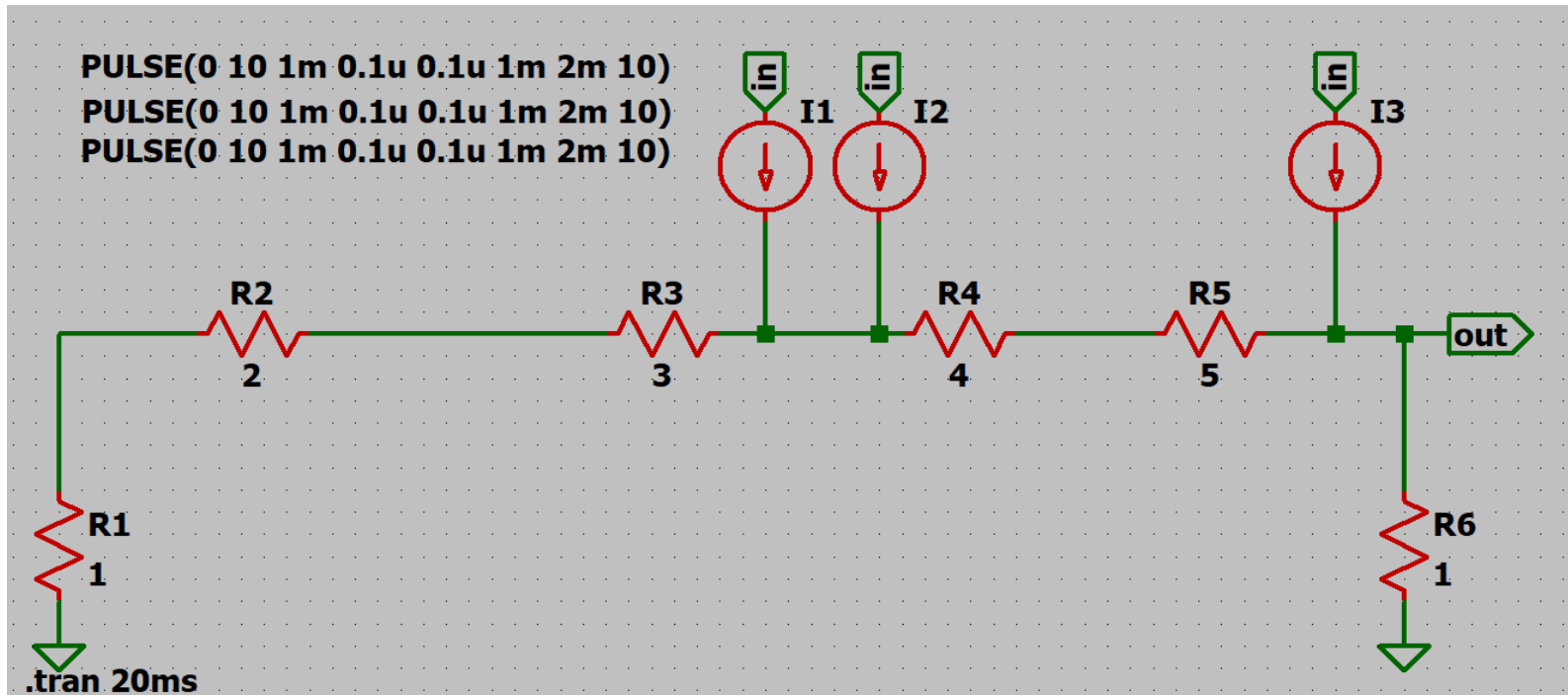


# When Digital Input is 27: Calculation

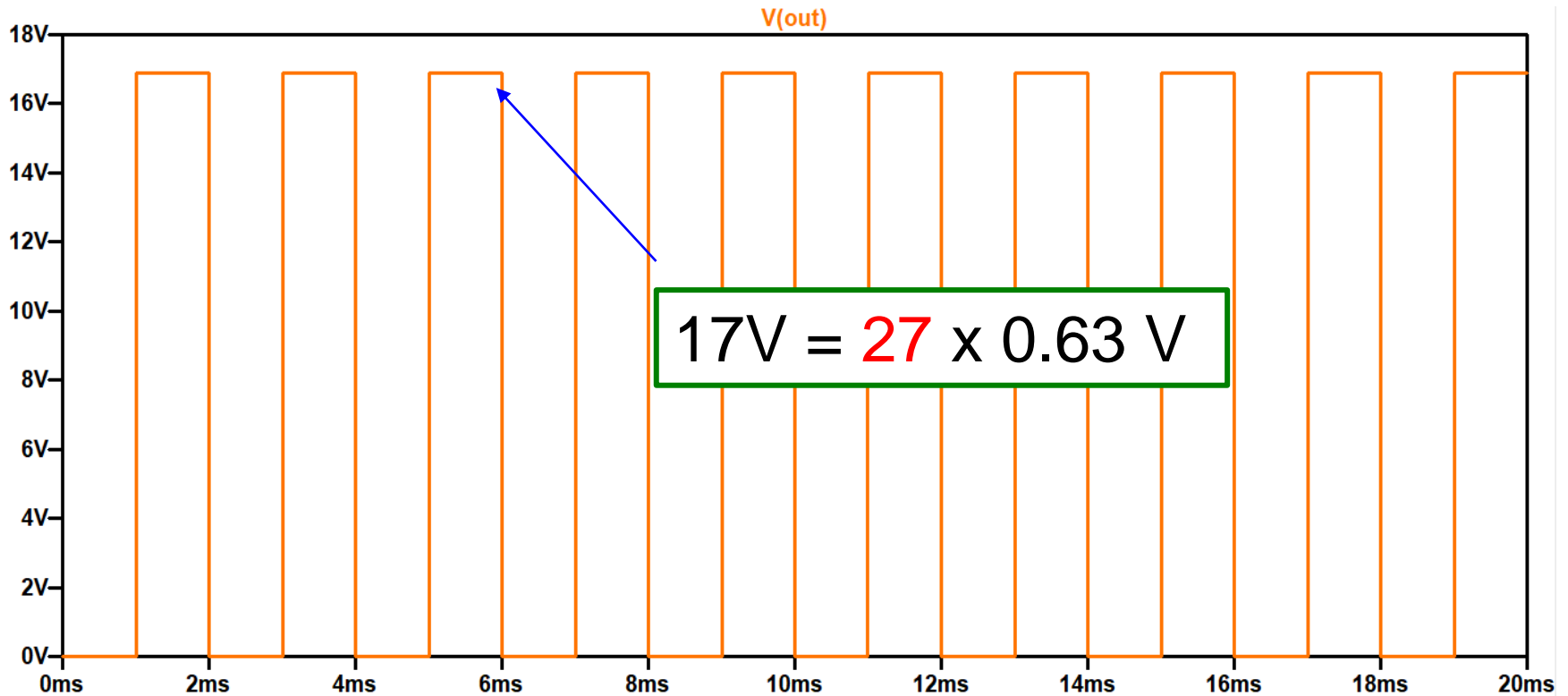


$$V_{out} = (6+6+15)V_o = 27V_o.$$

# When Digital Input is 27: Simulation



# When Digital Input is **27**: **Simulation Result**



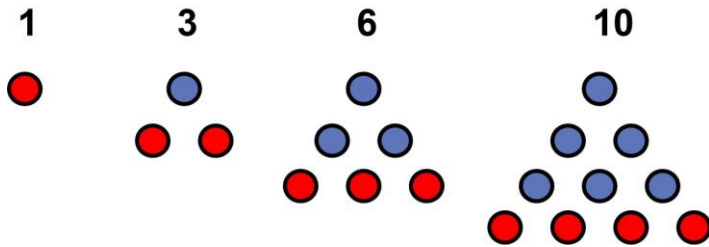
# Outline

- Background
- Triangular Number DAC
- **Polygonal Number DAC**
- Prime Number DAC
- Summary

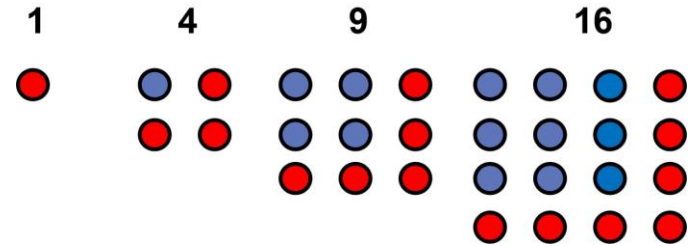
# Polygonal Number

## Polygonal number

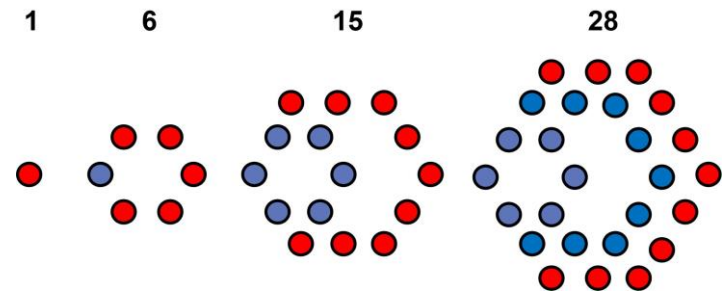
Represented as dots or pebbles arranged in the shape of a regular polygon.



(a) Triangular numbers.



(b) Square numbers.



(c) Hexagonal numbers.



# Polygonal Number Theorem

Any natural number



expressed by

Sum of **N** N-angular numbers

k-th of N-angular number,  $m(N, k)$  can be expressed by

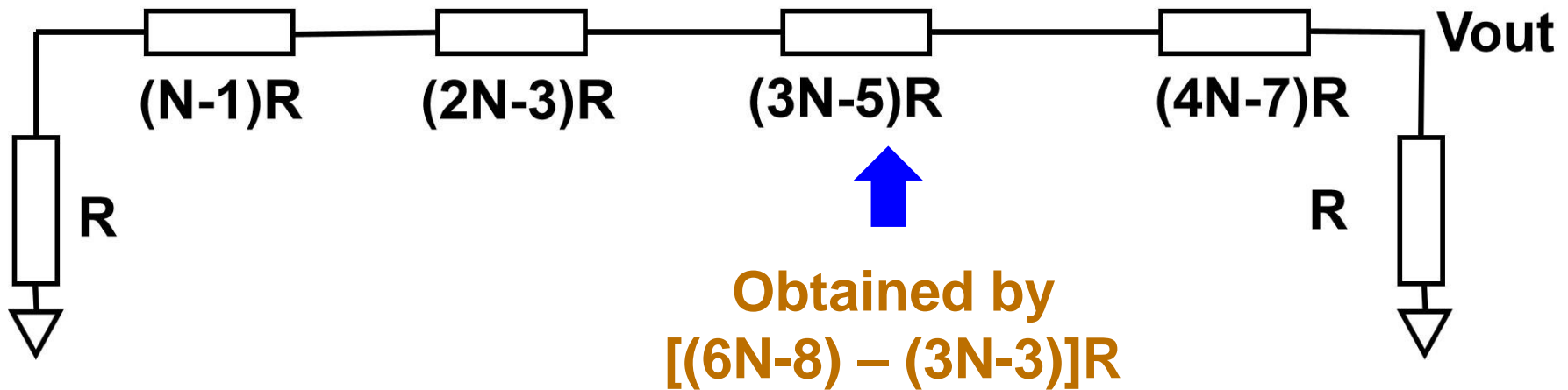
$$m(N, k) = (1/2) k [(N-2)k - (N-4)]$$

Then N-angular numbers are given by

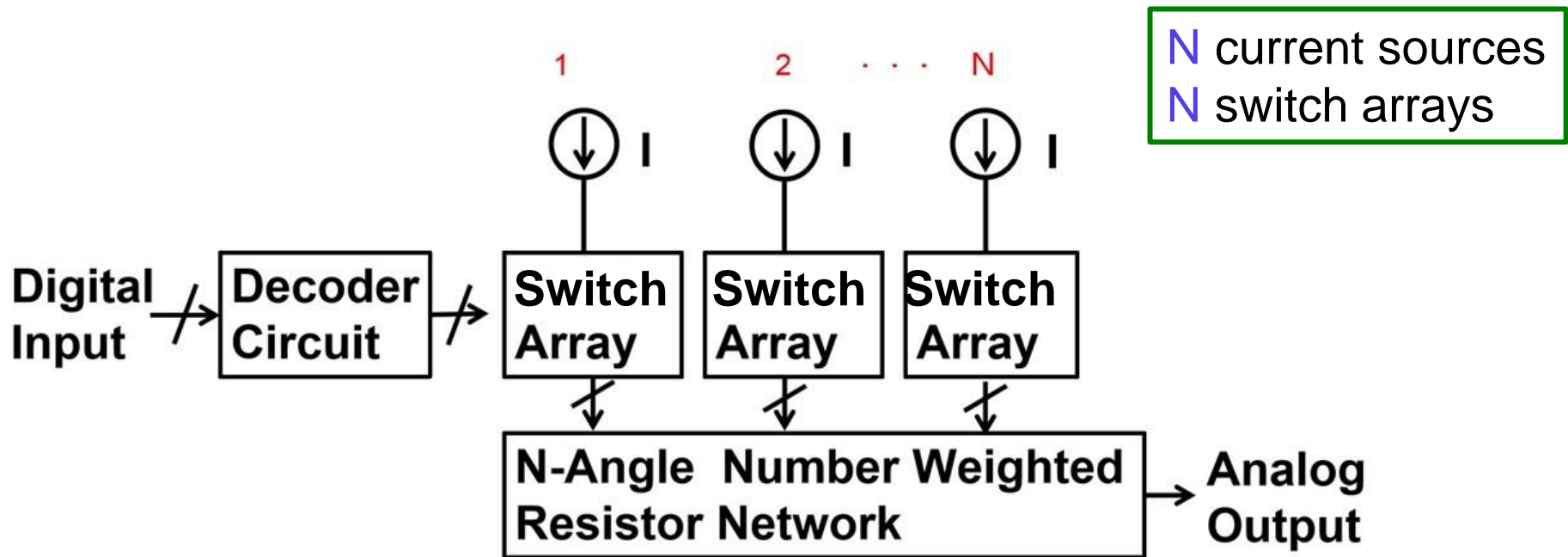
$$1, N, 3N-3, 6N-8, 10N-15, \dots$$

for  $k=1, 2, 3, 4, 5, \dots$

# N-angle Number Weighted Resistor Network

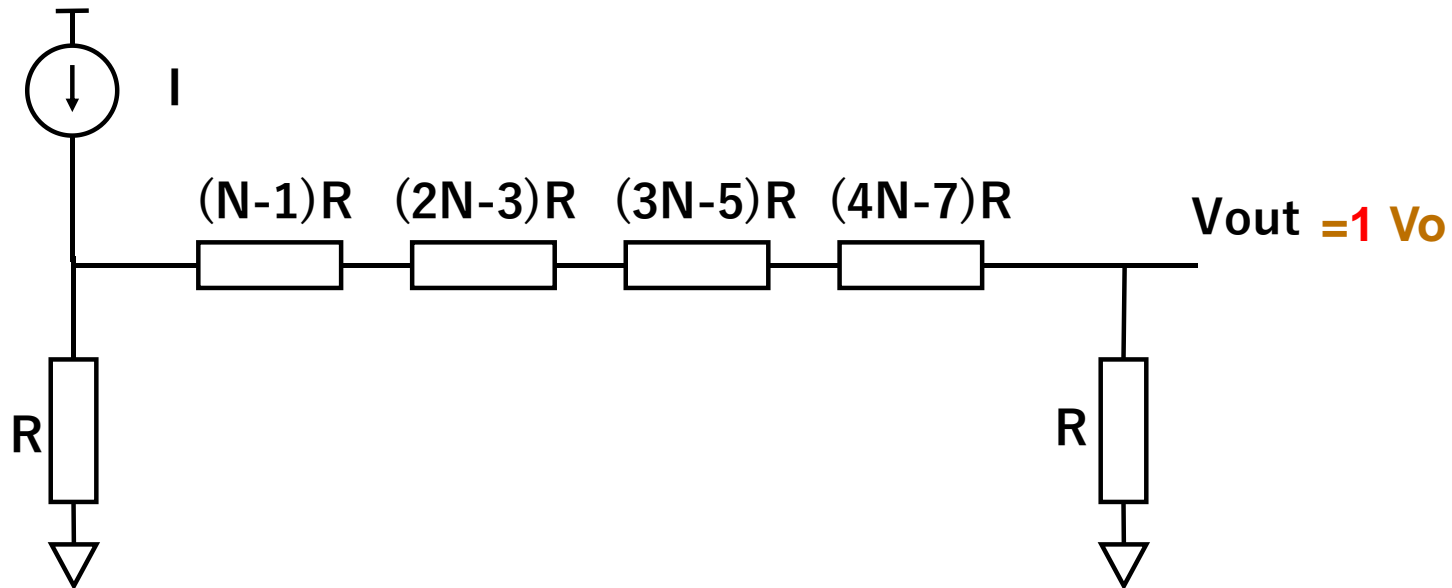


# DAC Configuration based on N-angular Number



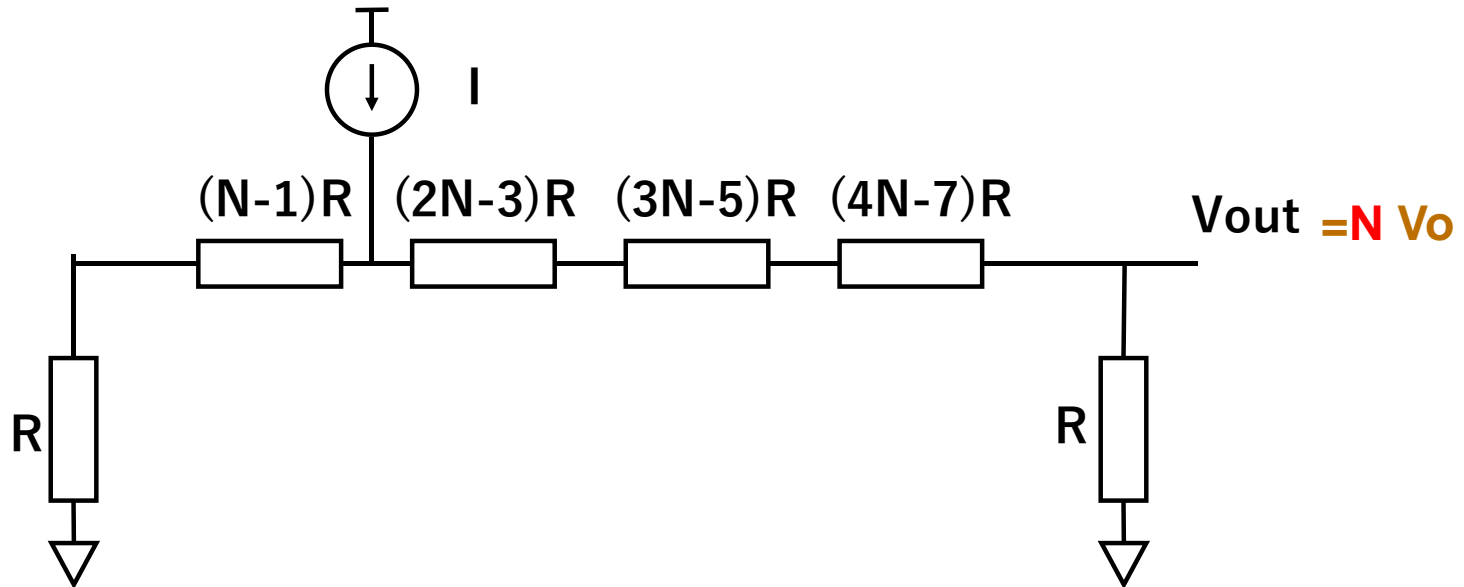
# N-angular Number Weighted Voltage (1)

N-angular Number: 1, N, 3N-3, 6N-8, 10N-15, ...



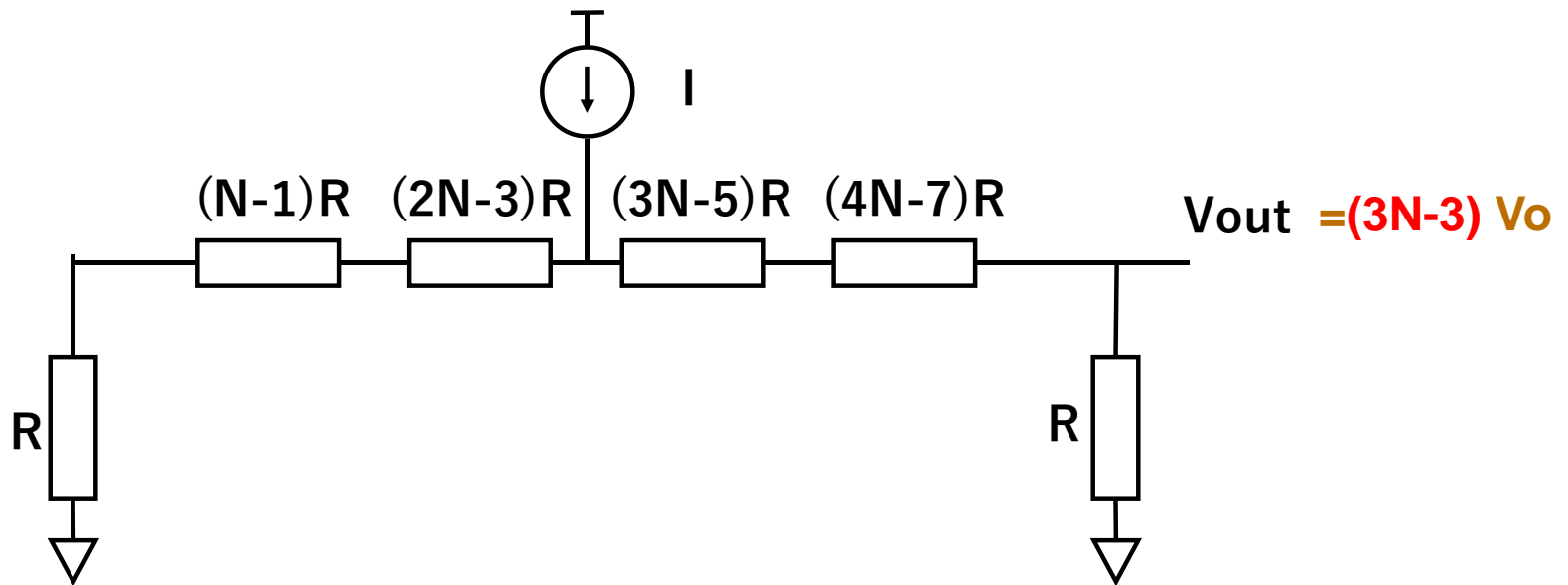
# N-angular Number Weighted Voltage (2)

N-angular Number: 1, **N**, 3N-3, 6N-8, 10N-15, ...



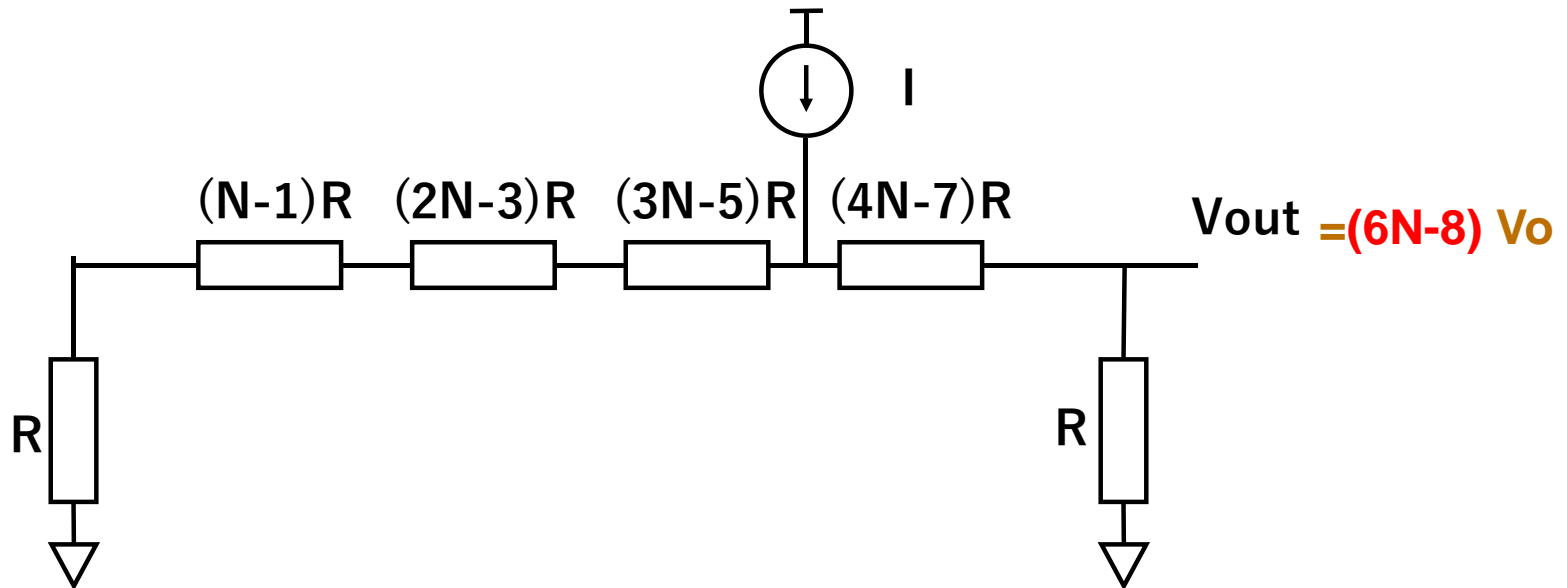
# N-angular Number Weighted Voltage (3)

N-angular Number: 1, N, **3N-3**, 6N-8, 10N-15, ...



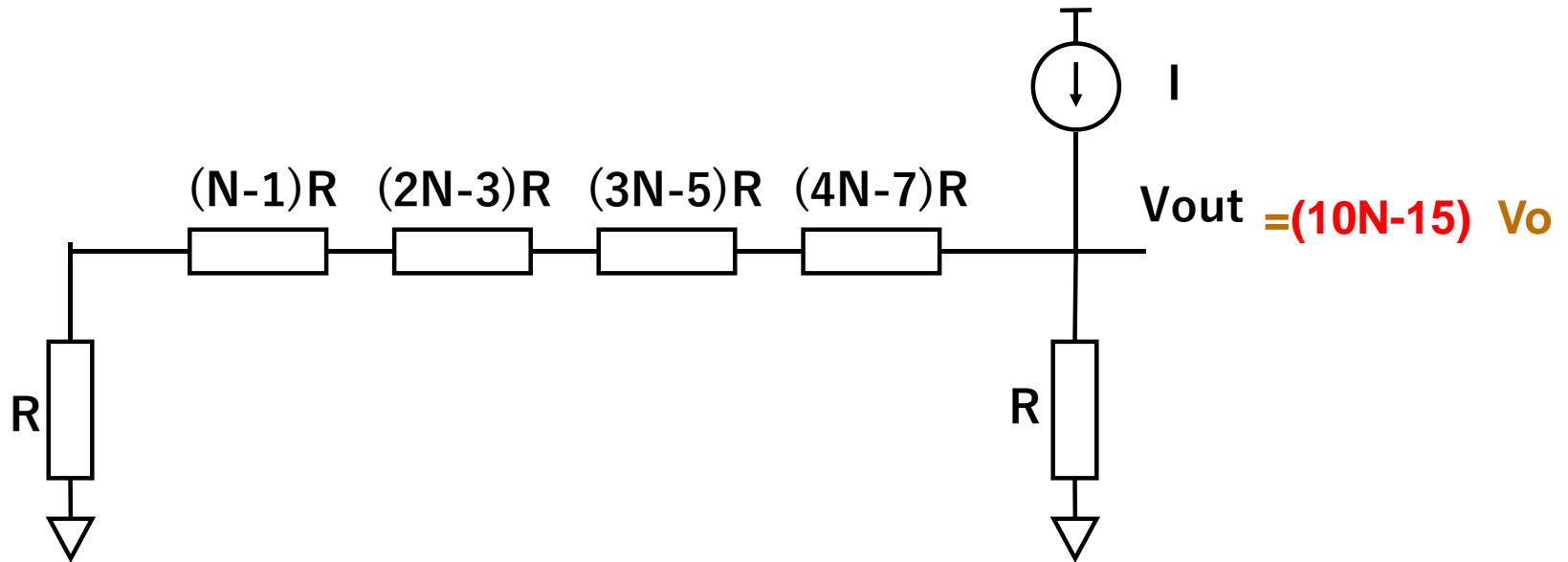
# N-angular Number Weighted Voltage (4)

N-angular Number: 1, N, 3N-3, **6N-8**, 10N-15, ...



# N-angular Number Weighted Voltage (5)

N-angular Number: 1, N, 3N-3, 6N-8, **10N-15**, ...





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# Goldbach's Conjecture

## Goldbach's Conjecture:

All even numbers can be represented by sum of two prime numbers.

+	2	3	5	7	11	13	17	19
2	4	5	7	9	13	15	19	21
3	5	6	8	10	14	16	20	22
5	7	8	10	12	16	18	22	24
7	9	10	12	14	18	20	24	26
11	13	14	16	18	22	24	28	30
13	15	16	18	20	24	26	30	32
17	19	20	22	24	28	30	34	36
19	21	22	24	26	30	32	36	38

# Prime Numbers

Prime numbers:

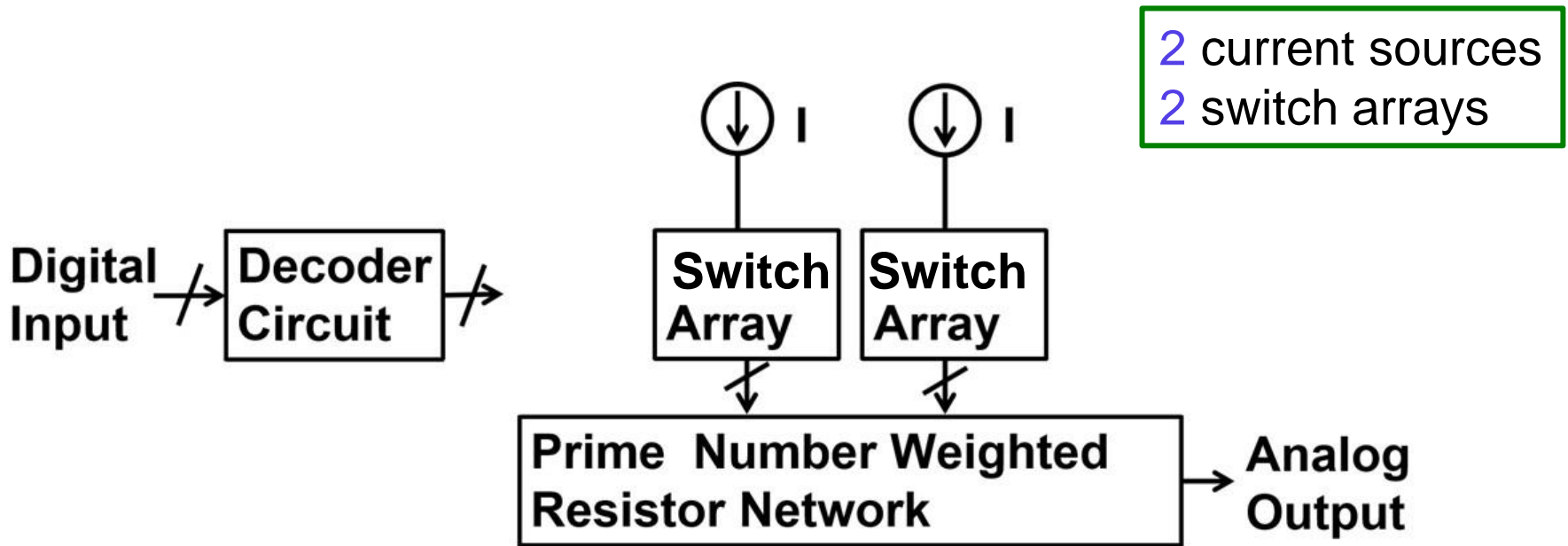
2, 3, 5, 7, 11, 13, 17, 19, 23, 29, .....

All even numbers are represented  
by two prime numbers

2:	2	32:	13+19
4:	2+2	34:	17+17
6:	3+3	36:	17+19
8:	3+5	38:	19+19
10:	3+7	40:	17+23
12:	5+7	42:	19+23
14:	7+7	44:	13+31
16:	5+11	46:	23+23
18:	7+11	48:	19+29
20:	7+13	50:	19+31
22:	11+11	52:	23+29
24:	11+13	54:	23+31
26:	13+13	56:	19+37
28:	11+17	58:	29+29
30:	13+17	60:	29+31

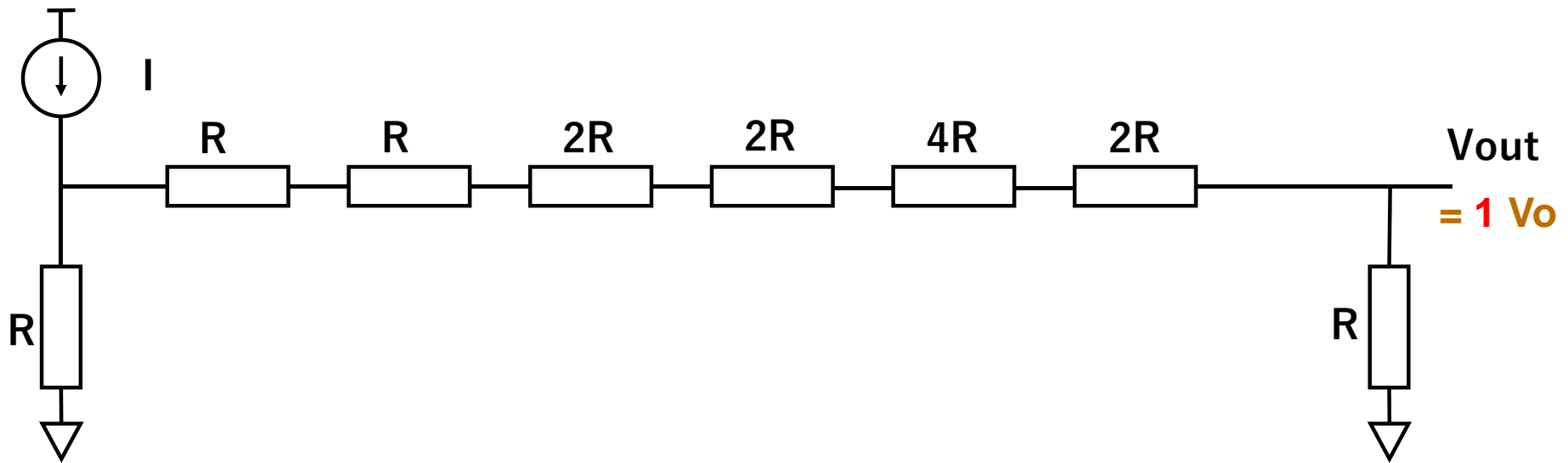


# Proposed Prime Number DAC



# Proposed Prime Number DAC Operation (1)

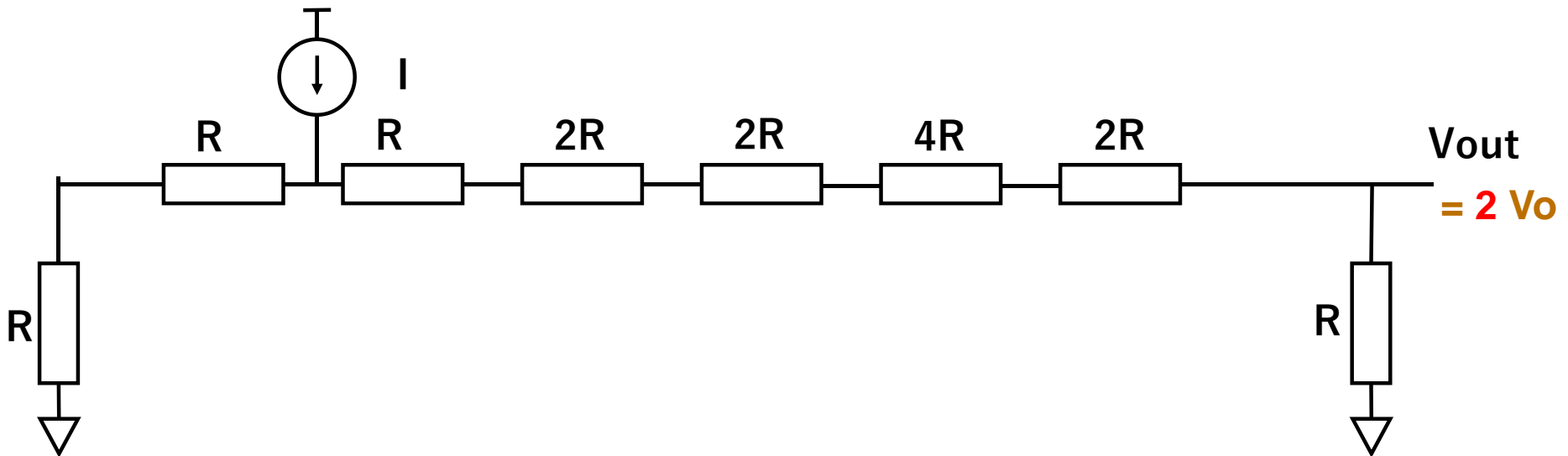
Prime numbers: 1, 2, 3, 5, 7, 11, 13, 17, 19, 23, 29, .....



Here  $V_o = (1/14)RI$

# Proposed Prime Number DAC Operation (2)

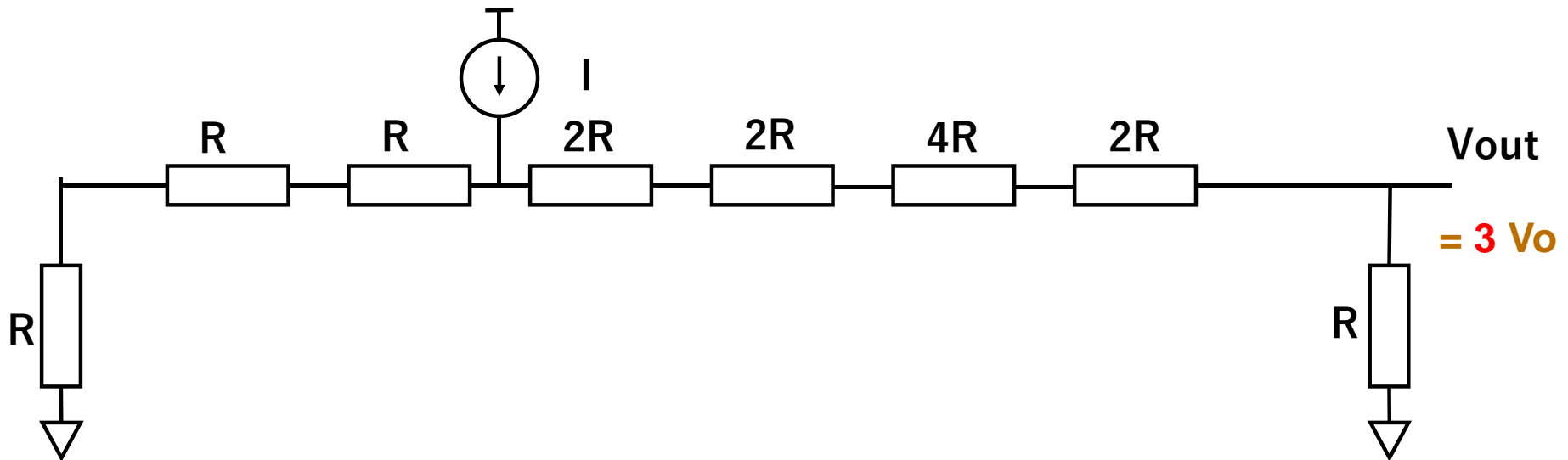
Prime numbers: 1, 2, 3, 5, 7, 11, 13, 17, 19, 23, 29, .....



Here  $V_o = (1/14)RI$

# Proposed Prime Number DAC Operation (3)

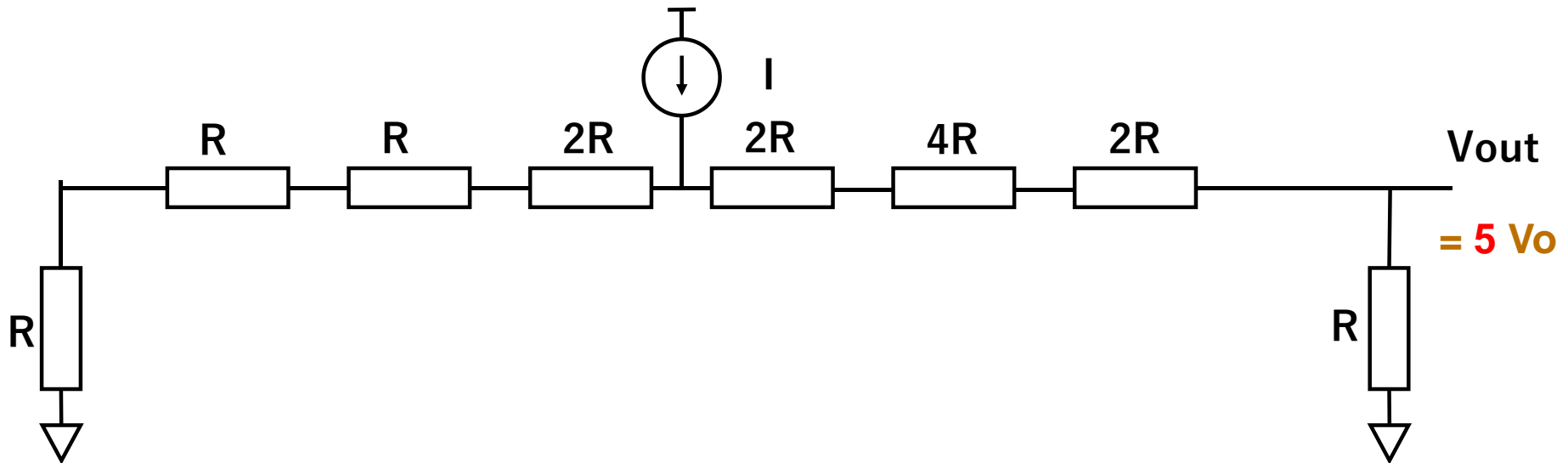
Prime numbers: 1, 2, 3, 5, 7, 11, 13, 17, 19, 23, 29, .....



Here  $V_o = (1/14)RI$

# Proposed Prime Number DAC Operation (4)

Prime numbers: 1, 2, 3, 5, 7, 11, 13, 17, 19, 23, 29, .....

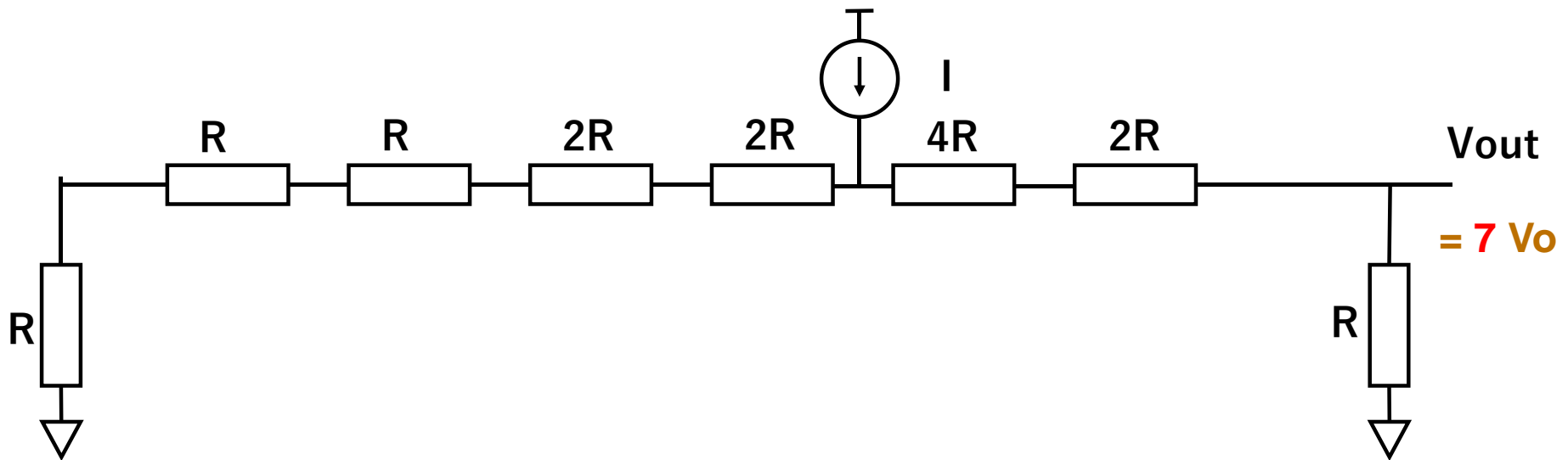


Here  $V_o = (1/14)RI$



# Proposed Prime Number DAC Operation (5)

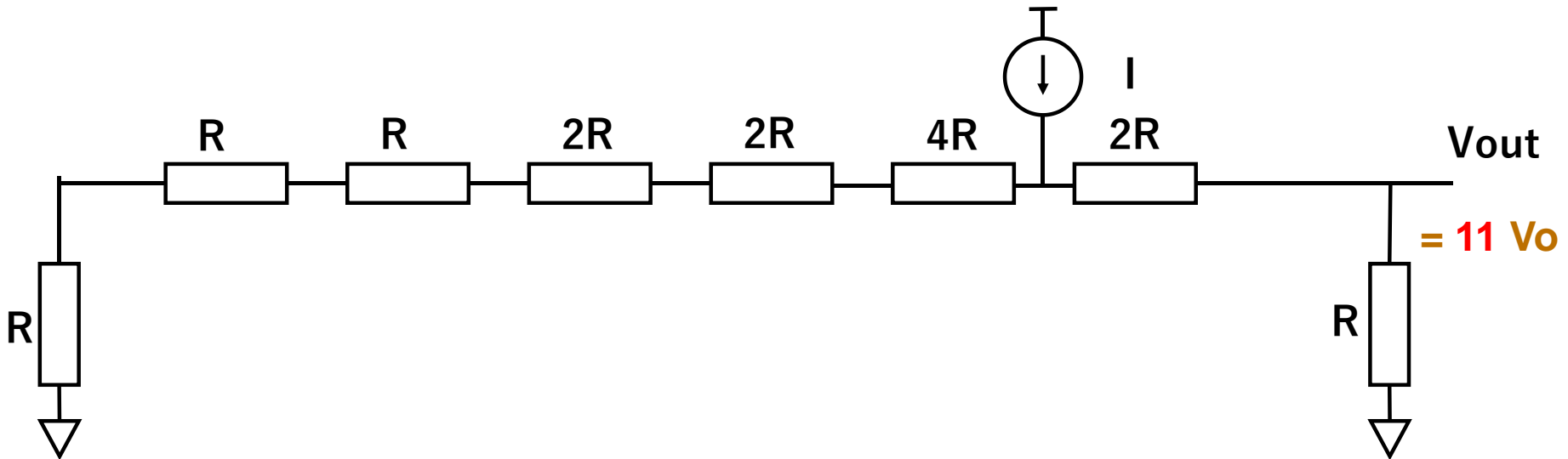
Prime numbers: 1, 2, 3, 5, 7, 11, 13, 17, 19, 23, 29, .....



Here  $V_o = (1/14)RI$

# Proposed Prime Number DAC Operation (6)

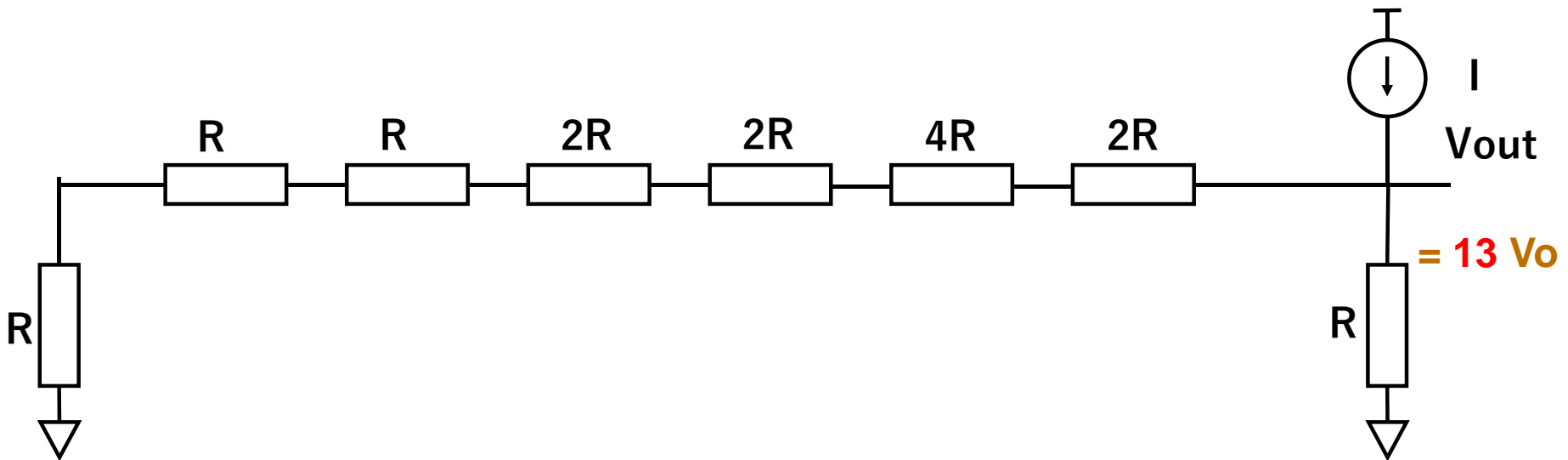
Prime numbers: 1, 2, 3, 5, 7, **11**, 13, 17, 19, 23, 29, .....



Here  $V_o = (1/14)RI$

# Proposed Prime Number DAC Operation (7)

Prime numbers: 1, 2, 3, 5, 7, 11, **13**, 17, 19, 23, 29, .....



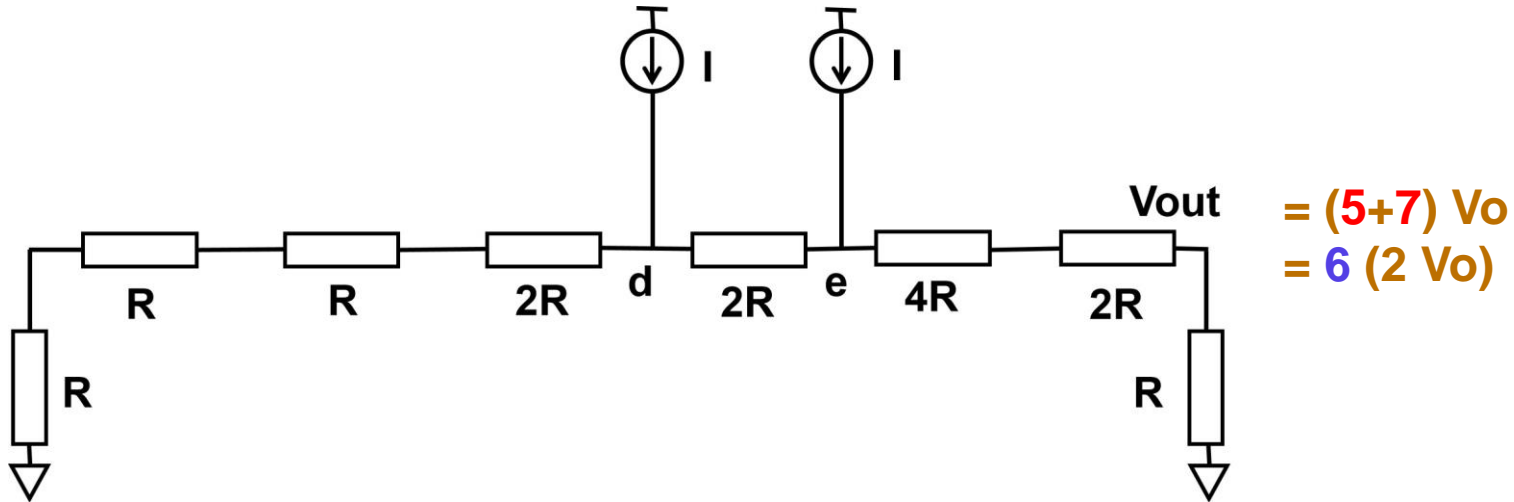
Here  $V_o = (1/14)RI$

# Digital Input with Addition of 2 Prime Numbers

	1	←	2:	2		16	←	32:	13+19
	2	←	4:	2+2		17	←	34:	17+17
	3	←	6:	3+3		18	←	36:	17+19
	4	←	8:	3+5		19	←	38:	19+19
	5	←	10:	3+7		20	←	40:	17+23
	6	←	12:	5+7		21	←	42:	19+23
Digital	7	←	14:	7+7	Digital	22	←	44:	13+31
Input	8	←	16:	5+11	Input	23	←	46:	23+23
	9	←	18:	7+11		24	←	48:	19+29
	10	←	20:	7+13		25	←	50:	19+31
	11	←	22:	11+11		26	←	52:	23+29
	12	←	24:	11+13		27	←	54:	23+31
	13	←	26:	13+13		28	←	56:	19+37
	14	←	28:	11+17		29	←	58:	29+29
	15	←	30:	13+17		30	←	60:	29+31

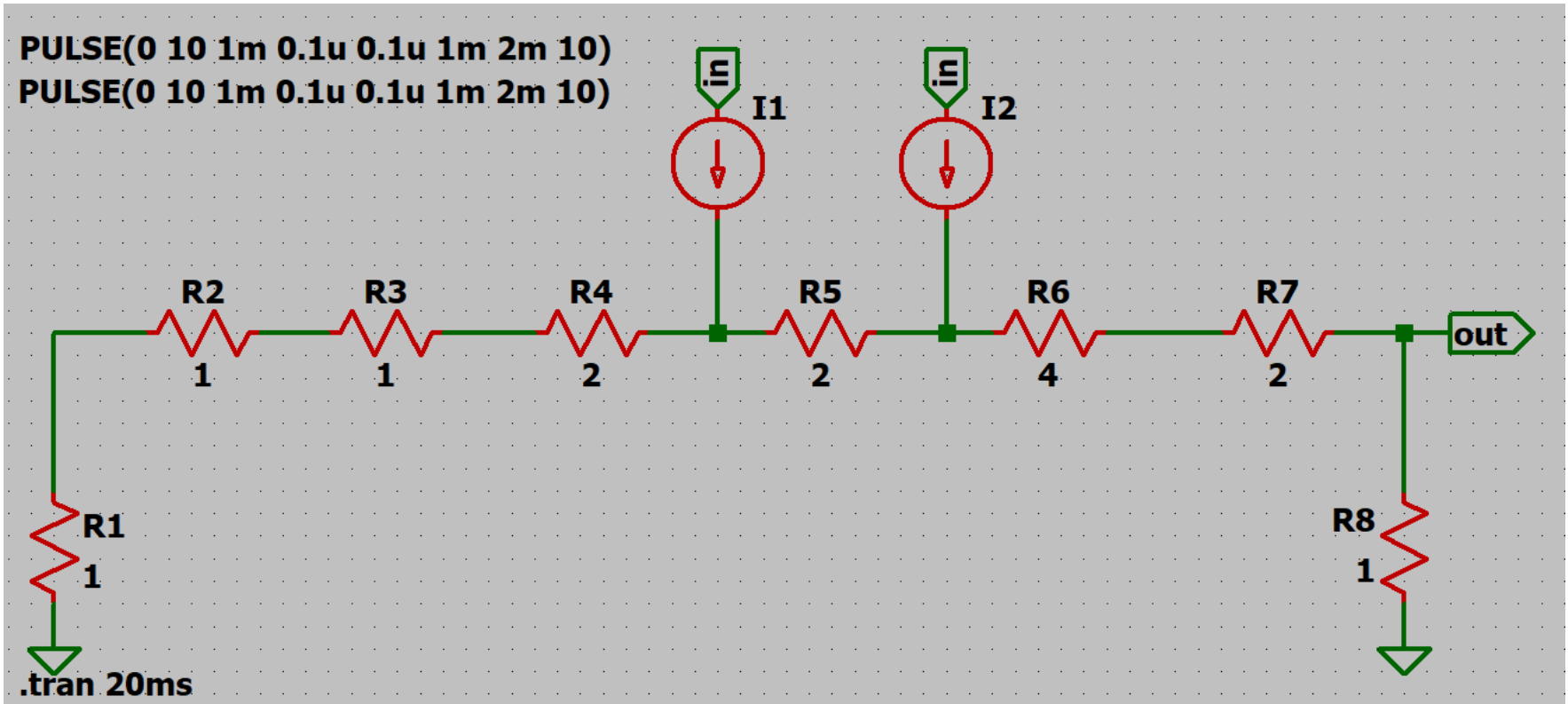
# Prime Number DAC Operation for digital input = 6

Prime numbers: 1, 2, 3, 5, 7, 11, 13, 17, 19, 23, 29, .....



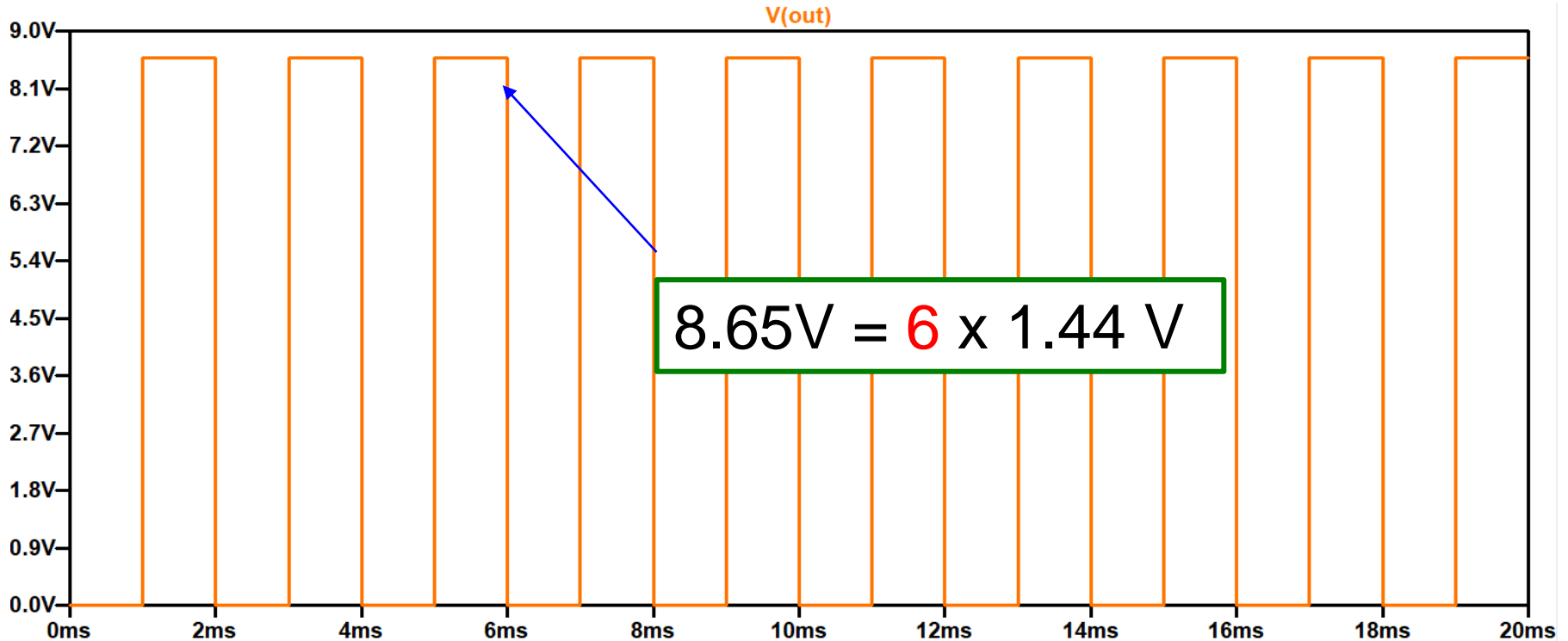
Here  $V_o = (1/14)RI$

# Prime number DAC operation for digital input = 6

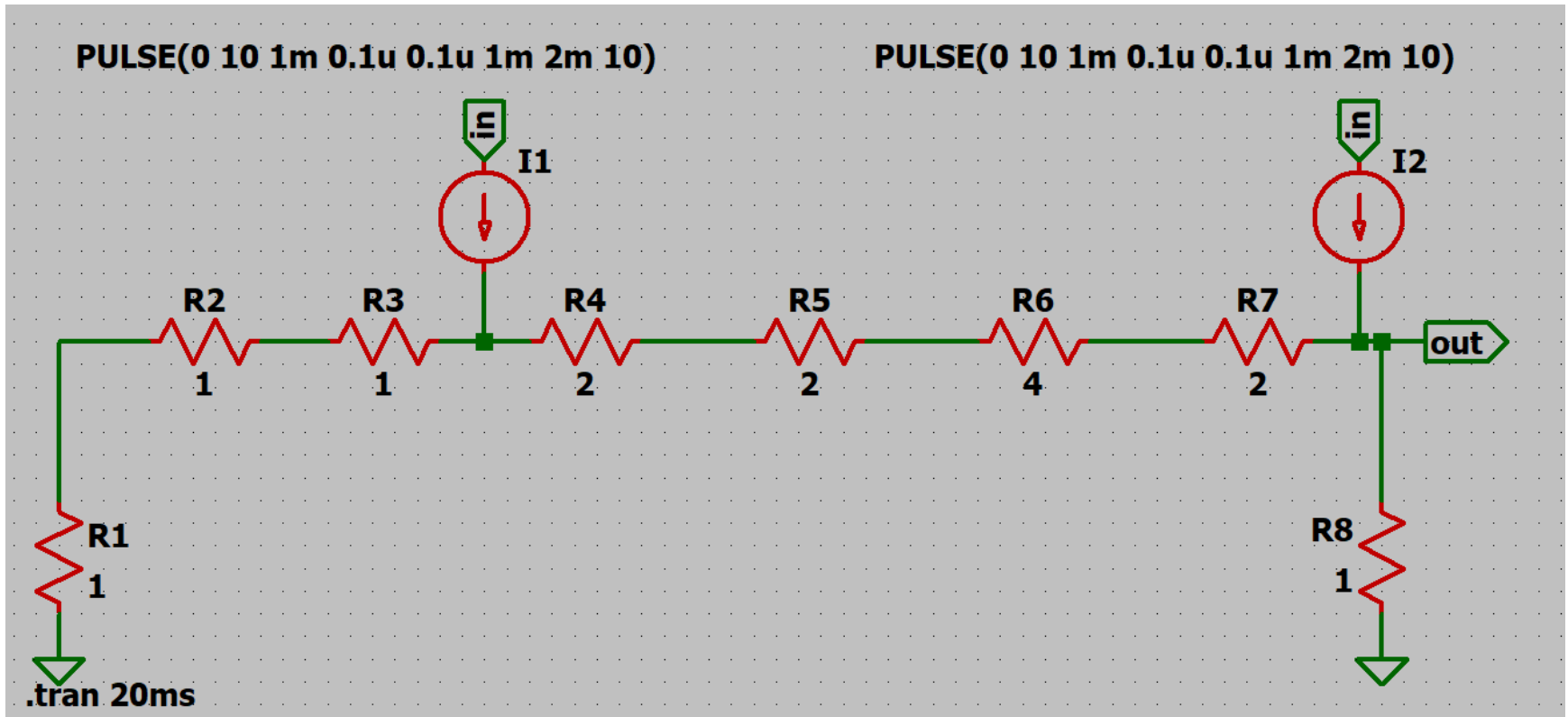




# Prime number DAC operation for digital input = 6

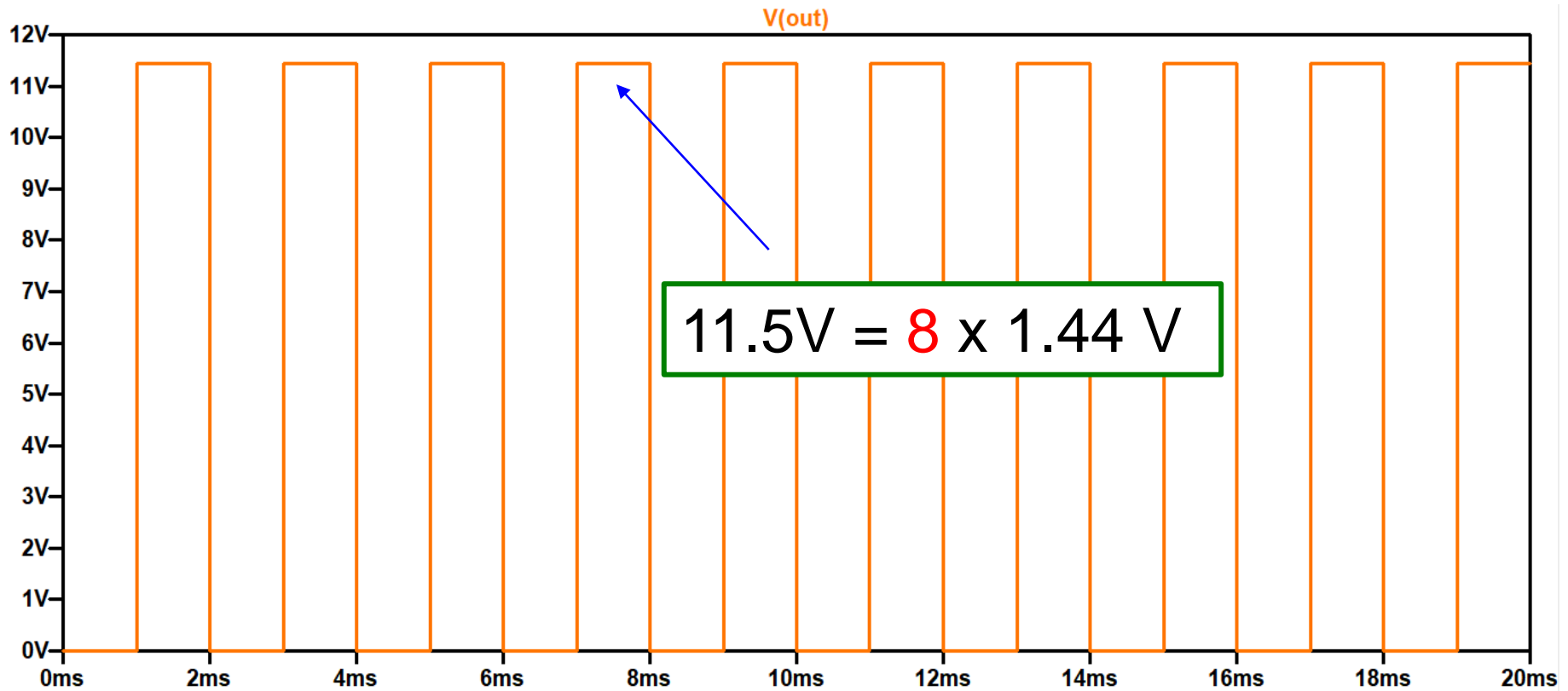


# In case that digital input is 8





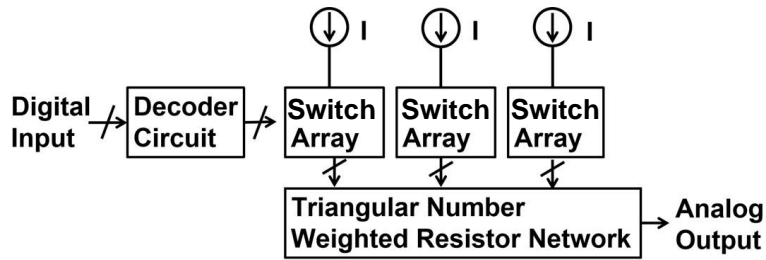
# In case that digital input is 8



# Outline

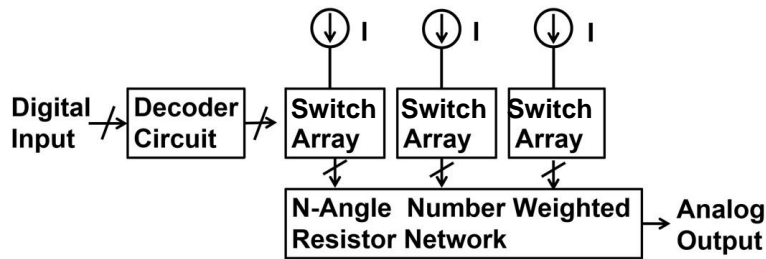
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# Summary



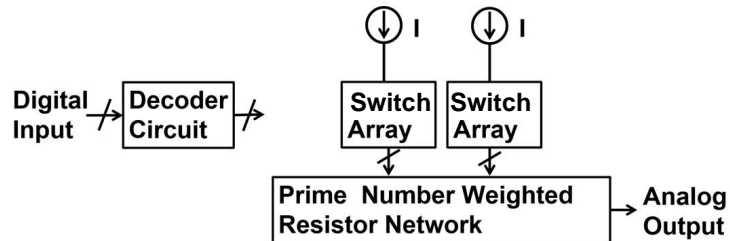
## Triangular number DAC

3 current sources  
 3 switch arrays



## Polygonal number DAC

N current sources  
 N switch arrays



## Prime number DAC

2 current sources  
 2 switch arrays

# Conclusion

- **Completely new DAC architectures based on integer theory**
- **Discussions on their pros and cons are left for the future work.**

# Acknowledgements

The authors thank Mr. Minh Tri Tran  
for having the simulation together.