Proactive Use of Finite Aperture Time in Sampling Circuit for Sensor Interface

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- Research Background and Objective
- Sampling Circuit for Signal Acquisition
- Low Pass Filtering Effect of Aperture Time
 in Sampling Circuit
- Charge Injection Reduction of Finite Aperture Time
 in Sampling Circuit
- Clock Feedthrough in Sampling Circuit
- Simulation Results of Pedestal Error in Sampling Circuit
- Summary

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Clarification of proactive use of finite aperture time in sampling circuit

- Low-pass filter chip area reduction
- Low frequency signal quality improvement



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Sampling Circuit for Waveform Acquisition



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Aperture Time in Sampling Circuit



- Finite aperture time:
- Integral time
 from hold start
 to switch opening end.

- Inverse of camera shutter speed
 Finite aperture time
- High speed shutter
 Clear fast moving object
 Slow speed shutter
 - Clear background



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Low Pass Filtering Effect of Aperture Time



Explicit transfer function

$$\frac{V_C}{V_{in}} = \frac{sinc(\omega\tau_2)}{sinc(\omega\tau_2) + j\omega\tau_1} \qquad \text{Here } \tau_1 = \text{RC}.$$

Finite aperture time au_2



Lowpass filter action

- Bad for high frequency signal sampling
- Good for low frequency signal sampling
 Lowpass filter simplification

$$\frac{V_C}{V_{in}} = \frac{sinc(\omega\tau_2)}{sinc(\omega\tau_2) + j\omega\tau_1}$$
$$(\tau_1 = RC , \tau_2 = \tau)$$

Transfer function in case of finite aperture time

$$\tau_{2} \rightarrow 0$$

$$\int sinc(\omega\tau_{2}) \rightarrow 1$$

$$\frac{V_{C}}{V_{in}} = \frac{1}{1 + j\omega\tau_{1}}$$

$$(\tau_{1} = RC)$$

$$V_{in} \circ V_{C}$$

Transfer function in case of zero aperture time

τ_1, τ_2 Effects to Bandwidth

Numerical calculation from the derived transfer function



 $\tau_1 (= R C)$: fixed τ_2 (aperture time) : varied

Bandwidth starts to decrease at $\tau_2 / \tau_1 = 1$

 τ_1 , τ_2 effects to bandwidth are comparable.

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Charge Injection



NMOS switch on-state: Channel charge in NMOS

NMOS turns off is Channel charge is dispelled into source and drain

Charge injection

Charge Injection and Pedestal Error

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turn off V_{G} • Charge injection Pedestal error ΔV_{out} +channe channel $V_{out} = V_{in} - \Delta V_{out}$ charge. charge V_{in} Pedestal error caused by - Charge injection Quick close Slow close VG VG - Clock feedthrough 3.3V 3.3V 0V Vout Vout Vin Vin • Long aperture time Vin-4Vb Vin-∆Va Pedestal error reduction Voltage drop by charge Voltage drop by charge injection and clock feedthrough injection and dock feedthrough time time

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Clock Feedthrough in Sampling Circuit



$$\Delta V = V_{CK} \frac{WC_{ov}}{WC_{ov} + C_H}$$

Clock feed-through: Cgs and Cgd signals is coupled to the source and drain to affect the signal. 17/25

ideal state: no parasitic capacitance

actual situation : parasitic capacitance





Simulation Result: Clock Feedthrough in Sampling Circuit



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Simulation Result: Pedestal Error in N-ch Sampling Circuit



- Both charge injection, clock feedthrough are included in simulation
- Aperture time increases \rightarrow Pedestal error Δ Vout decreases.
- For long aperture time, Vin decreases

Pedestal error Δ Vout decreases.

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Simulation Result: Pedestal Error in P-ch Sampling Circuit



- Both charge injection, clock feedthrough are included in simulation
- Aperture time increases

Pedestal error Δ Vout decreases.

• For long aperture time, Vdd - Vin decreases

Pedestal error Δ Vout decreases.

Pedestal Error Difference in N-ch, P-ch Sampling Circuits



Pedestal error difference between N-ch, P-ch sampling circuits.

Within ± 10% for the same Vineff

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- Finite aperture time in sampling circuit: Integral time from hold start to switch opening end.
- High frequency signal acquisition:

Performance deterioration

- Low frequency signal acquisition:

Proactive use for lowpass filtering Explicit transfer function

• Pedestal error:

Caused by charge injection and clock feedthrough.

• Pedestal error reduction for long aperture time.

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Thank you for your listening



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