2019 Taiwan and Japan Conference on Circuits and Systems

学会報告 conference report

Tochigi, Japan, August 19-21, 2019

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### **TJCAS Objectives**

- To exchange on the latest results related to circuits and systems

- To enhance and develop various levels of cooperation between Taiwan and Japan.

- All researchers including students are welcome.

### **TJCAS History**

Until now, the conference of the TJCAS was held successfully for 5 years. Starting from 2015, the TJCAS has held in Japan and Taiwan every year.

2015 1st Shikoku U., Japan 2016 2nd National Chen Kung U., Taiwan 2017 3rd Okayama Convention Center, Japan 2018 4th National Chung Hsing U., Taiwan 2019 5th Nikko, Japan

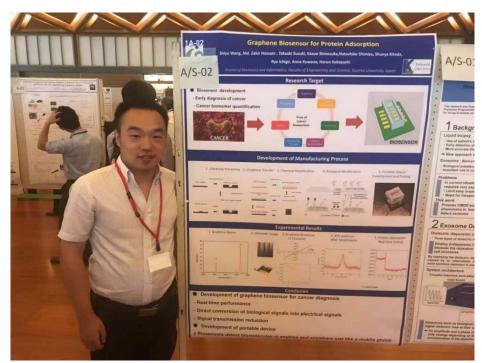
### **Plenary Talks**

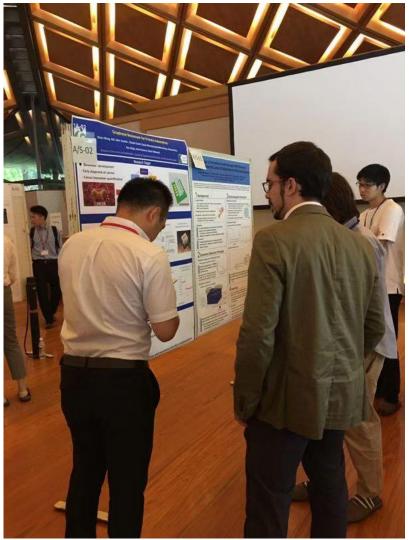
In plenary talks, there are 3 professors have been invited to do the keynote.

There are Prof. Hao-Chiao Hong (Electrical and Computer Engineering, National Chiao Tung University, Hsinchu, Taiwan) Title: Design of BIST Delta-Sigma ADC. Dr. Koji Asami (Advantest Corp.) Title: Signal Processing Techniques for Testing RF, Mixed-signal and Analog ICs. And Mr. Atsushi Motozawa (Renesas Electronics) Title: Phase-Locked Loop Circuit Design From Basics to State-of-The-Art and Industrial Practices.

### Report

As a participant, I published a report on biosensors, and discussed circuits and systems related issues with the experts. Through exchanges with Taiwanese scholars, I found that universities in Taiwan always work with companies including TSMC, which will make their design activate. And the price is cheaper. For the 180nm process, if you work with TSMC, these circuits may be produced for only 30,000 yen.





# **Social Events**

The welcome dinner is very delicious. At the end of the dinner we sang together and relaxed.



I also visited Edo Village and experienced the Edo culture at Afternoon, Aug. 21th (Wed). Edo Wonderland (Nikko Edomura「日光江戸村」)



#### Acknowledgment

Thanks to the members of Kobayashi lab for their hard work.

# Summary

At this conference, I not only discussed the latest research results of circuits and systems with experts but also participated in the planning work of the conference as a staff. This is very meaningful for me.