

# IEEE Asian Solid-State Circuits Conference

## International SoC Design Conference

2011 年 11 月、韓国 濟州島

● 下記の 3 件の発表

- [1] Kiichi Niitsu, Masato Sakurai, Naohiro Harigai, Takahiro J. Yamaguchi, Haruo Kobayashi, "An On-Chip Timing Jitter Measurement Circuit Using a Self-Referenced Clock and a Cascaded Time Difference Amplifier with Duty-Cycle Compensation," IEEE Asian Solid-State Circuits Conference (A-SSCC 2011), Jeju, Korea (Nov. 2011).
- [2] Masato Sakurai, Kiichi Niitsu, Naohiro Harigai, Daiki Hirabayashi, Daiki Oki, Takahiro J. Yamaguchi, and Haruo Kobayashi, "Analysis of Jitter Accumulation in Interleaved Phase Frequency Detectors for High-Accuracy On-Chip Jitter Measurements," International SoC Design Conference (ISOCC2011), Jeju Korea (Nov. 2011)
- [3] Masato Sakurai, Kiichi Niitsu, Naohiro Harigai, Takahiro J. Yamaguchi, Haruo Kobayashi, "A Reference-Clock-Free On-Chip Timing Jitter Measurement Circuit Using a Cascaded Time Difference Amplifier with Duty-Cycle Compensation in 65nm CMOS," International SoC Design Conference (ISOCC2011), Chip Design Contest, Jeju Korea (Nov. 2011)

● 研究室から 15 名が参加。

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