

IEEJ International Analog VLSI Workshop (2011年11月, バリ島)での 学生の研究発表(2件)

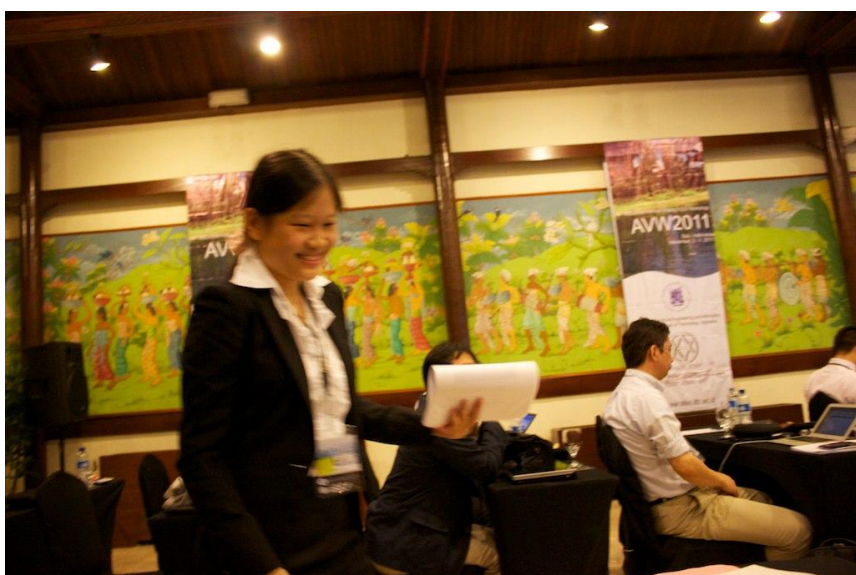
発表論文

Jiani Ye, Zachary Nosker, Kazuyuki Wakabayashi, Takuya Yagi, Nobukazu Takai, Kiichi Niitsu, Keisuke Kato, Takao Ootsuki, Osamu Yamamoto, Isao Akiyama, Haruo Kobayashi,
"Architecture of High-Efficiency Digitally-Controlled Class-E Power Amplifier,"
IEEJ International Analog VLSI Workshop, Bali, Indonesia (Nov. 2011).

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Jiani Ye received her B.S. degree in 2005 from Jinan University (Guangzhou, China). After working for FOXCONN as an electronics engineer, she came to Japan for graduate school and received an M.S. degree in 2011 from Gunma University (Kiryu, Japan). Jiani currently works at the Sanyo Research Lab in Gifu, Japan. Her research interests include analog electronics and wireless communication systems.





発表論文

Zachary Nosker, Yasunori Kobori, Haruo Kobayashi, Kiichi Niitsu, Nobukazu Takai, Takeshi Oomori, Takahiro Odaguchi, Isao Nakanishi, Kenji Nemoto, Jun-ichi Matsuda,
"A Small, Low Power Boost Regulator Optimized for Energy Harvesting Applications,"
IEEJ International Analog VLSI Workshop, Bali, Indonesia (Nov. 2011).

発表者 Zachary NOSKER

Zachary Nosker received his BSEE from San Francisco State University in 2005. He has over 5 years of Silicon Valley work experience, and has worked for National Semiconductor Corporation (now Texas Instruments) and Apple Inc. After receiving a full-ride graduate student scholarship from the Japanese ministry of education (MEXT), he came to Japan and is now pursuing a PhD degree at Gunma University in Kiryu, Gunma, studying under professor Haruo Kobayashi. Zachary's research interests include analog electronics, power supplies for portable (battery-powered) applications, and energy harvesting power electronics.



