ICSICT 2016 Advance Program

PID	SID	Name	Session Name	Date	Start time	Room	Minute	Title
S0542	S05-3	Koyo Asaishi	Analog Circuits Ⅰ	Oct. 26	14:30	3B	15	Hysteretic Controlled Buck Converter With Switching Frequency Insensitive to Input/Output Voltage Ratio
S0324	S05-5	Jianlong Wang	Analog Circuits Ⅰ	Oct. 26	15:00	3B	15	Analysis and Design of Operational Amplifier Stability Based on Routh-Hurwitz Method
S0334	S05-6	Mayu Hirano	Analog Circuits Ⅰ	Oct. 26	15:15	3B	15	Simple Reference Current Source Insensitive to Power Supply Voltage Variation Improved Minoru Nagata Current Source
S0335	S06-2	Satoshi Yoshizawa	Analog Circuits Ⅱ	Oct. 26	16:15	3B	15	Comparator Circuit Automation by Combination of Game Tree Search and Partial Optimization
S0346	S06-3	Gopal Adhikari	Analog Circuits Ⅱ	Oct. 26	16:30	3B	15	Study of Gray Code Input DAC Using MOSFETs for Glitch Reduction
S0398	S06-5	Yasunori Kobori	Analog Circuits Ⅱ	Oct. 26	17:00	3B	15	Single-Inductor Dual-Output Soft-Switching Converter
S0449	S06-7	Nobukazu Tsukiji	Analog Circuits Ⅱ	Oct. 26	17:30	3B	15	Derivation of loop gain from output impedances in DC-DC buck converter
S0593	S12-4	You Yin	Memory Ⅱ	Oct. 26	17:15	7	30	Advanced Nanofabrication and its Application to Nano Phase-Change Memory for Reducing Writing Current
S0470	S22-4	Keita Kurihara	Ultra High Speed Transistors	Oct. 27	16:45	3A	15	Study On Electron Mobility Model for AlN/GaN MIS-HEMTs with Embedded Source Field-Plate Structures
S0327	S25-3	Jun-ya Kojima	Power Devices and Reliability	Oct. 27	16:30	3B	15	Optimization and Analysis of High Reliability 30-50V Dual RESURF LDMOS
S0340	S62-1	Haruo Kobayashi	Analog Circuits Ⅲ	Oct. 28	10:15	VIP	30	Analog / Mixed-Signal Circuit Design Based on Mathematics
S0380	S63-2	Yoshiro Tamura	Analog Circuits IV	Oct.28	14:00	VIP	15	RC Polyphase Filter as Complex Analog Hibert Filter
S0487	S63-5	Kazuto Okochi	Analog Circuits IV	Oct. 28	14:45	VIP	15	Automatic Design of Operational Amplifier by Combination Method of Function Block
S0373	S47-2	Tomonori Yanagida	Signal Processing	Oct. 28	10:45	3B	15	High-Frequency Low-Distortion One-Tone and Two-Tone Signal Generation Using Arbitrary Waveform Generator
S0389	S47-3	Tomonori Yanagida	Signal Processing	Oct.28	11:00	3B	15	Sine Signal Generation with Specified Multiple Harmonics Suppression
S0318	S59-3	Jun-ya Kojima	High-Performance Data Converters Ⅱ	Oct. 28	11:00	8	15	Limit Cycle Suppression Technique Using Digital Dither in Delta Sigma DA Modulator